```
X000
Y000
X012
Y000
X001
Y001
X013
Y001
X002
Y002
X014
Y002
X003
Y003
Y003
Y012
M10
   0 LD
\begin{array}{c} 1 \\ 2 \\ 3 \\ 4 \\ 5 \\ 6 \\ 7 \\ 8 \\ 9 \\ 10 \\ 11 \\ 12 \\ 13 \\ 14 \\ 15 \\ 16 \\ 17 \\ \end{array}
             OR
             ANI
OUT
             LD
             OR
             ANI
OUT
LD
OR
ANI
OUT
LD
OR
ANI
OUT
             LD
OR
             ANI
OUT
18
19
20
21
22
23
24
25
26
27
28
29
30
31
                                              Y013
                                             M10
             LD
                                              Y013
             OR
                                             M11
             ANI
OUT
                                              Y012
                                             M11
                                             X012
Y004
             LD
             LDI
             ANI
AND
                                              Y000
                                             X013
             ORB
                                             M10
             LD
             AND
ORB
                                             X013
                                            Y004
Y000
Y001
32
33
34
35
36
37
38
39
40
             LDI
ANI
ANI
ANI
AND
ORB
                                             Y005
                                             Y006
                                             X014
                                             M10
             LD
             AND
ORB
LD
OR
                                             X014
\begin{array}{c} 41\\ 42\\ 43\\ 44\\ 45\\ 46\\ 47\\ 48\\ 49\\ 50\\ 51\\ 52\\ 53\\ 54\\ \end{array}
                                            Y003
Y011
             ANB
OR
ANI
OUT
LD
OR
OR
                                            MO
XO15
MO
                                            Y002
Y007
Y010
                                             X012
Y004
             LD
             LDI
             ANI
AND
                                            Y000
X013
55
56
             ORB
                                             M10
             LD
57
58
59
60
             AND
ORB
                                             X013
             ANB
OR
                                            M1
X014
M1
Y001
             ANI
OUT
61
62
63
64
             LD
OR
OR
AND
                                              Y005
                                              Y006
65
66
67
68
69
70
71
72
73
74
75
76
77
78
80
81
                                            X012
M2
X013
M2
X013
X014
X015
Y000
Y004
             OR
ANI
OUT
             LD
OR
OR
LD
            OR
ANB
OR
ANI
OUT
LD
OR
OR
                                            M3
X012
M3
Y001
Y005
Y006
```

```
82
      LD
                   X014
 83
                   X015
       OR
 84
       ANB
 85
       OR
                   X013
M4
      ANI
OUT
 86
 87
                   Y002
 88
       LD
 89
90
       OR
                   Y007
                   Y010
X015
      OR
AND
OR
ANI
OUT
LD
 91
 92
93
94
                   M5
X014
                   M5
M0
 95
      OR
OR
 96
                   M1
 97
                   M2
      AND
ANI
                   X017
Y013
 98
 99
      ANI
OUT
100
                   M6
101
                   Y012
102
       LD
                   M3
103
       OR
                   M4
104
       OR
                   M5
105
       AND
                   X017
      ANI
ANI
106
                    Y012
107
                    M6
                    Y013
108
       OUT
109
       LD
                    X000
110
       OR
                    X004
111
       AND
                    X012
       LD
                    X001
113
       OR
                    X005
114
       OR
                    X006
115
       AND
                   X013
116
       ORB
                   X002
117
       LD
118
       OR
                    X007
       OR
                    X010
119
120
121
      AND
ORB
                   X014
                   X003
X011
X015
122
123
124
125
126
127
128
129
131
      LD
OR
AND
ORB
OR
ANI
OUT
LDF
                   Y014
X016
Y014
Y001
       ORF
                    Y006
133
135
       ORF
                    Y005
       ORF
                    Y002
137
       ORF
                    Y010
139
       ORF
                    Y007
141
       ORF
                    Y004
143
       ORF
                    Y000
145
       ORF
                    Y003
147
       ORF
                    Y011
149
       OR
                    Y014
150
       MPS
                    X016
151
       ANI
152
       OUT
                    Y014
153
       MPP
154
       SET
                    M6
                   X016
155
       LD
       OUT
                                K30
156
                    TO
159
       LD
                   T0
160
       OR
                   Y015
      ANI
MPS
161
                   X017
162
                   Y014
       ANI
163
164
       OUT
                   Y015
       MPP
165
      RST
LD
                   M6
166
                   M10
167
168
       MPS
                   X013
Y007
Y010
169
170
171
172
173
174
175
      AND
ANI
ANI
ANI
ANI
ANI
ANI
                    Y002
                   Y011
Y003
```

Y006

150	ANDE	770
176 178	ANDF OUT	Y015 M12
179	MPP	MIZ
180	AND	X014
181	ANI	Y011
182	ANI	Y003
183 184	ANI ANDF	Y010 Y015
186	OUT	M13
187	LD	M11
188	MPS	VO12
189 190	AND ANI	X013 Y004
191	ANI	Y000
192	ANI	Y005
193	ANDF	Y015
195 196	OUT MPP	M14
197	AND	X014
198	ANI	Y005
199	ANI	Y006
200 201	ANI ANI	Y001 Y004
	ANI	Y000
203	ANI	Y007
204	ANDF	Y015
206 207	OUT LD	M15 X004
208	OR	Y004
209	ANI	X012
210	OUT	Y004
211 212	LD OR	X006 Y006
213	LD	M11
214	ANI	M14
215	ORI	X013
216 217	ANB OUT	Y006
218	LD	X005
219	OR	Y005
220 221	LD	M10
221 222	ANI ORI	M12 X013
223	ANB	V019
224	OUT	Y005
225	LD	X010
226 227	OR LD	Y010 M11
228	ANI	M15
229	ORI	X014
230 231	ANB	11010
231	OUT LD	Y010 X007
233	OR	Y007
234	LD	M10
235	ANI	M13
236 237	OR I ANB	X014
238	OUT	Y007
239	LD	X011
240	OR	Y011
241	ANI	X015
242 243	OUT END	Y011
210	LIID	