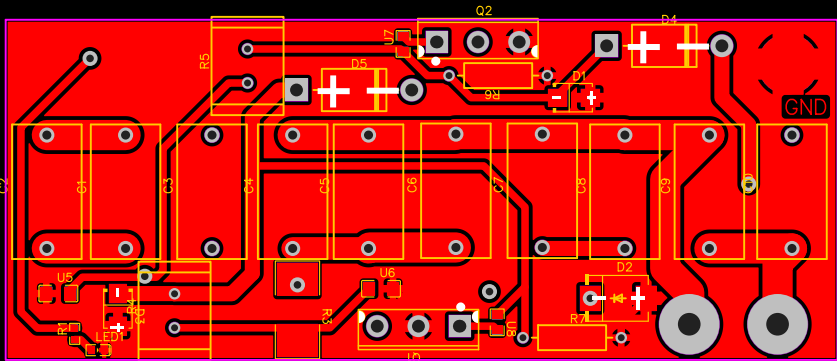
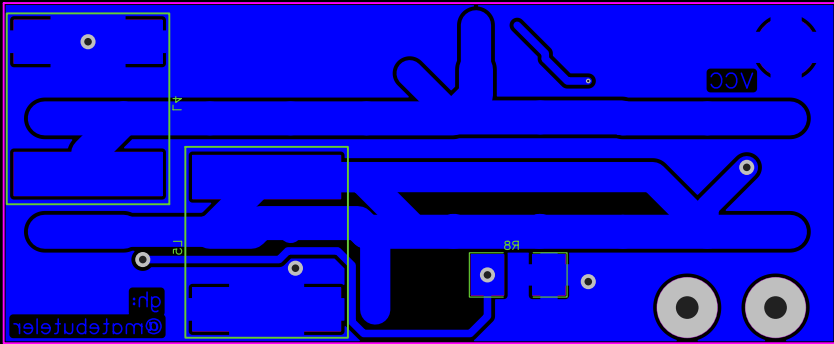


TOP LAYER (GND)



BOTTOM LAYER (VCC)



TITLE: PCB_indheater_2		REV: 1.0
EasyEDA	Company: GitHub: @matebuteler	Sheet:
	Date: 2024-08-24	Drawn By: mateojosebuteler