Operateur Name: Mateus Vicente

Supervisor Name :

Phone: 075 411 82 61

Office: DPNC





100μPET test-chip

Description

Test-chip for the $100\mu PET$ project.

Neural Microsystems Platform's machine needed Plasma etcher (PINK), Sputter (AC 450), Semi-Automatic Coater, Mask exposure (MLA), developer, Dry etcher (Corial) Substrate Type 4-inch Si wafer Process Outline Color chart Si Ti Pt PR Al SiO₂

01	Surface Preparation Machine: Plasma etcher (PINK) Recipe: 2min_High	
02	Metal deposition 10 nm Ti + 100 nm Pt Machine: Sputter (AC 450) Recipe:	
03	Dehydration & PR Coating Hot plate: 1min @ 110°C Machine: Semi_auto_coater Recipe: Baking:	

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Office : DPNC

E-mail: mateus.vicente barretop into @unige.ch



04 Exposure window:	
Dry Etching Machine: Corial Recipe: Time:	
PR Stripping Ultrasonic bath & Acetone	
Dehydration & PR Coating Hot plate: 1min @ 110°C Machine: Semi_auto_coater Recipe: Baking:	
Mask Exposure + Develop Machine: MLA + developer Layer to expose: • Layer #134 Invert Mask polarity: Exposure window: See #04 Alignment Cross: X	

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09	Metal deposition 10 nm Ti + 300 nm Al Machine: Sputter (AC 450) Recipe:	
10	Lift off Ultrasonic bath & Acetone	
11	Dehydration <u>Machine</u> : Hot plate <u>Recipe:</u> 110°C – 1 min	
12	Passivation coating 300 nm SiO ₂ Machine: Sputter (AC 450) Recipe: Baking:	
13	Dehydration & PR Coating Hot plate: 1min @ 110°C Machine: Semi_auto_coater Recipe: Baking:	
14	Mask Exposure + Develop Machine: MLA + developer Layer to expose: • Layer #9 Invert Mask polarity: Exposure window: See #04 Alignment Cross: See #08 Dose: Development:	

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15	Dry Etching Machine: Corial Recipe: Time:	
16	Stripping Ultrasonic bath & Acetone	
17	Annealing 1 hour @ 900°C	
18	Dicing	