Node Name	Customi ze	n	Locatio n	I/O Bank	VREF Group	Fitter Location		Reserve d	Current Strengt	Slew Rate	Differen tial Pair
	Columns.						d		h		
a[7]		Input	PIN_Y23	5	B5_N2	PIN_N8	2.5 V (def ault)		8mA (def ault)		
a[6]		Input	PIN_Y24	5	B5_N2	PIN_L4	2.5 V (def ault)		8mA (def ault)		
a[5]		Input	PIN_AA2 2	5	B5_N2	PIN_R7	2.5 V (default)		8mA (def ault)		
a[4]		Input	PIN_AA2 3	5	B5_N2	PIN_K7	2.5 V (def ault)		8mA (def ault)		
a[3]		Input	PIN_AA2 4	5	B5_N2	PIN_L8	2.5 V (def ault)		8mA (def ault)		
a[2]		Input	PIN_AB2 3	5	B5_N2	PIN_R1	2.5 V (def ault)		8mA (def ault)		
a[1]		Input	PIN_AB2 4	5	B5_N2	PIN_L6	2.5 V (def ault)		8mA (def ault)		
a[0]		Input	PIN_AC2 4	5	B5_N2	PIN_P1	2.5 V (def ault)		8mA (def ault)		
b[7]		Input	PIN_AB2 6	5	B5_N1	PIN_L7	2.5 V (def ault)		8mA (def ault)		
b[6]		Input	PIN_AD2 6	5	B5_N2	PIN_P2	2.5 V (def ault)		8mA (def ault)		
b[5]		Input	PIN_AC2 6	5	B5_N2	PIN_M5	2.5 V (def ault)		8mA (def ault)		
b[4]		Input	PIN_AB2 7	5	B5_N1	PIN_J7	2.5 V (def ault)		8mA (def ault)		
b[3]		Input	PIN_AD2 7	5	B5_N2	PIN_M3	2.5 V (def ault)		8mA (def ault)		
b[2]		Input	PIN_AC2 7	5	B5_N2	PIN_J6	2.5 V (def ault)		8mA (def ault)		
b[1]		Input	PIN_AC2 8	5	B5_N2	PIN_L3	2.5 V (def ault)		8mA (def ault)		
b[0]		Input	PIN_AB2 8	5	B5_N1	PIN_R2	2.5 V (def ault)		8mA (def ault)		
carryBorro w		Output	PIN_F19	7	B7_N0	PIN_L2	2.5 V (def ault)			2 (default)	
mode[1]		Input	PIN_AB2 5	5	B5_N1	PIN_K8	2.5 V (def ault)		8mA (def ault)		
mode[0]		Input	PIN_AC2 5	5	B5_N2	PIN_U3	2.5 V (def ault)		8mA (def ault)		
result[7]		Output	PIN_G21	7	B7_N1	PIN_L1	2.5 V (def ault)			2 (default)	
result[6]		Output	PIN_G22	7	B7_N2	PIN_J5	2.5 V (def ault)			2 (default)	
result[5]		Output	PIN_G20	7	B7_N1	PIN_M1	2.5 V (def ault)		8mA (def ault)	2 (default)	
result[4]		Output	PIN_H21	7	B7_N2	PIN_M7	2.5 V (def ault)			2 (default)	
result[3]		Output	PIN_E24	7	B7_N1	PIN_N3	2.5 V (def ault)			2 (default)	
result[2]		Output	PIN_E25	7	B7_N1	PIN_M8	2.5 V (def ault)			2 (default)	
result[1]		Output	PIN_E22	7	B7_N0	PIN_N4	2.5 V (def ault)			2 (default)	
result[0]		Output	PIN_E21	7	B7_N0	PIN_M2	2.5 V (def ault)			2 (default)	
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