

Yasboot memory layout

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1 Introduction

This document describes Yasboot memory layout. Be careful with using it, each MCU or board may have slightly different exact mapping. For details about specific MCU, please check dedicated document.

2 Memory Map

Yasboot uses MBR layout scheme where it is possible. Yasboot second stage is placed just after MBR block. Size of second stage is highly configurable via:

```
$ make menuconfig
```

Generic memory layout:

Section Name	Section Address	Section Size
MBR	0x00000000	0x200
Yaspem 2-stage	0x00000200	0x800...0x8000
Optional BIOS	0x00000200 + 2stage size	0x4000
1-st partition	user defined	user defined
...
n-th partition*	user defined	user defined

* where **n** maximum is 4 according to MBR scheme.

Add pictures about Yasboot configure options