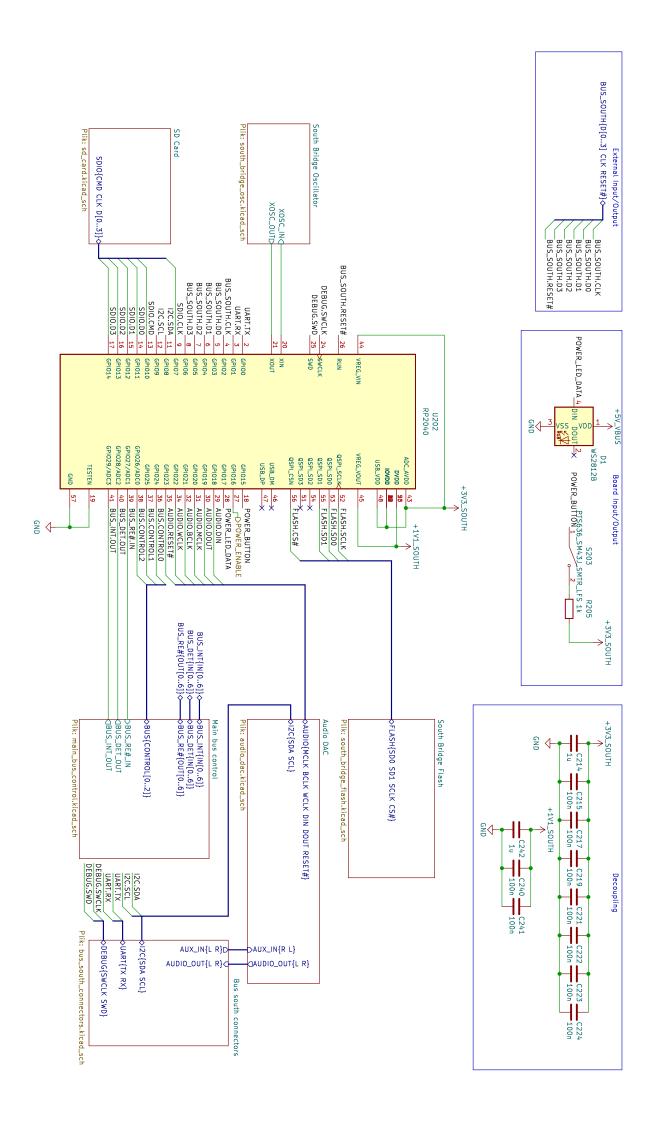
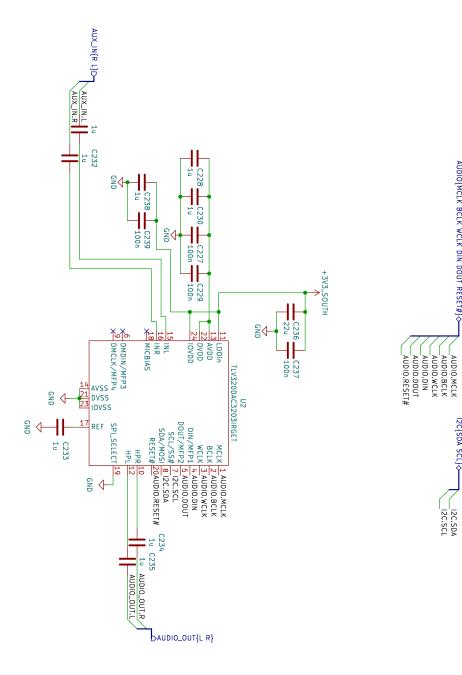


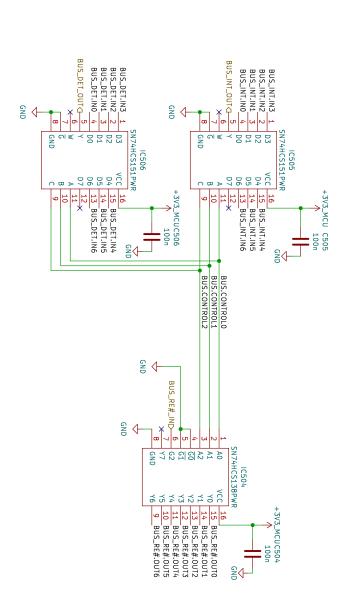
C322 22u

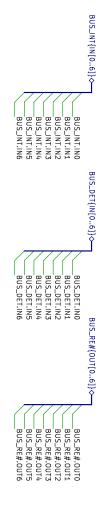




AUDIO{MCLK BCLK WCLK DIN DOUT RESET#}

I2C{SDA SCL}♦—





BUS{CONTROL[0..2]}♦—

BUS.CONTROLO
BUS.CONTROL1
BUS.CONTROL2

