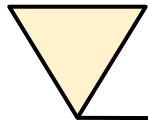
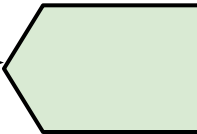


Signal Source
(Antenna)



ADC



Signal Processing
(FPGA/ASIC/CPU)



I

Q

