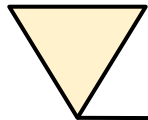
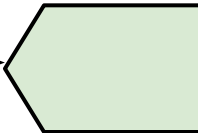


Signal Source
(Antenna)



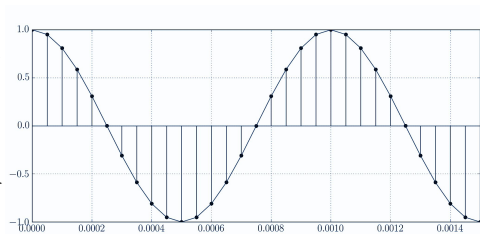
ADC



Signal Processing
(FPGA/ASIC/CPU)



I in-phase (ϕ)



Q quadrature-phase ($\phi-90$)

