

Need formal lecture on mesh currents +

Thermin + Norton equivalent  
 KVL KCL  
 Post some practice problems

Lecture 3: mesh currents (briefly)

- Thermin + Norton equivalent ckt
- Source Transformations
- Possibly start with Phasors

NOTE:

due to bank holiday:  
 1) office hours next week 8-9am

moved to ~~8-9am~~ Tues. morn

2) homeworks due → used 1/31/05

3) labs domains - brace to

do the problems - laboratory  
 first 4 hrs  
 pay attention to lab!

Lab report: → "Shall have"

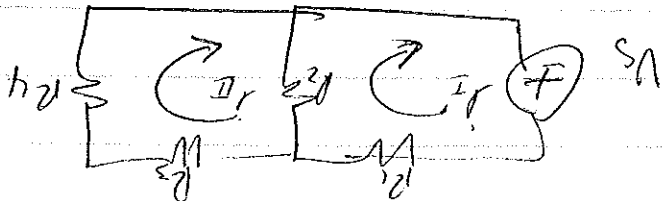
→ answer all m. lab 7's, in lab 7's  
 + post lab 7's

count the 2 and put answer...  
 1/ lab group.

## Review mesh currents:

meshes define closed loops  
you will follow around etc  
to reconfirm KVL

→ mesh currents will be your  
variables, and you will multiply  
them by each resistor (impedance)  
to determine voltage.  
→ mesh current will define  
polarity around the circuit



- ① identify meshes (closed loops)
- ② Keep ALL mesh currents (clockwise)
- ③ KVL each mesh + current direction defines polarity
- ④ solve

$$\text{mesh I: } -V_s + i_I R_1 + R_2 (i_I - i_{II}) = 0$$

$$\text{mesh II: } R_2 (i_{II} - i_I) + R_3 i_{II} + R_4 i_{II} = 0$$

if direction is clockwise  
→ you have a current source, don't have voltage  
so, pick a different loop!

what did we get last class?

$$\begin{aligned} \text{Loop 1: } 10 - 20I_1 + 50I_2 + 20I_3 &= 0 \\ \text{Loop 2: } 10 - 20I_2 + 50I_1 + 20I_3 &= 0 \end{aligned}$$

$$-10 + (I_1 - I_2)20 + 20I_3 = 0$$

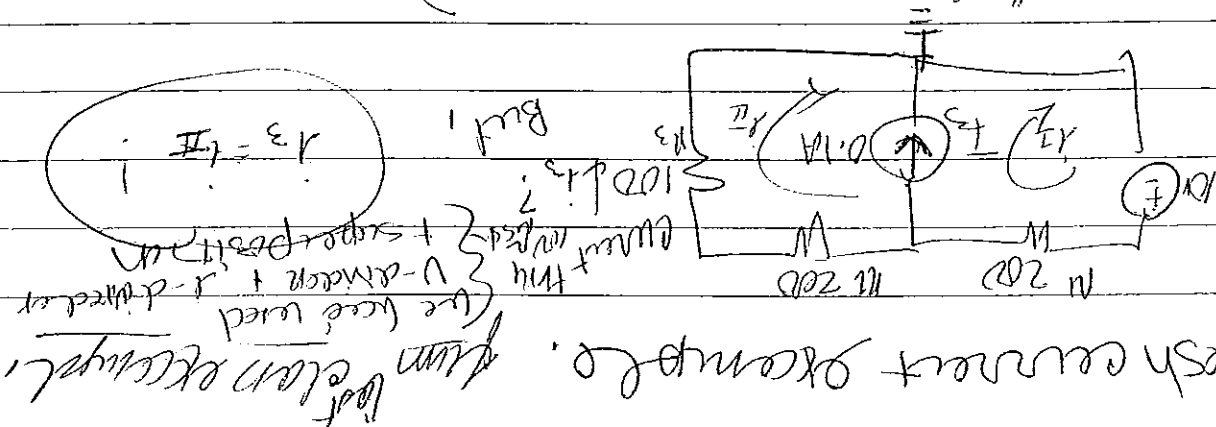
~~$$10 - 20I_1 + 50I_2 + 20I_3 = 0$$~~

$$I_3 = I_1 - I_2$$

constraint

$$-10 + I_1 20 + 1I_2 20 + 1I_3 10 = 0$$

"Supermesh" (skip the current source)



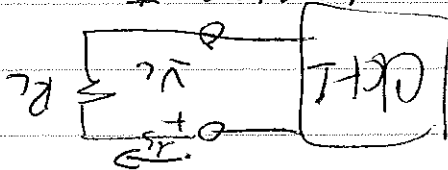
⑩

Another Tool:

Source Transformations:

you might have network  $\Rightarrow$  nodal analysis  
 cts are  $\Rightarrow$  series of only  
 1 type of source (either v or i)  
 rather than 2 in one ckt

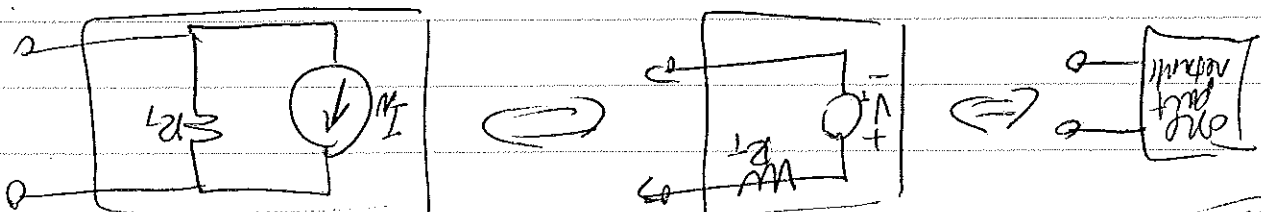
A circuit is defined by its  $i-v$  relations so



you can ~~represent~~ any one-port network by a voltage source in series with a resistor, or a current source in // w/ a resistor

$\Rightarrow$  either one is equivalent to the original ckt as long as the  $v-i$  at the port are the same as the original.

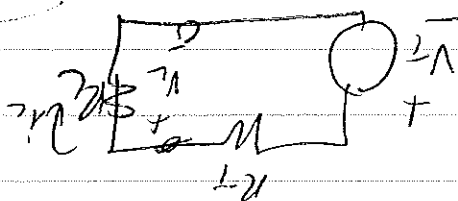
Ckt 1:



$$V_N = R_1 I_N$$

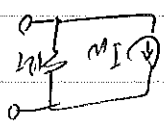
Why do we want? (11)

~~Another example:~~



find  $V_L + I_L$   

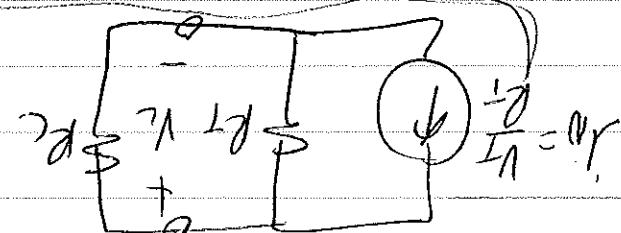
$$I_L = \frac{R_L}{R_T + R_L} V_T$$



using  $V_T = I_N R_T$   

$$I_N = \frac{V_T}{R_T}$$

Source x gain:



$I_L \Rightarrow$  current divider:  

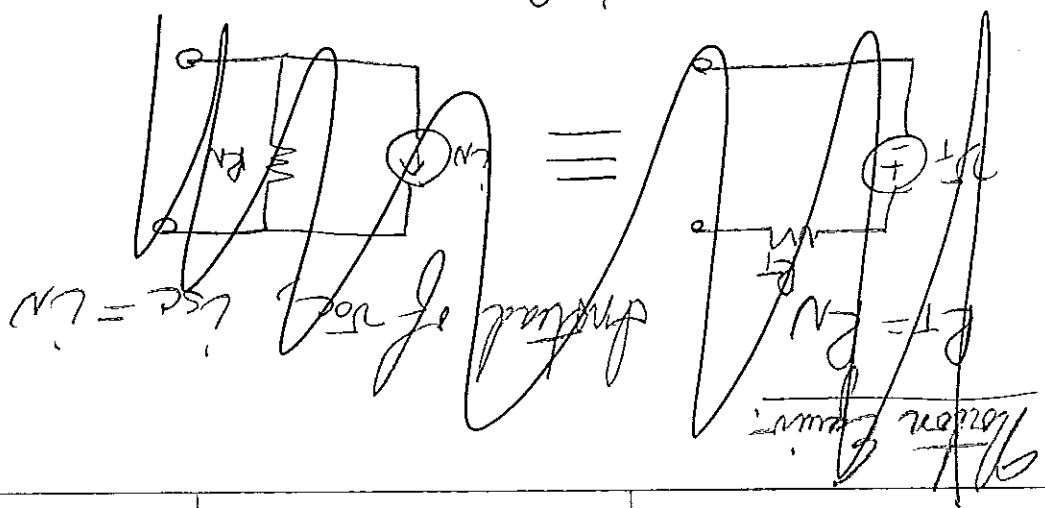
$$I_L = I_N \frac{R_T}{R_T + R_L} = \frac{V_T}{R_T} \frac{R_T}{R_T + R_L} = \frac{V_T}{R_T + R_L}$$

$$V_L = I_L R_L = V_T \frac{R_L}{R_T + R_L}$$

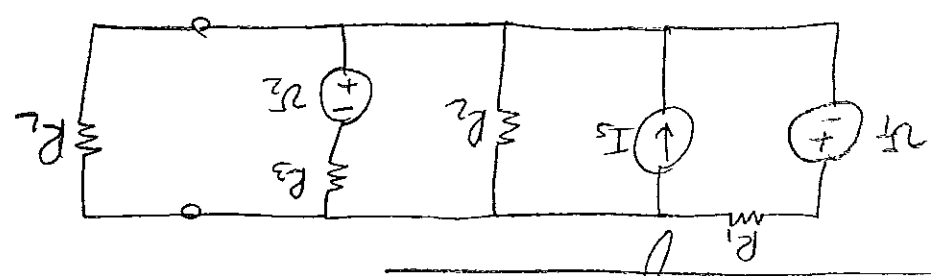
Same  $I_L + V_L$  to load either way you represent it

WAP

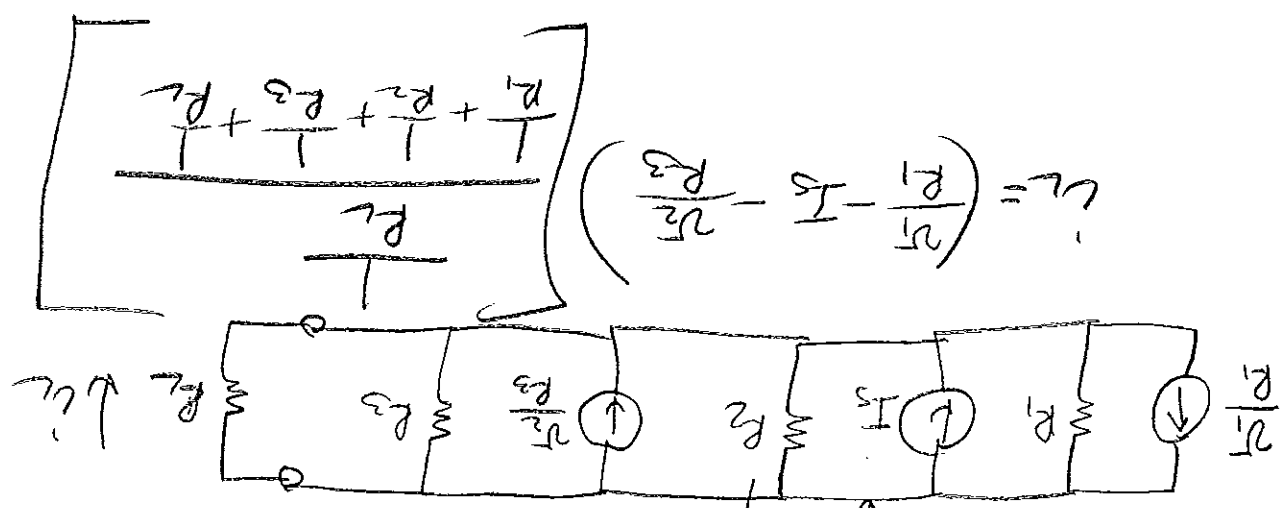
$$V_T = I_N R_T$$



Source Transformation



"sample" current divider



$$I_L = \left( \frac{V_T}{R_1 + R_2 + R_3} \right) R_3$$

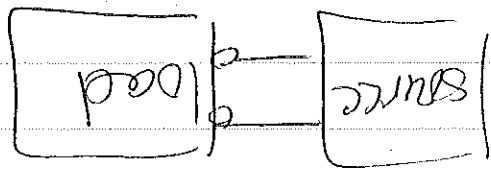
①

Today  $\Rightarrow$  find out the "source" & "load"  
 & simplify it  $\Rightarrow$  use  
 algebraic terms/equations, I/V characteristics

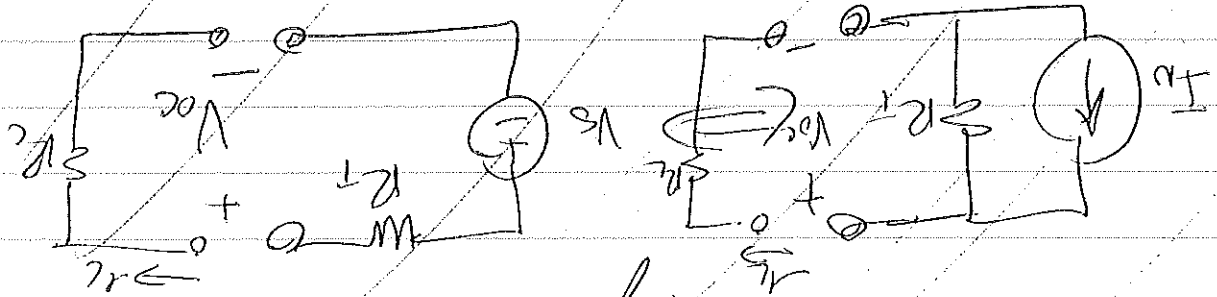
"one port networks" & equivalent circuits  $\Rightarrow$

each box is 2 port  
 device  $\Rightarrow$  describe  
 by an i-v charac.

(no matter what is in box)



Thévenin Thm: when viewed from  
 the load, any network composed  
 of ideal current & voltage sources  
 linear resistors may be represented  
 by an equivalent circuit consisting  
 of an ideal voltage source  $V_T$  in  
 series with an equivalent resistance  $R_T$



Reveal our source & terms:

where  $V_S = I_N R_T$   
 in both cases  $V_{OC}$  &  $R_L$  are the same!

②

Just time we went from  $V$ -source to  $V$ -source + back up as resistor

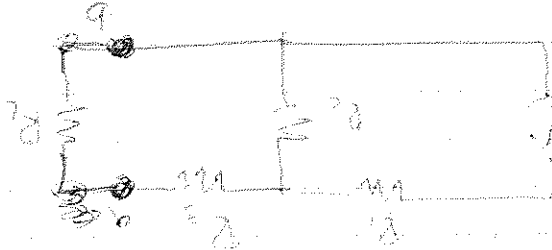
Time  $t_{\text{me}} \Rightarrow$  combine entire subnets  
of elements into a single  $V$ -source  
series  $R$  (or source + parallel  $R$ )

why? if you have large parts of a Ckt that  
netter charge  $\Rightarrow$  say, a load  
resistance that also charge, you simplify  
the non-charging parts so you can  
easily see  $V$ - $V$  & determine input  
varying load w/  $V$ -div. or  $V$ -div.

Nearly identical! This do it?

Step 1: Find equivalent resistance  
presented by Ckt at  
its terminals (of port current Ckt load)



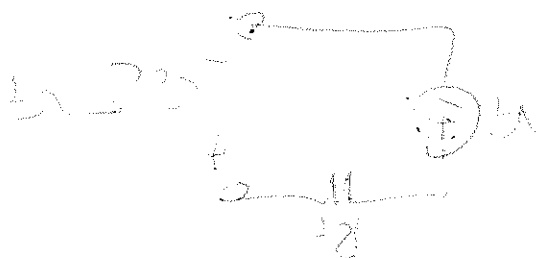
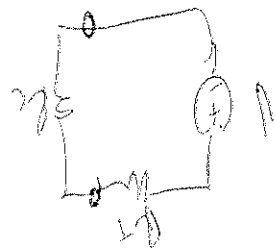


1/2 way there!

Need to compute the equivalent (Thevenin) source voltage. This is given by the open circuit voltage provided at the load terminals after load is removed.

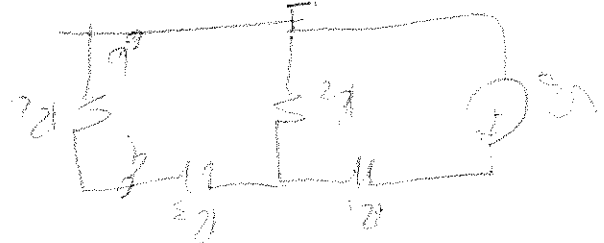
Steps

- 1) Remove load & load terminals are open circuit
- 2) Define the open circuit voltage
- 3) Apply any method (nodal analysis) to solve for  $V_{oc}$
- 4)  $V_T = V_{oc}$

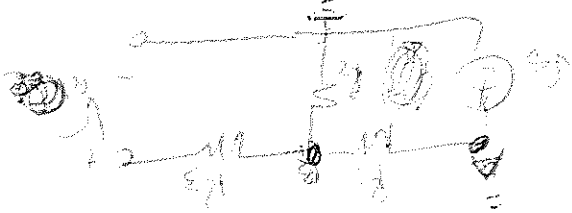


\* The current flows through  $R_T$  when circuit is again modelling drop so  $V_T = V_{oc}$

Back to original example:



after 2 steps:

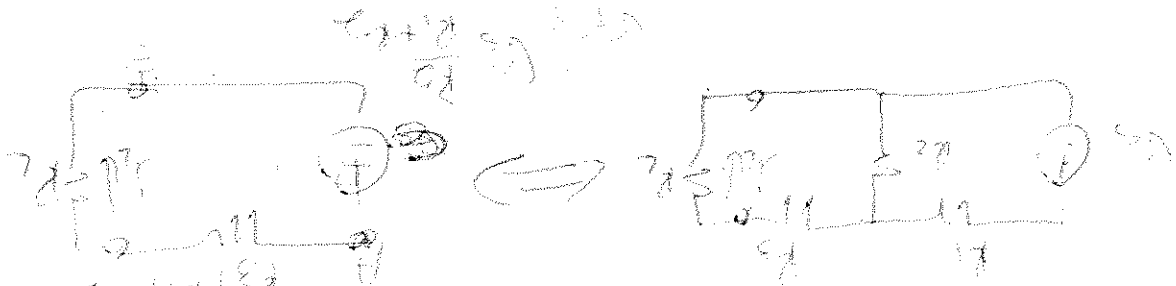


Assume  $R_T = R_1 // R_2 + R_3$

note: no current flows through  $R_3$  b/c the open circuit so  $V_B = V_{oc}$

$$V_L = \frac{V_S \left( \frac{R_3}{R_2 + R_3} \right)}{\frac{R_1 + R_2}{R_1 + R_2} + R_L} = \frac{V_S \left( \frac{R_3}{R_2 + R_3} \right)}{R_1 + R_2}$$

the current drawn by the load is the same in both circuits!



So, we agree about equivalent circuits!

$$\frac{1}{\frac{1}{R_1} + \frac{1}{R_2}} = \frac{R_1 R_2}{R_1 + R_2}$$

$$V_C = V_B - V_S \left( \frac{R_1}{R_1 + R_2} \right)$$

$$V_B = V_S \left( \frac{R_2}{R_1 + R_2} \right)$$

$$V_B \left( \frac{R_1}{R_1 + R_2} + \frac{R_2}{R_1 + R_2} \right) = V_S$$

$$\frac{V_B}{R_1 + R_2} = \frac{V_S}{R_1 + R_2}$$

So for given  $V_C = V_B$  - make a note

get in 3.6

Thorton Theorem: Theorem 3.6

The Norton Theorem: When observed from the load, any network comprised of ideal voltage & current sources and linear resistors may be represented by an equivalent circuit consisting of an ideal current source in parallel with an equivalent resistance.

⇒ could compute the current  $I_N$  at  $V_N$   $\Rightarrow \frac{V_N}{R_N}$

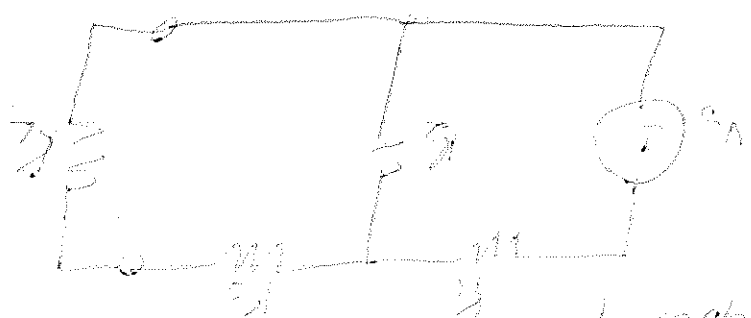
How compute the Norton resistance? duality?

start to short circuit circuit that connected from by the load were replaced by a short circuit.

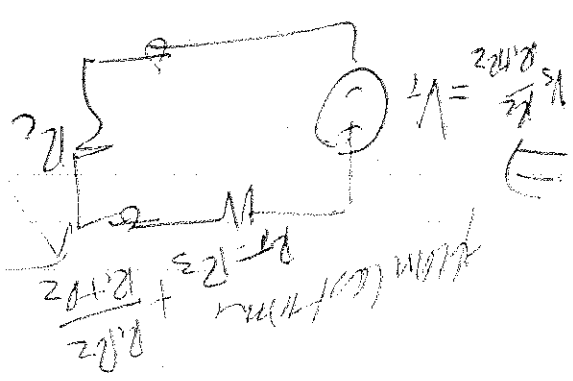
1] replace load of short circuit

2] design the Norton equivalent circuit

3] solve for the necessary method



Example: find  $I_N$  and  $V_N$

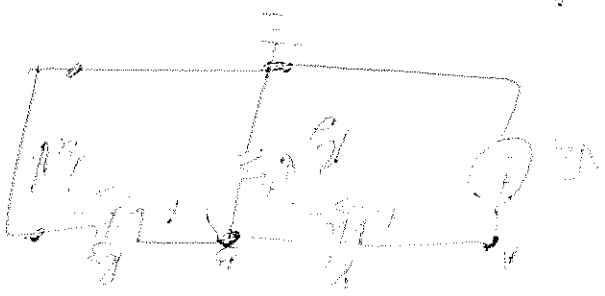


Recall: equivalent resistance seen at terminals a & b



$R_N = R_1 + R_2 + R_3$

Next: super node



nodal analysis

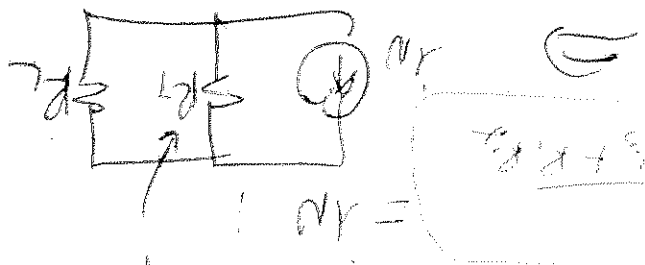
Qb

$$V_3 - V_2 = \frac{R_1}{R_2 + R_3} = \frac{R_1}{\frac{R_2 R_3}{R_2 + R_3}} = \frac{R_1 (R_2 + R_3)}{R_2 R_3}$$

Left hand side

$$= V_B \left( \frac{R_2 R_3 + R_1 R_3 + R_1 R_2}{R_1 R_2 R_3} \right)$$

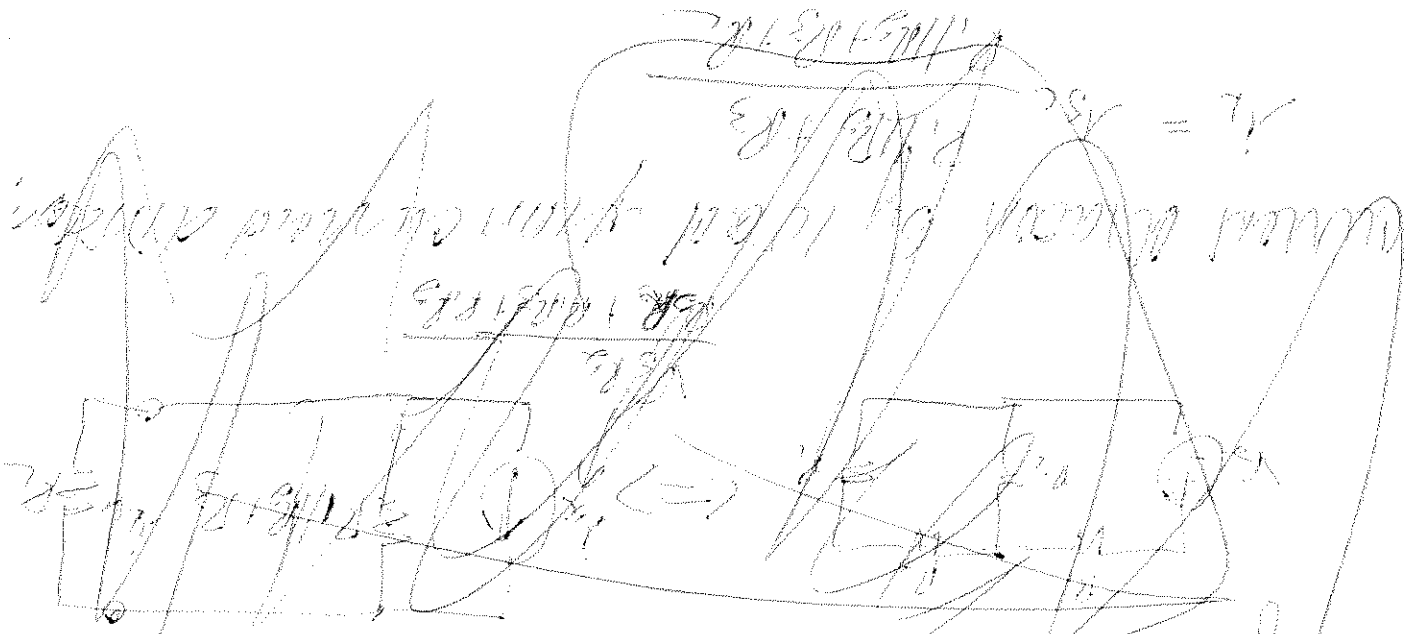
$$V_3 = \frac{R_1 R_2 R_3}{R_2 R_3 + R_1 R_3 + R_1 R_2}$$



$R_1 // R_2 // R_3$

$$-V_2 = \frac{V_3 R_2}{R_2 R_3 + R_1 R_3 + R_1 R_2}$$

super node analysis

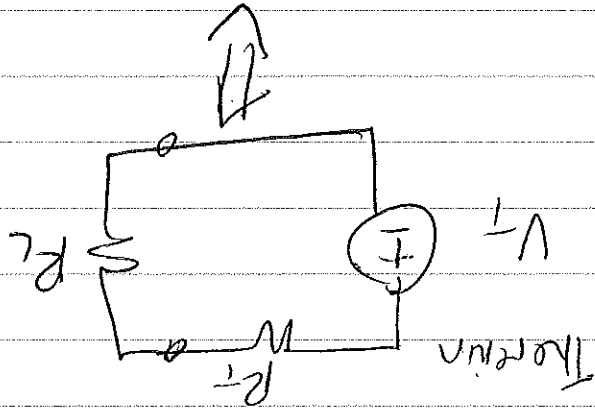
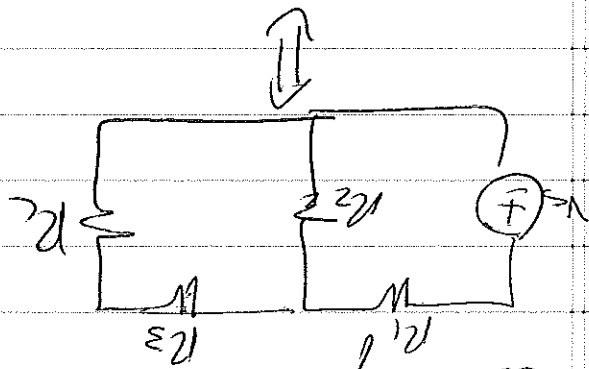


$$V_2 = V_3$$

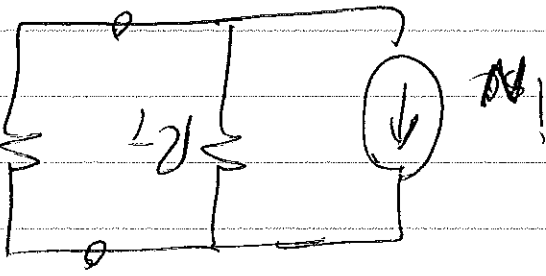
current through  $R_1$  and  $R_2$  is zero

11

→ equivalent circuit



Norton:



$$V_T = V_s \frac{R_2}{R_2 + R_1 R_3 + R_1 R_2 + R_3}$$

$$R_T = R_1 \parallel R_2 + R_3$$

$$= \frac{R_1 R_2}{R_1 + R_2} + R_3$$

Reverse source Xforms:

$$V_T = I_N R_T$$

is this true for our example?

Want to yourself!