# Matheus Sobreira Farias

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## **EDUCATION**

#### **Harvard University**

Cambridge, MA

Ph.D. in Electrical Engineering

2021-2026 (EXPECTED)

Hardware-software co-design of efficient hardware architectures for deep learning. Advised by Prof. H. T. Kung (link). GPA: 3.90/4.00

## Federal University of Pernambuco

Recife, Brazil

B.Sc. in Electronics Engineering

2016-2021

1st out of 40 students, GPA 8.90/10. Senior Thesis: iOwlT: Sound Geolocalization System (link).

# RESEARCH

## Detailed information can be found here.

# EfficientAI/TinyML (Meta AI/AFRL collab)

2021-Present

Harvard University

Designing algorithms to improve deep neural networks efficiency (i.e. quantization, pruning, knowledge distillation, etc). Past work adresses bottlenecks such as data conversions, nonidealities, programming time and weight mapping of compute-in-memory crossbars.

#### iOwlT: Sound Geolocalization System (link)

2019-2020

Federal University of Pernambuco

Developed a system using neural networks, adaptive filtering and real-time processing in FPGAs to recognize sound events and determine gun shooters location on a mobile application. Earned 3 international awards at InnovateFPGA 2019 in China (Top 0.7%).

# | Lock-in: Nano-Volt Signal Amplifier (link)

2019-2020

Federal University of Pernambuco

Design and optimization of a phase-sensitive lock-in amplifier advised by the former Minister of Science and Technology of Brazil Prof. Sergio Rezende to investigate magnetic properties of IrMn/Py thin films using MOKE technique.

#### | iTraffic: Smart Semaphore Network (link)

2017

Federal University of Pernambuco

Design and proposal of an internet of things intelligent system to dynamically choose traffic lights timing to optimize vehicle flow on urban roads using genetic algorithm. Achieved 130% improvement in the average speed of cars in tested tracks.

#### Maracatronics: Robotics Team (link)

2017

Federal University of Pernambuco

Member of the collective autonomous soccer sub-team, acting on robots control on Tiva-C microcontroller, computer vision mapping and tracking, and intelligent robots decision-making strategies. Achieved 5th Place at XVI Latin American Robotics Competition.

## SELECTED PUBLICATIONS

- [2] M. Farias, H. T. Kung, "Efficient Reprogramming of Memristive Crossbars for DNNs: Weight Sorting and Bit Stucking", ISCAS 2025, https://arxiv.org/pdf/2410.21730.
- [1] M. Farias, H. T. Kung, "Sorted Weight Sectioning for Energy-Efficient Unstructured Sparse DNNs on Compute-in-Memory Crossbars", *ISCAS* 2025, https://arxiv.org/pdf/2410.11298.

## WORK EXPERIENCE

#### Nissan Advanced Technology Center

Silicon Valley, CA

AI Hardware Accelerator Intern

SUMMER 2025

Led AI accelerator architecture exploration and C++ behavioral modeling. Designed vectorized processing elements optimized for self-driving vehicles, synthesizing RTL using Vitis HLS. Conducted architecture performance analysis and benchmarking, delivering reports on resource utilization and timing metrics.

Neurotech Recife, Brazil

Machine Learning Operations Intern

2020-2021

Implemented 5 machine learning algorithms for creditworthiness assessment system. Built end-to-end ML pipeline using PyTorch for model development, ONNX for production deployment, and MLflow for experiment tracking and model management.

# **TEACHING**

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