
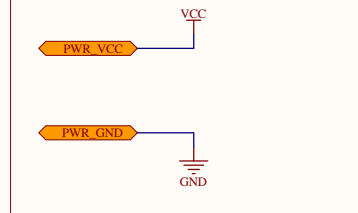


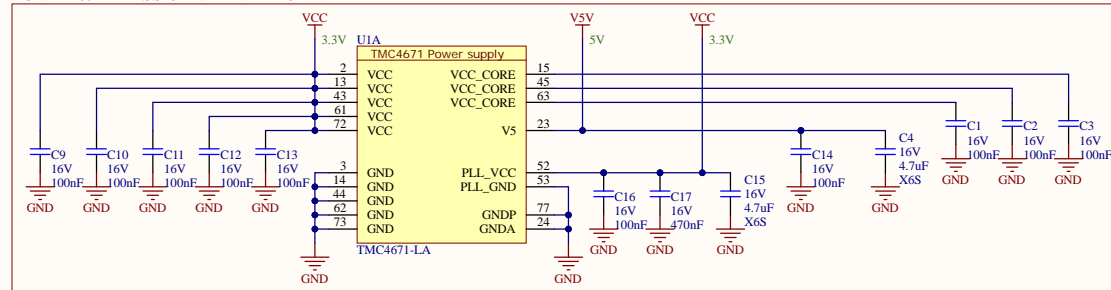
Title: PSU & Interconnexions	Version: 3.0.0	
Board: SSL_Brushless	Size: A3	
Project: SSL 2021	Sheet: 1 of 3	
Author: RDE / ACH	Date modified: 29/07/2021	



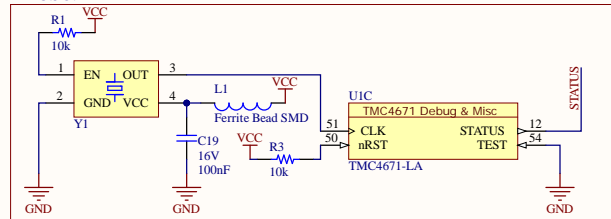
POWER & BYPASS CAP. GLOBAL



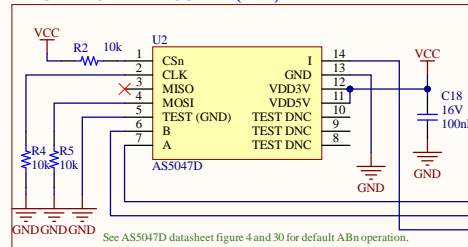
POWER & BYPASS CAP. TRINAMIC



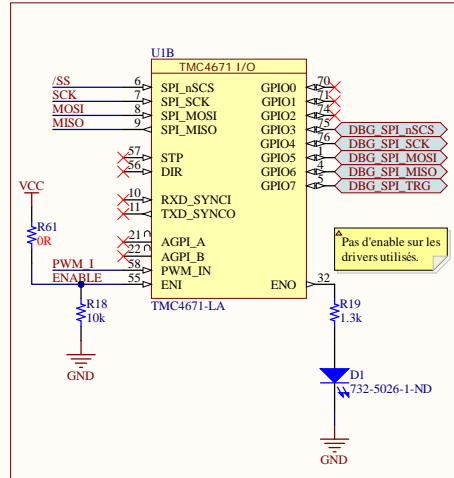
HF OSC.



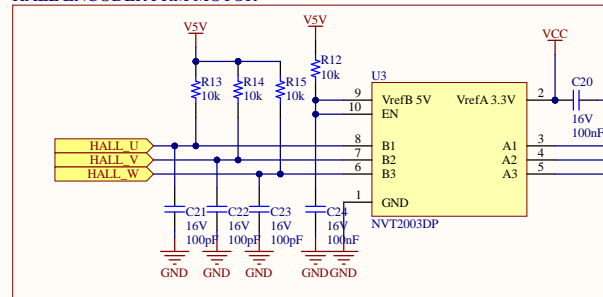
MAGNETIC ABn ENCODER (INT.)



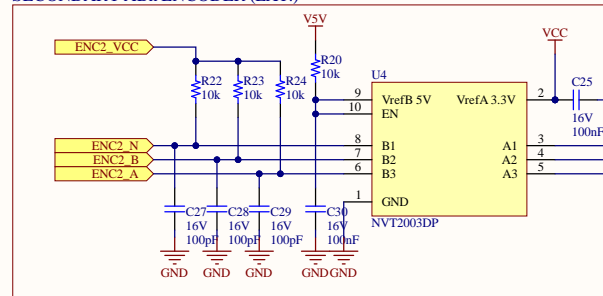
SPI & RTMI DEBUG



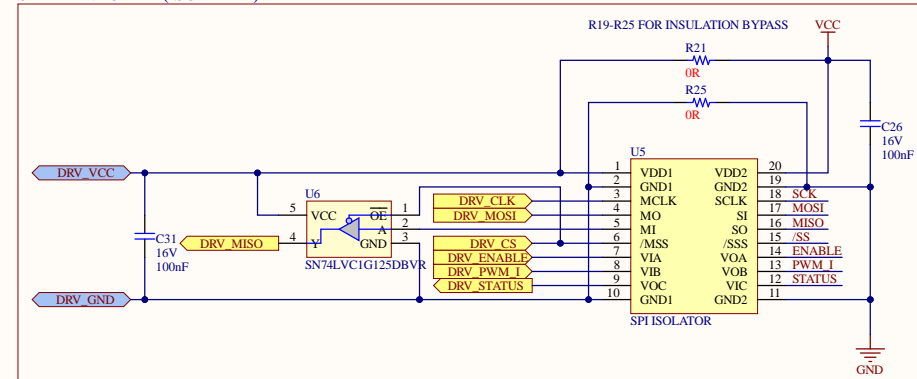
HALL ENCODER FRM MOTOR



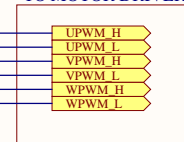
SECONDARY ABn ENCODER (EXT.)



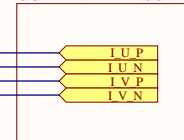
SPI MAINBOARD (ISOLATED)

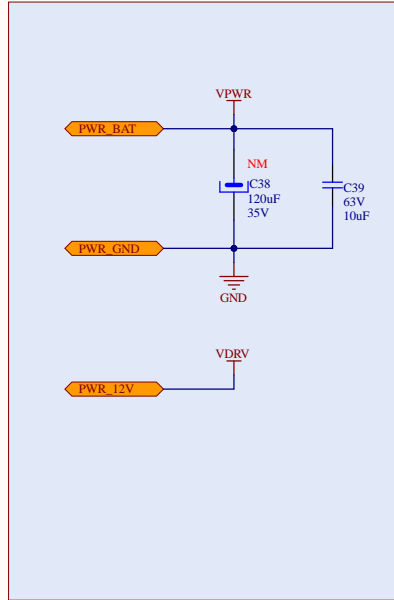


TO MOTOR DRIVERS



CURRENT MEASURES





POWER & BYPASS CAP.

