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PYCO RF USB Adapter						INFORMATION	DOC #			
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[illegible]

The diagram shows a voltage divider circuit. A 5V supply is connected to a 10k resistor (R1), which is connected to the non-inverting input (pin 1) of a TLV70033DCR TI op-amp (IC1). The op-amp is configured as a voltage follower, with its output (pin 5) connected back to its inverting input (pin 2). The op-amp's enable pin (pin 3) is connected to the 5V supply, and its ground pin (pin 4) is connected to ground. A 1μF capacitor (C1, ±10% tolerance) is connected between the 5V supply and ground. The output of the op-amp (pin 5) is connected to a 3V3 supply through a 10k resistor (R2). A 1μF capacitor (C2, ±10% tolerance) is connected between the 3V3 supply and ground.

Rout\_Cut1      Rout\_Cut2

INFORMATION			CLASSIFICATION		PROJECT		COMPONENT	
			CONFIDENTIAL		PYCO			
			TITLE					
			RF USB Adapter					
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