PIXEL CMOS PROJECT

MIMOSA26 PROTOTYPE TEST BOARD

Technical Documentation Version 0.1

JTAG CABLE FOR POWER PULSING

Ref Number: IPHC_CMOS_MIMOSA26_JTAG_PC_PORT_PAR_1002 (100217)

Support:

Web address: http://www.iphc.cnrs.fr/-CMOS-ILC-.html

IPHC
CMOS Group
23 Rue du Loess
F-67037 Strasbourg Cedex

Written by: Kimmo JAASKELAINEN (kimmo.jaaskelainen@ires.in2p3.fr)

Important Information

Warranty:

The MIMOSA26 test board is warranted against defects in material and workmanship for a period of one year from the date of shipment, as evidence by receipts or other documentation. IPHC laboratory will, at its option, repair or replace equipment that proves to be defective during the warranty period. This warranty includes parts and labor.

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Wiring Diagram

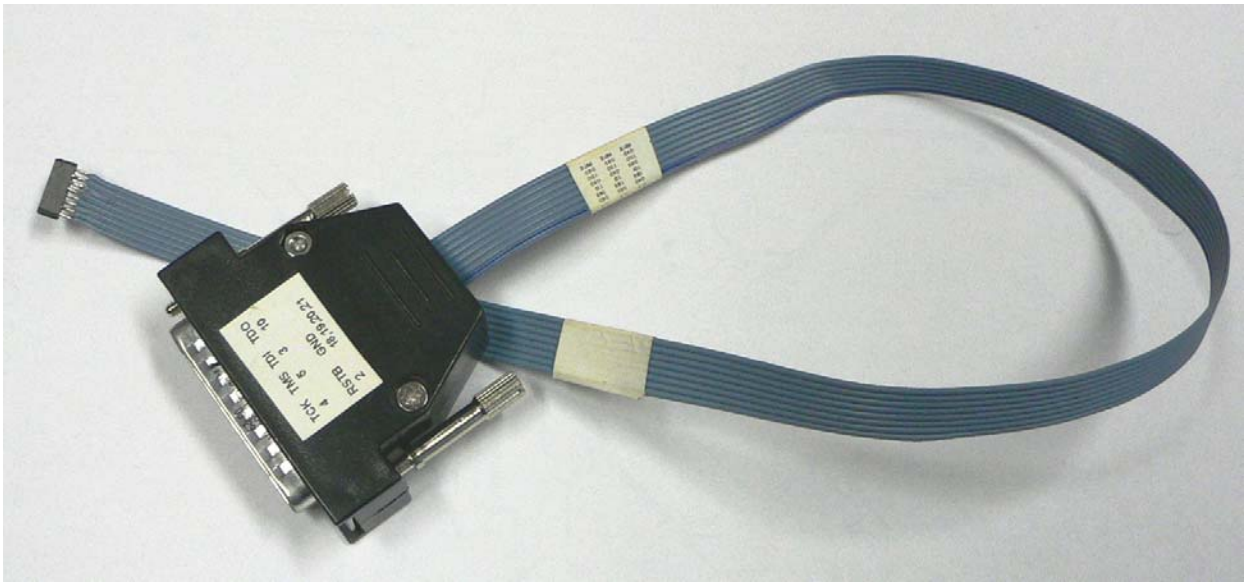
A wiring diagram of MIMOSA26 POWER PULSING JTAG cable for connecting to PC's parallel port

A wiring diagram for PC's Parallel Port (Front view)

Pin	Signal
2	RSTB
3	TDI
4	TCK
5	TMS
7	POWER PULSING STATUS OUT
10	TDO
12	POWER PULSING REQUEST IN
18,19,20,21	GND

Connector: Female D-Subminiature (SUBD) DB25 connector for cable connection

JTAG Cable for MIMOSA26 POWER PULSING



If there are compatibility problems with PC's parallel port the following modifications can be done:

1. Add a 220 ohm resistance between pin 10 (TDO signal) of connector DB25 and the ribbon cable (see the Figure below).

2. Add a bypass capacitor of 10 µF with a 50 resistance in serial to the pin 2 (RSTB) of connector DB25 as shown in Figure below.