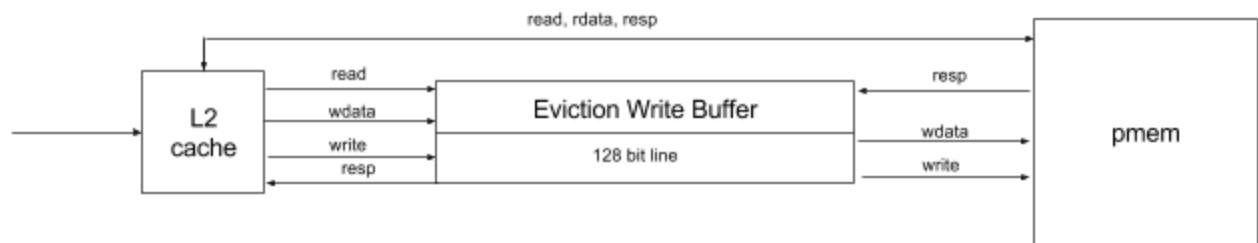


## Eviction Write Buffer Design



Our eviction write buffer takes care of all the writes from L2 as long as the line is free in the buffer. At a later time (as long as read/write are not high), it sends a write signal from itself to pmem.

The one weird case will be when we are waiting to write, but then another L2 write comes in, which can't occur as there is only one entry in the eviction write buffer. In this case, we will prioritize the write to pmem from the eviction write buffer, and then come back to the empty state where we will fill the buffer with the new line L2 wants to write.

It will have a state diagram such as below:

