

5

4

3

2

1

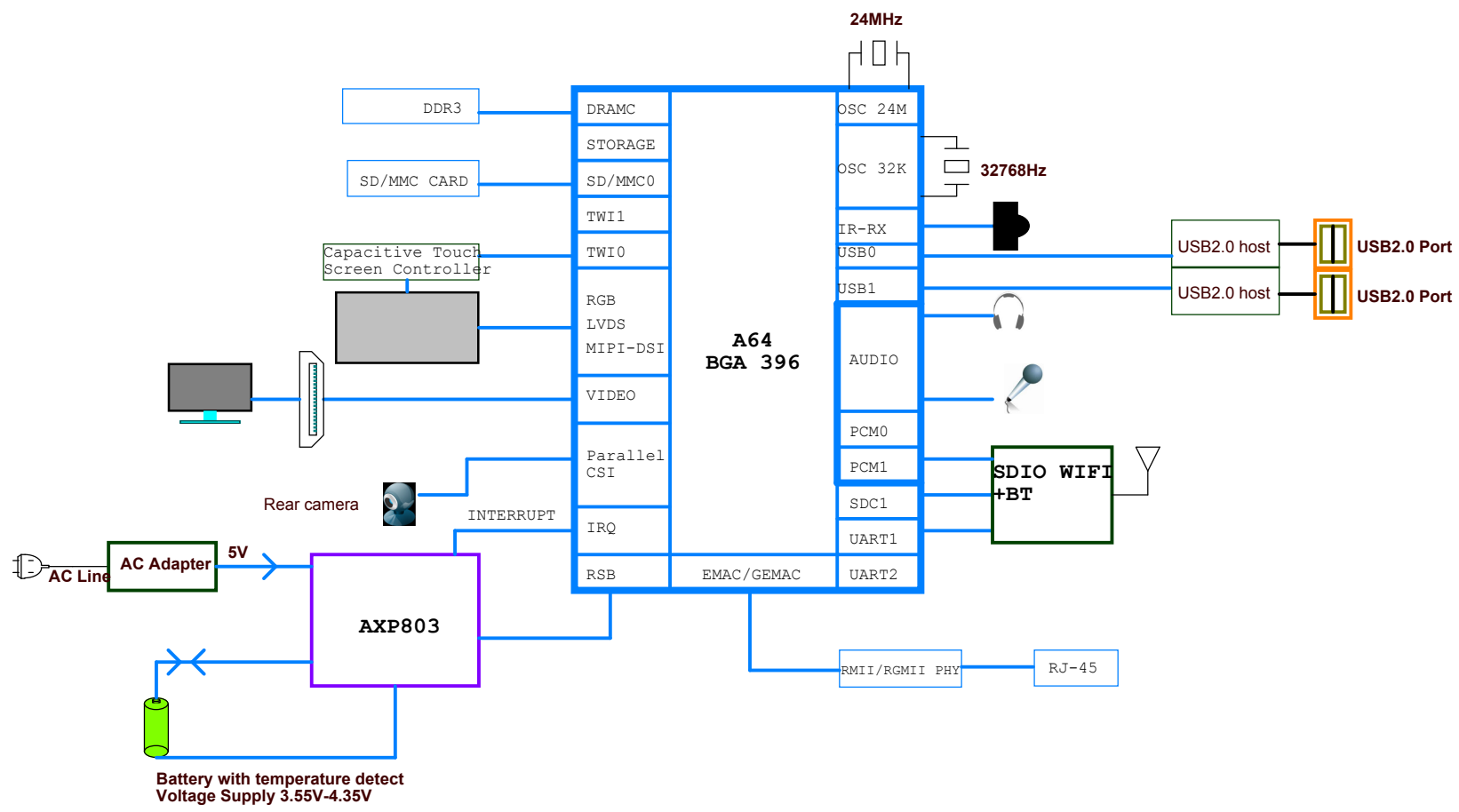
VERSION HISTORY

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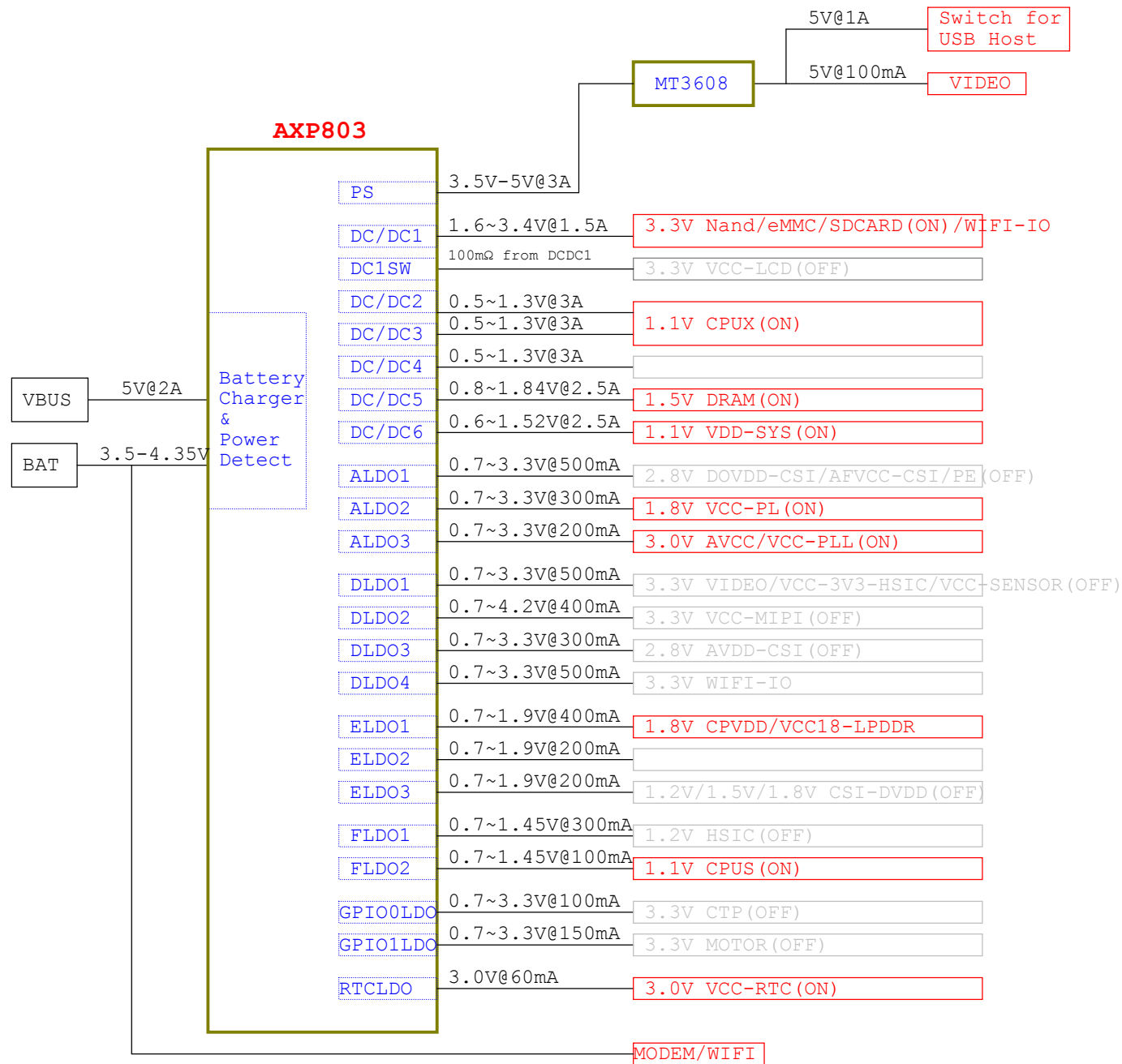
Revision	Description	Date	Drawn	Checked	Approved
Ver 1.0	Release version	2015-04-22			
Ver 1.2		2015-09-28			

BLOCK DIAGRAM



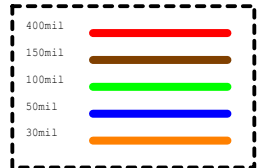
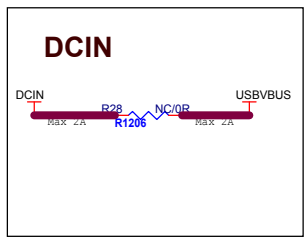
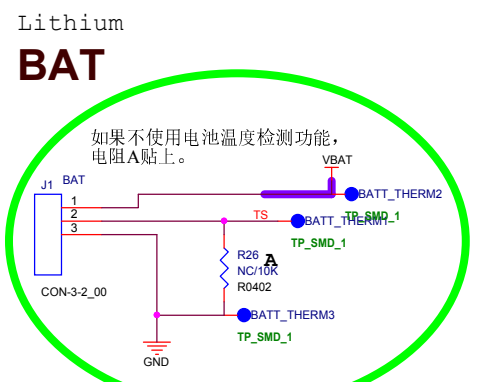
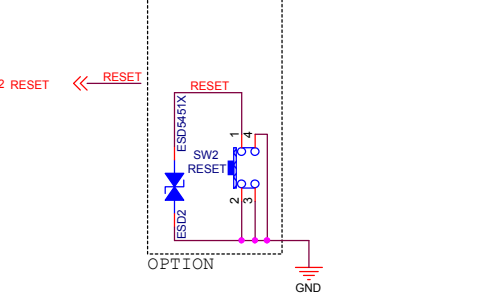
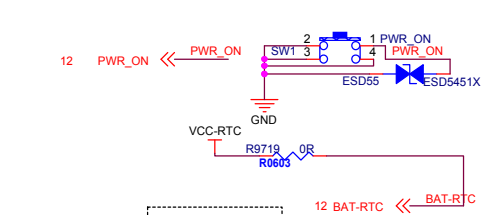
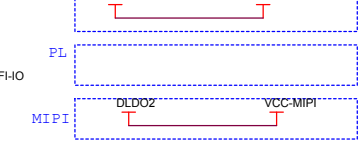
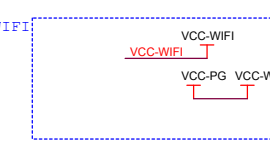
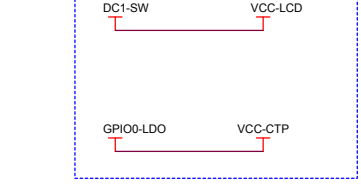
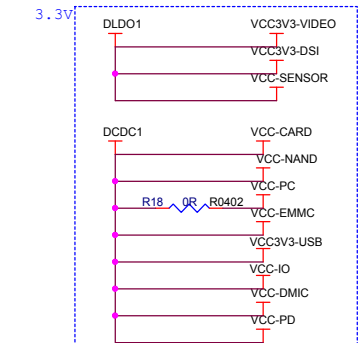
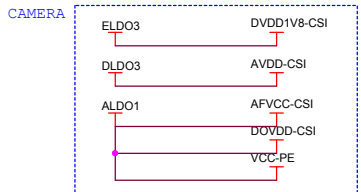
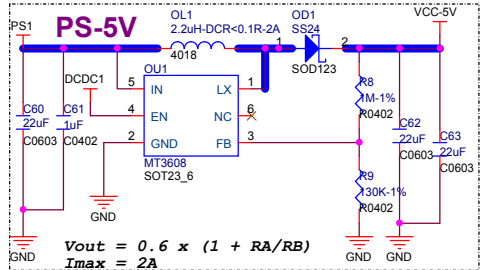
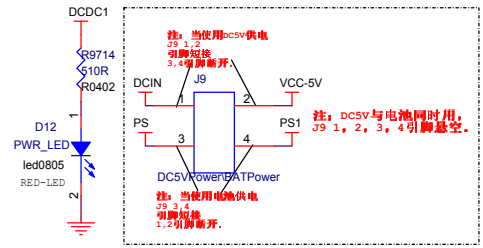
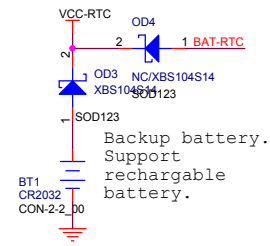
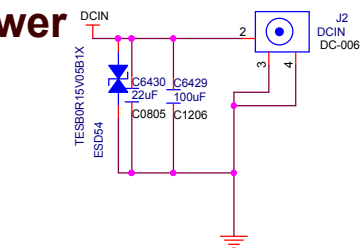
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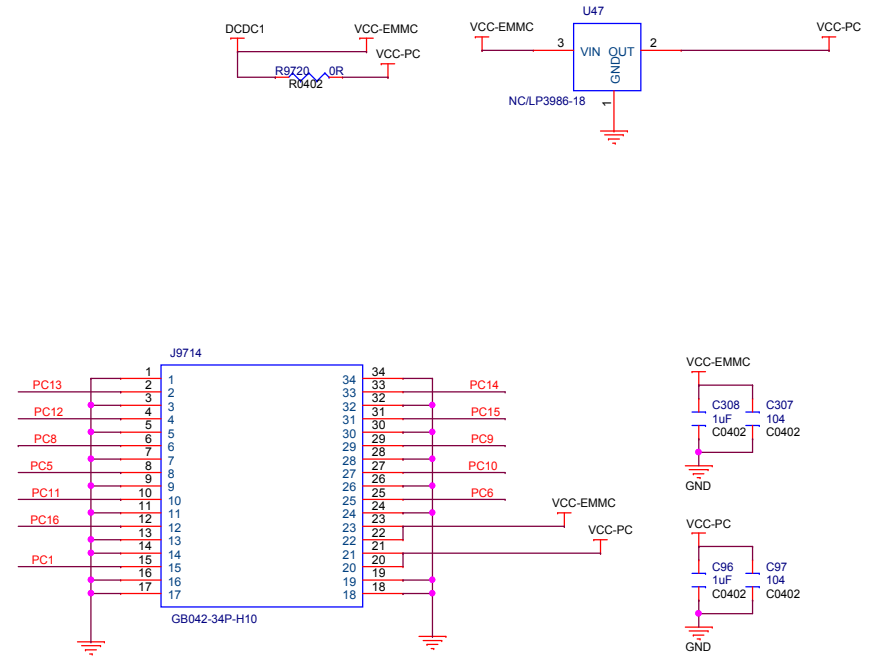
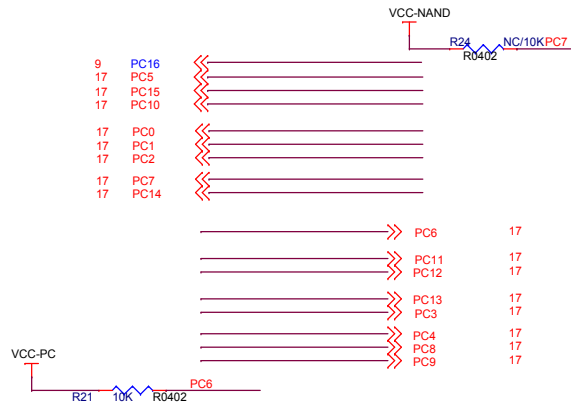
POWER TREE

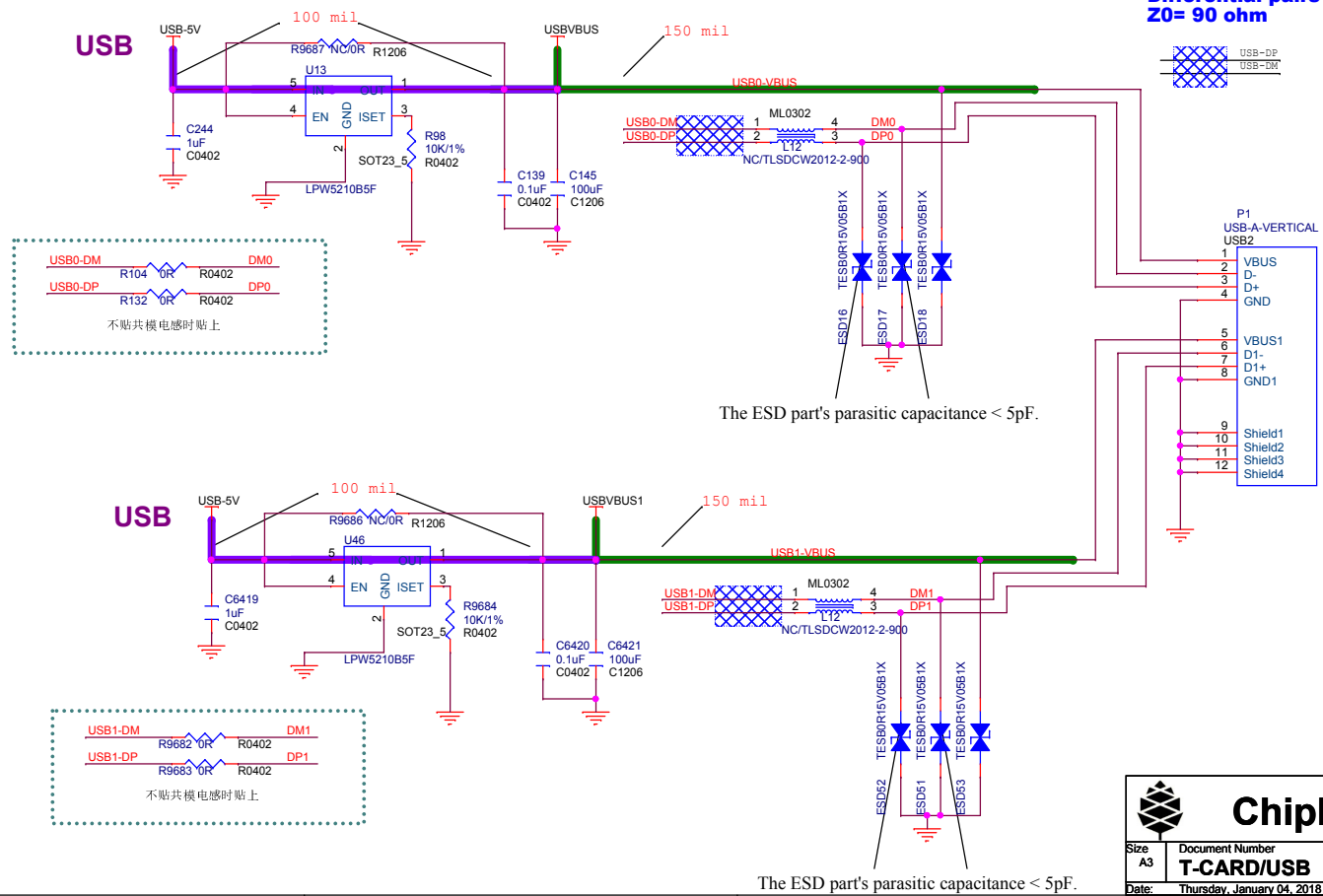
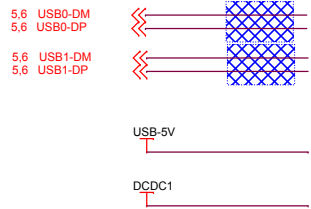


LPDDR3 32X1

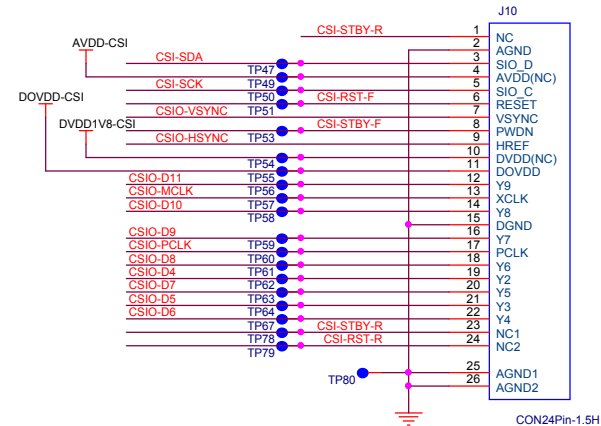
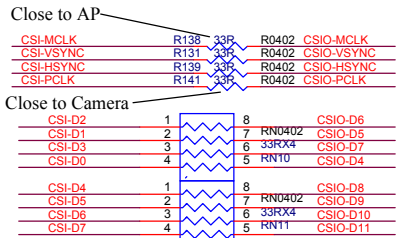
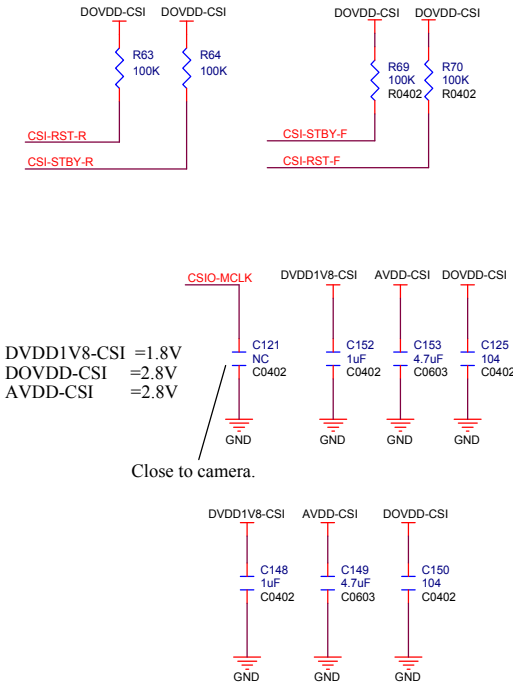
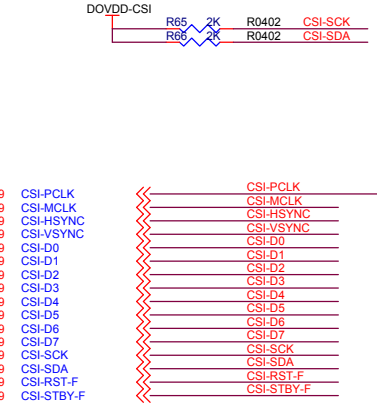
Power



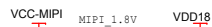
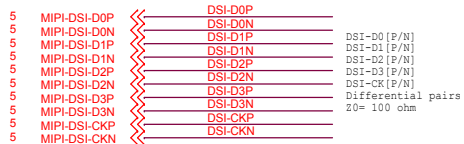
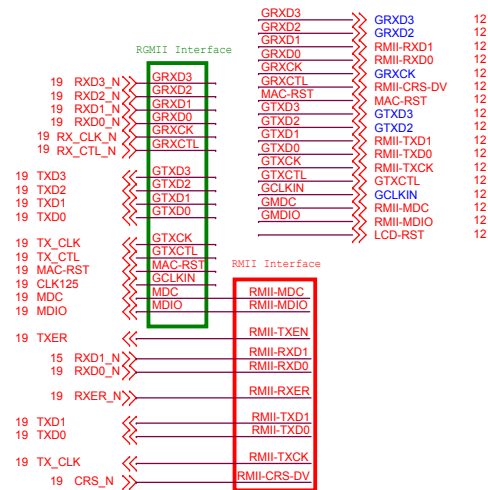
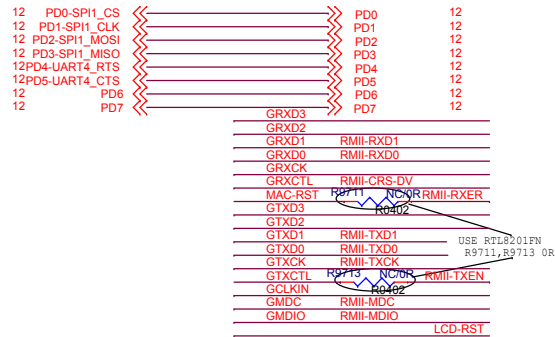




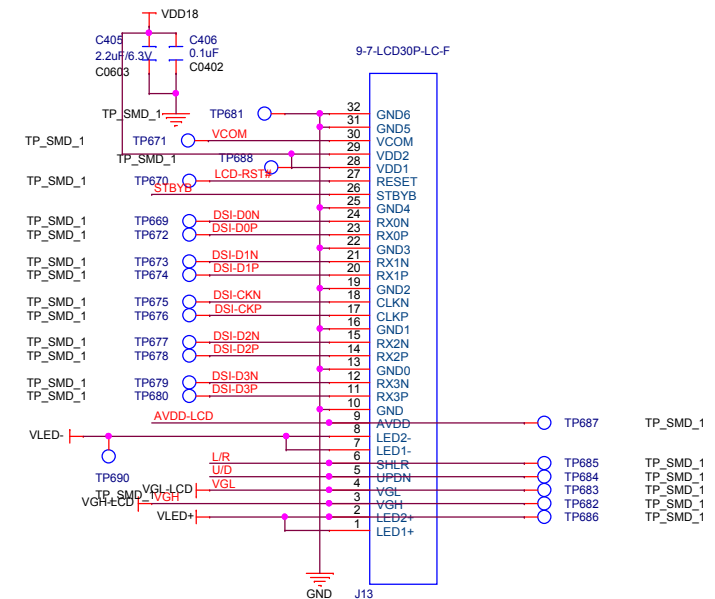
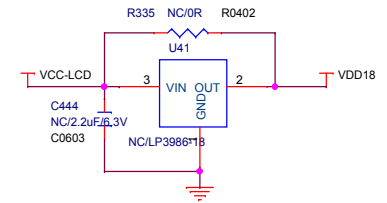
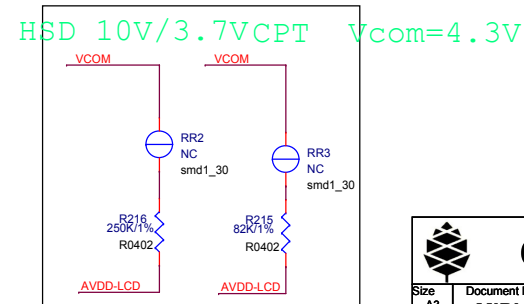
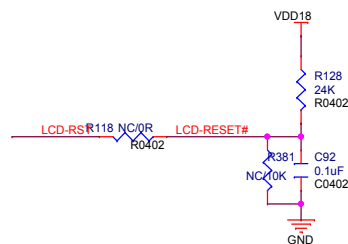
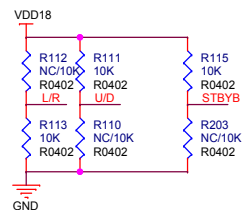
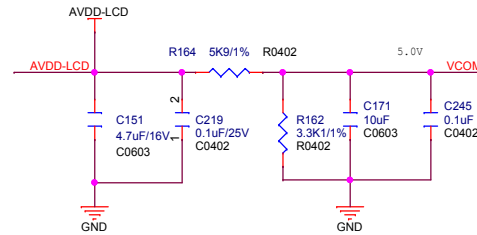
CAMERA



LCD



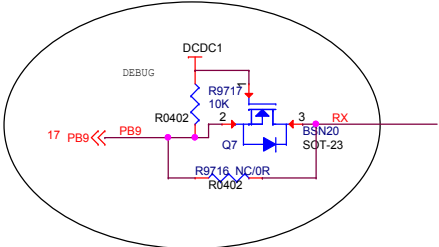
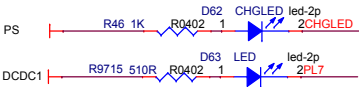
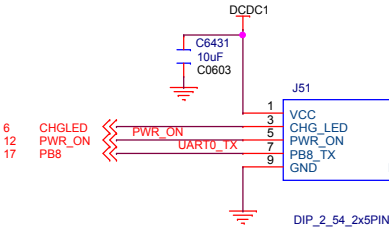
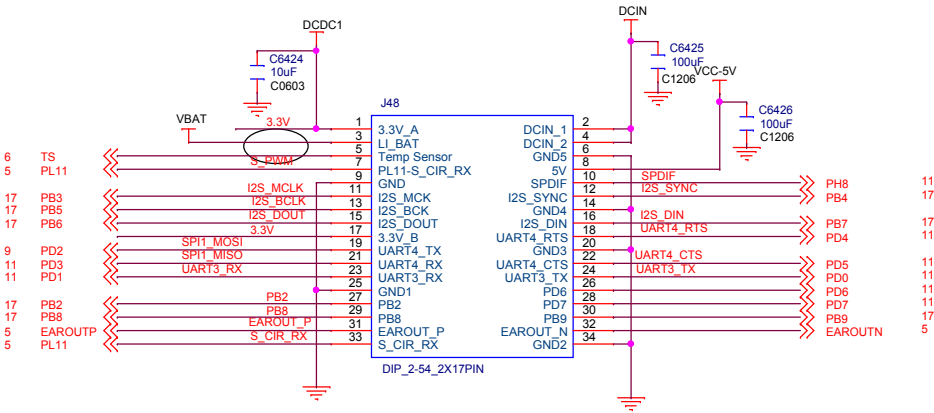
MIPI-LCD



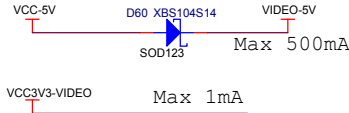
ChipHD to Pine64

Size A3	Document Number MIPI-LCD	Rev 1.2
Date:	Thursday, January 04, 2018	Sheet 11 of 19

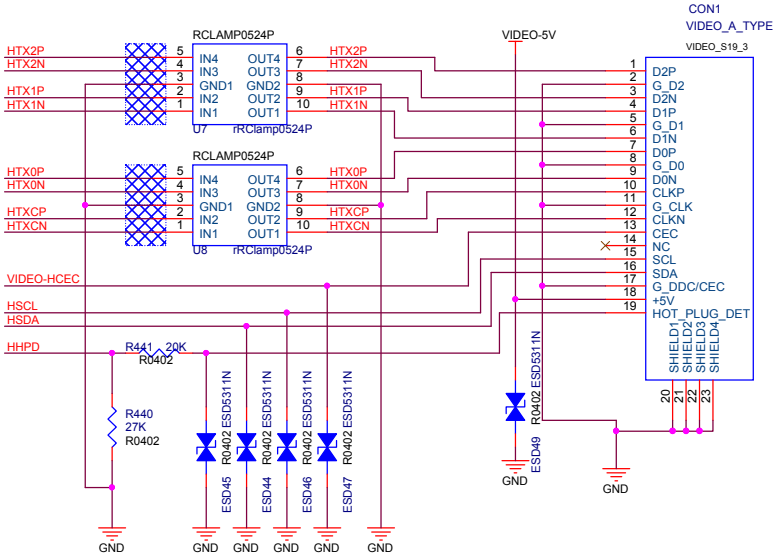
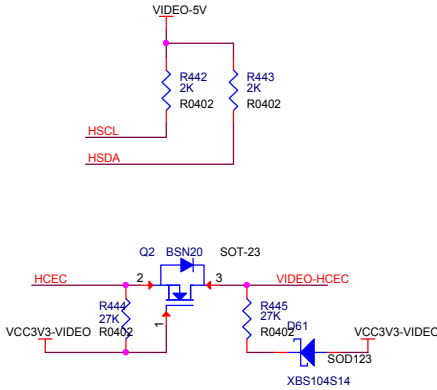
Euler bus

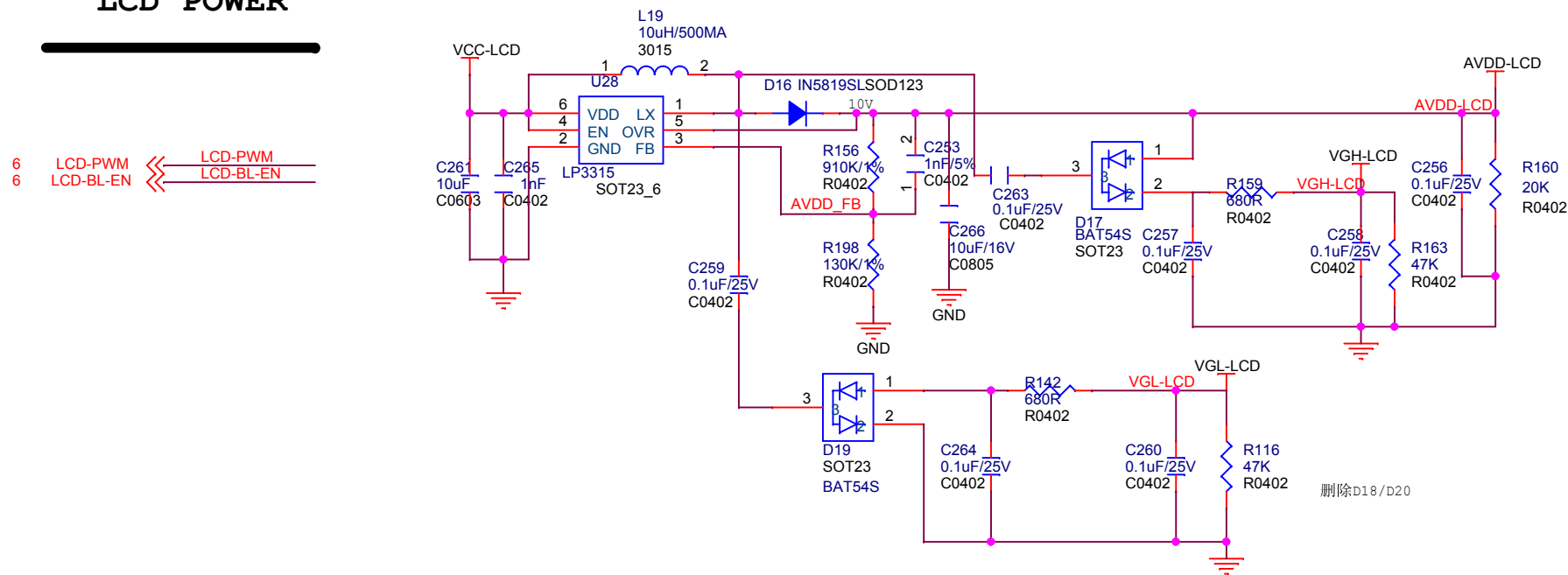


VIDEO

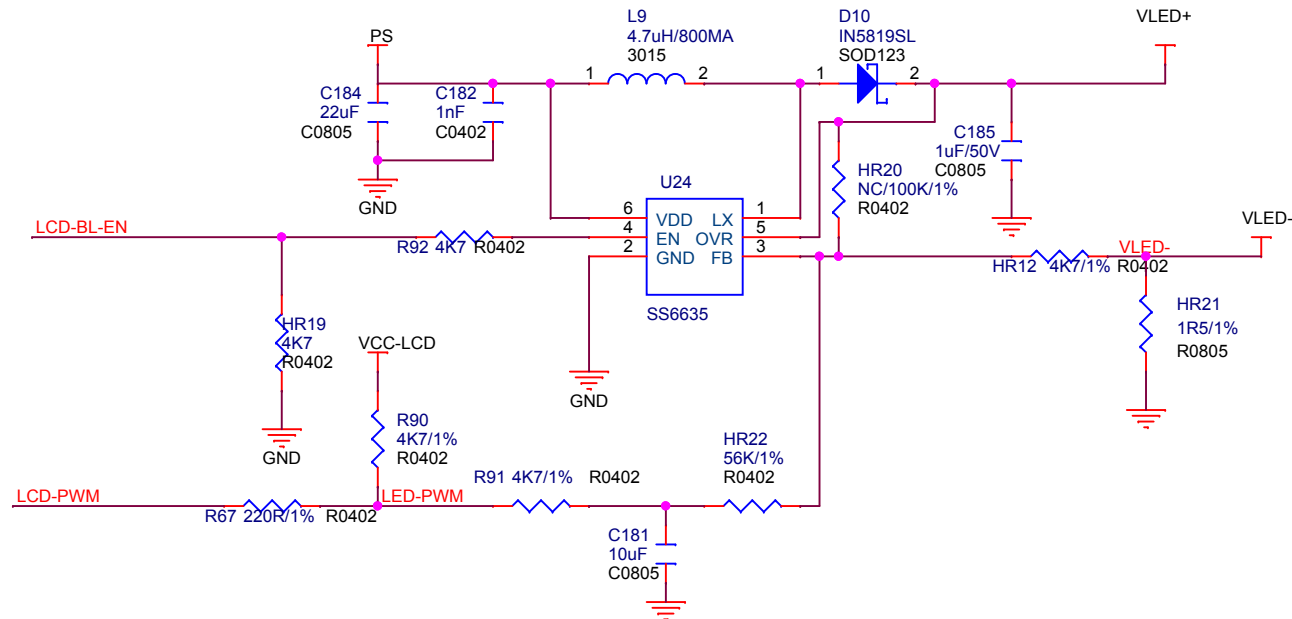


Differential pairs
Z0= 100 ohm





Backlight



ChipHD to Pine64

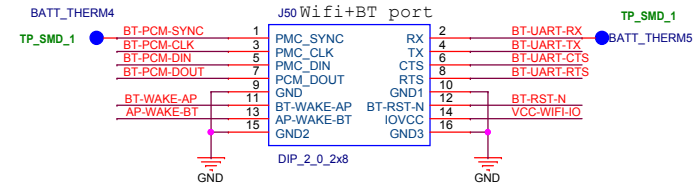
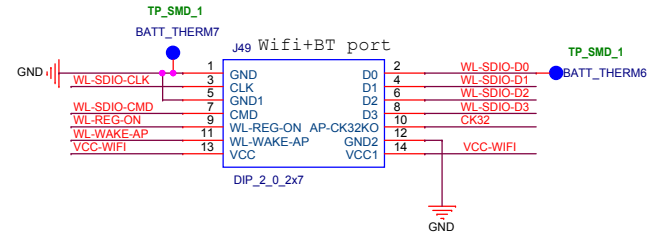
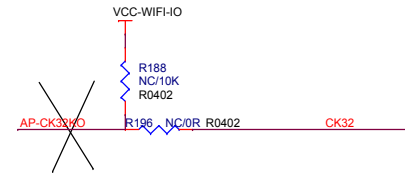
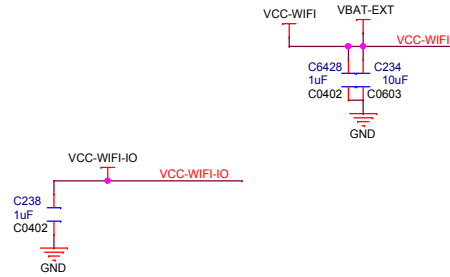
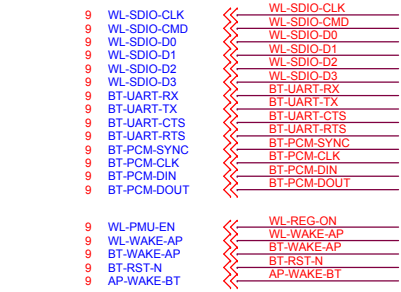
Size
A4

Document Number
LCD POWER

Rev
1.2

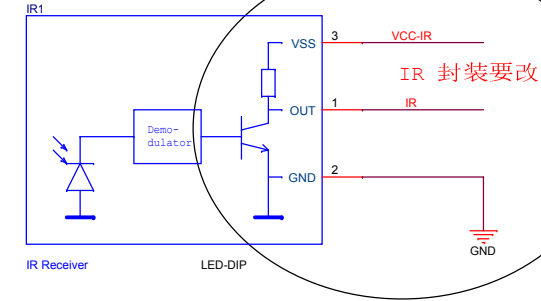
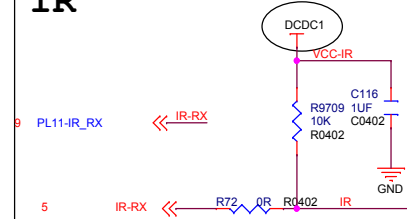
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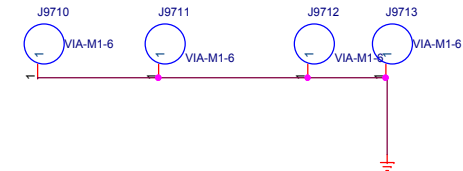
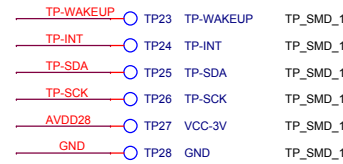
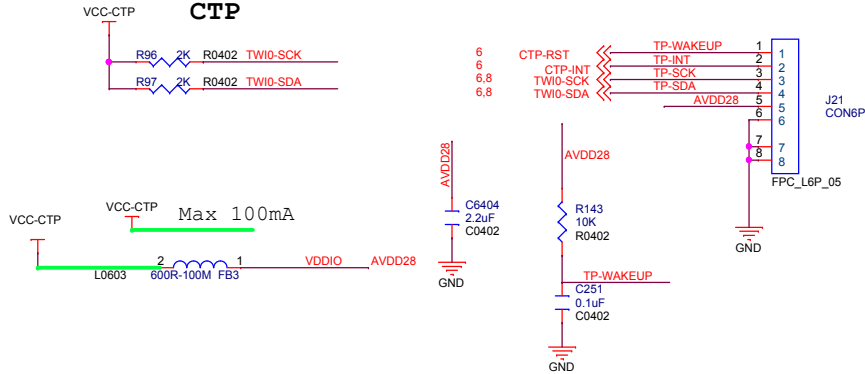


CTP

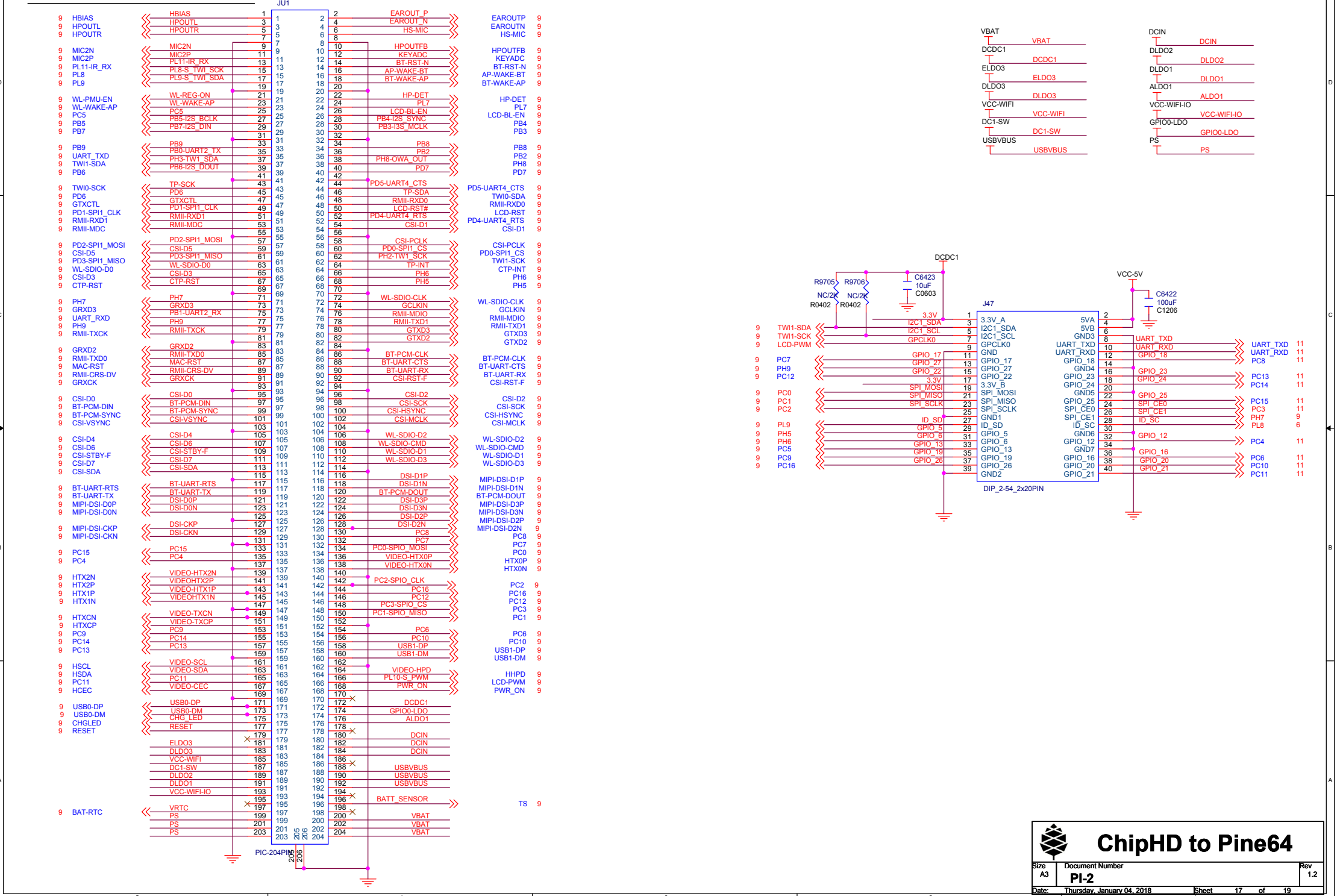
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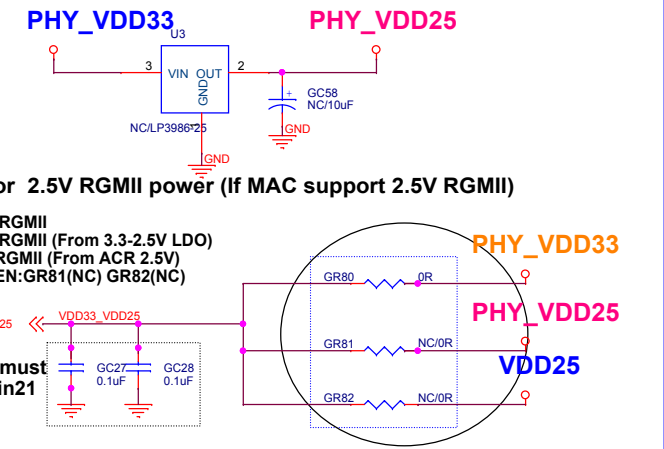


CTP

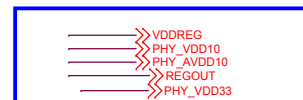
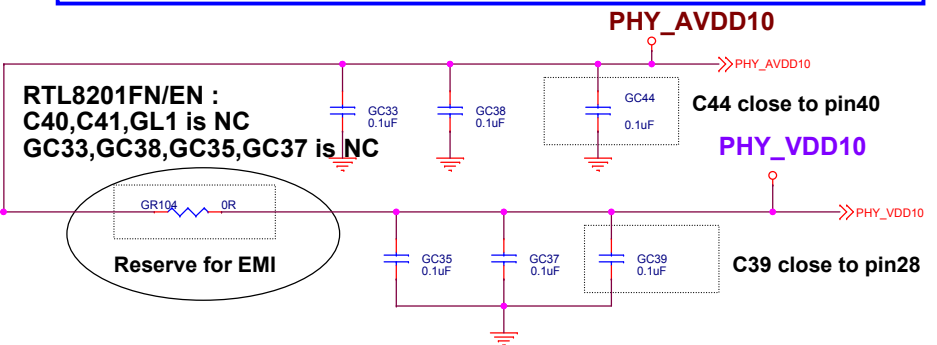
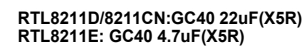


PI-2 bus





Note 1: The Trace length between GL1 and Pin 48 must be within 0.5 cm. GC40 and GC41 to G L1 must be within 0.5cm.



External Power Source

U10,GC69,GC68, GR122,GR120 and GR121 are only used by 8211CN/8211D/8211E application when switching regulator is disabled. For other applications, please remove them.

