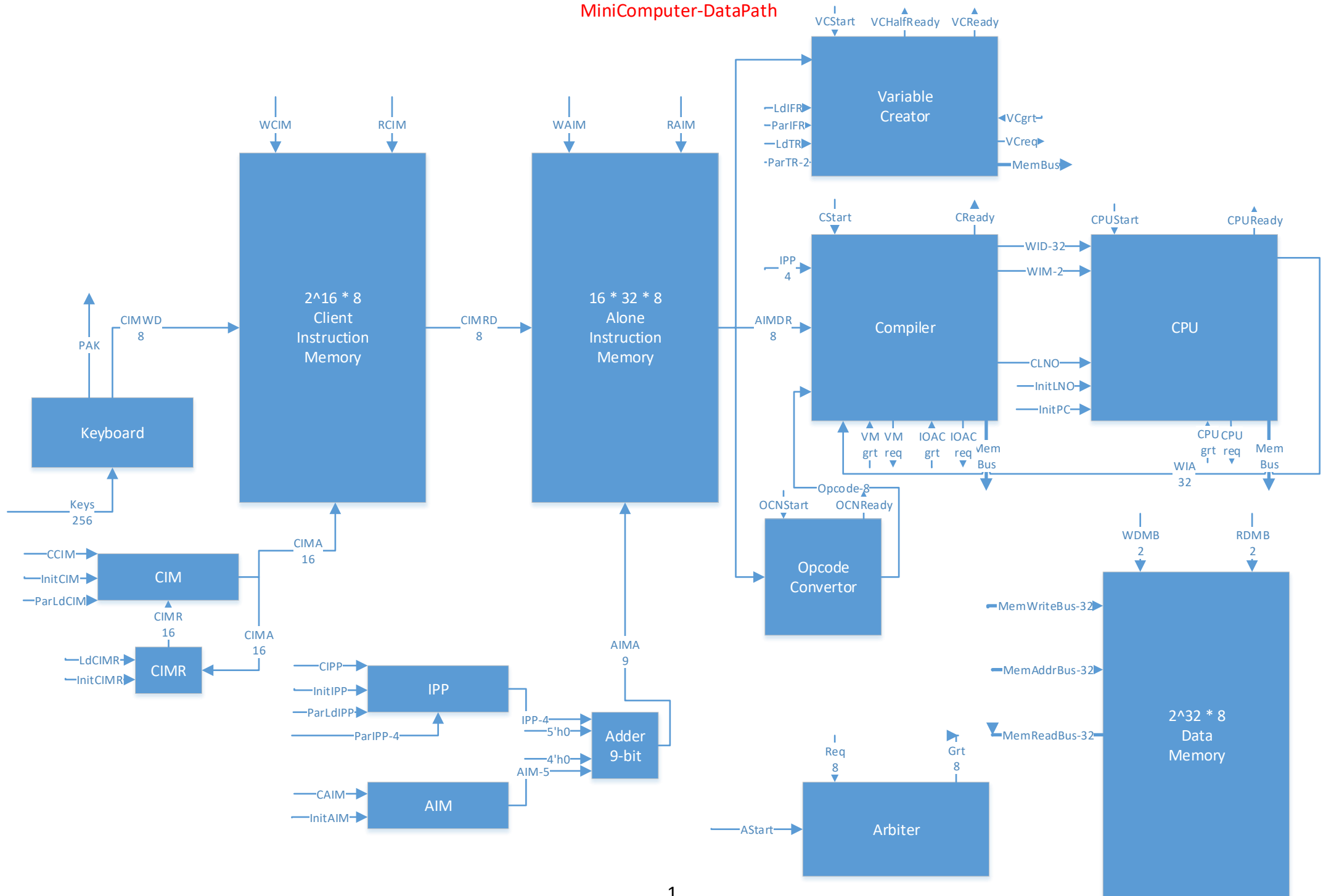
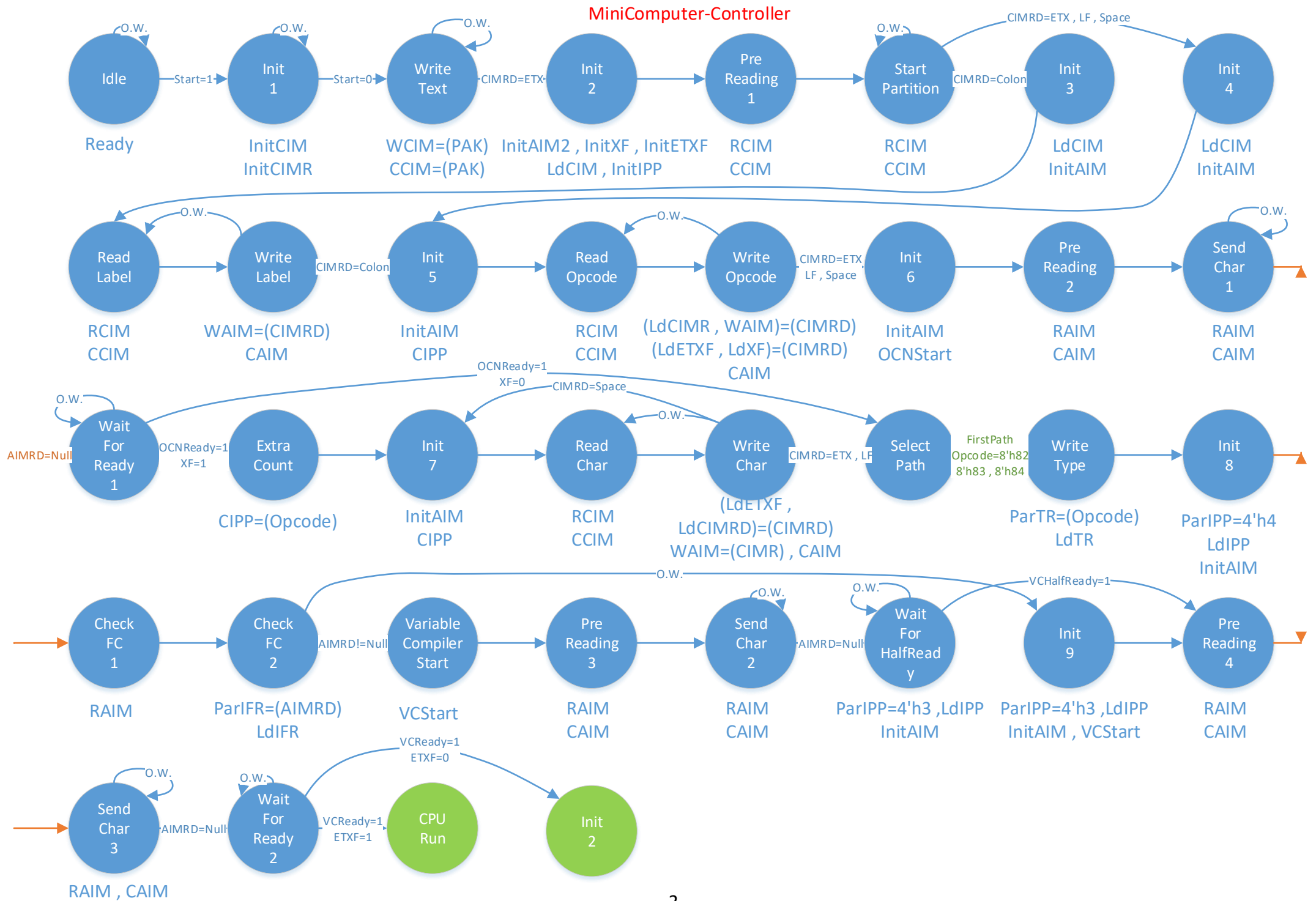
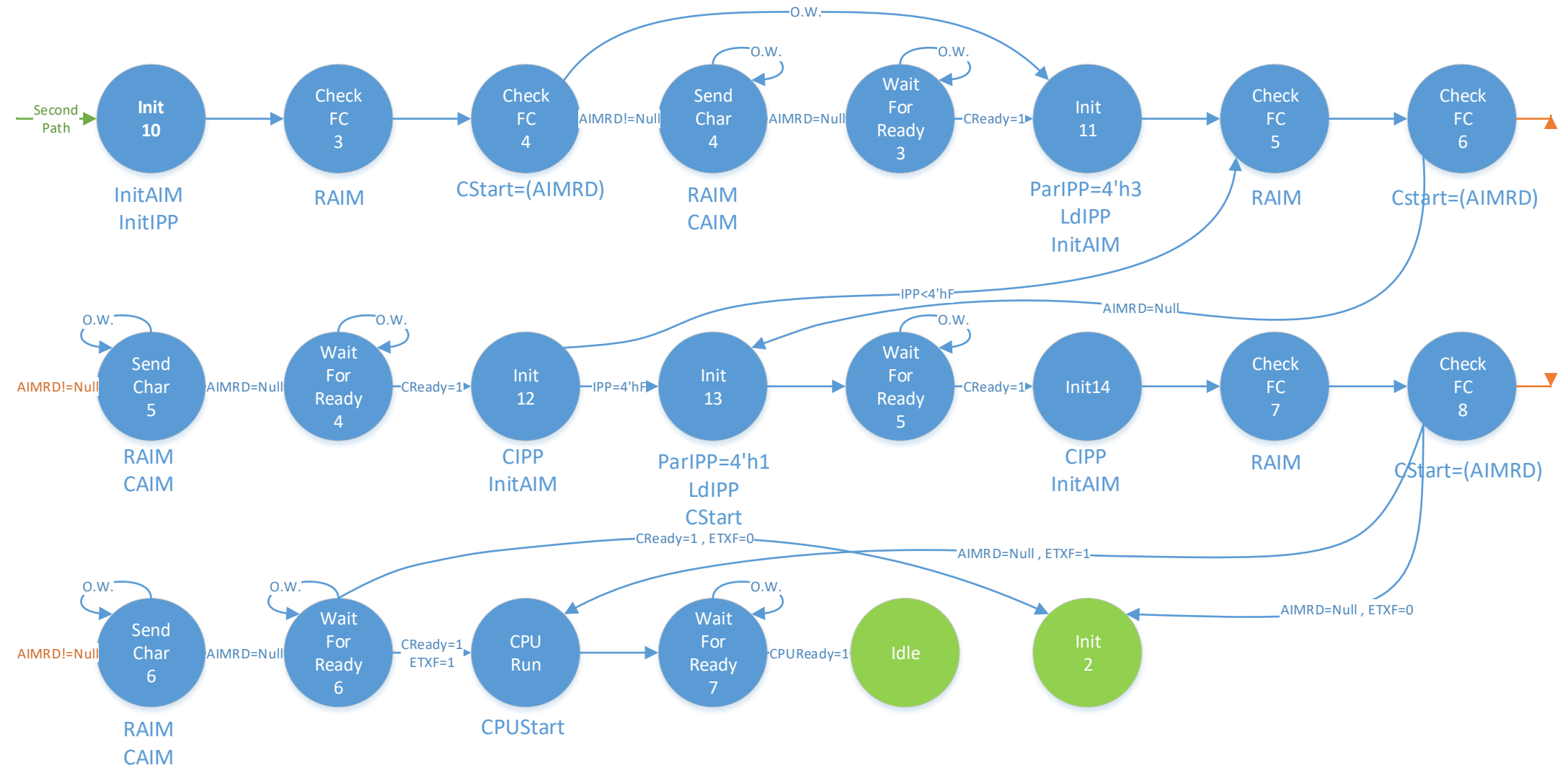


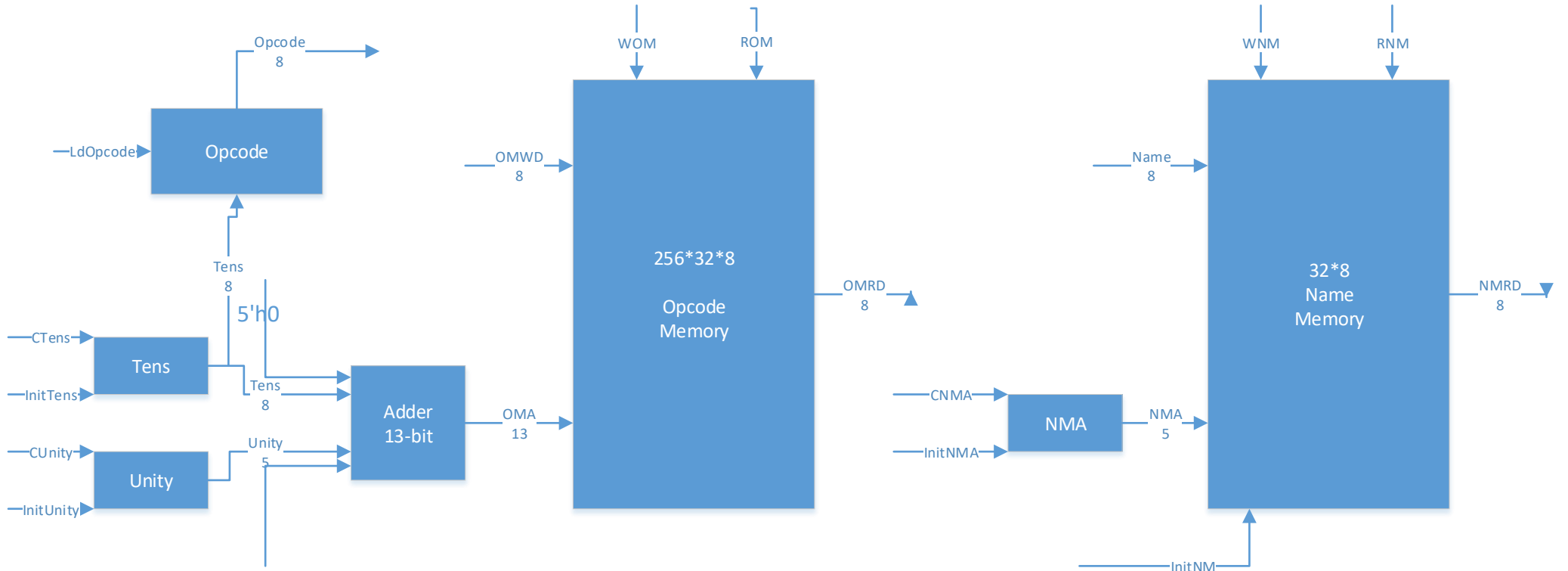
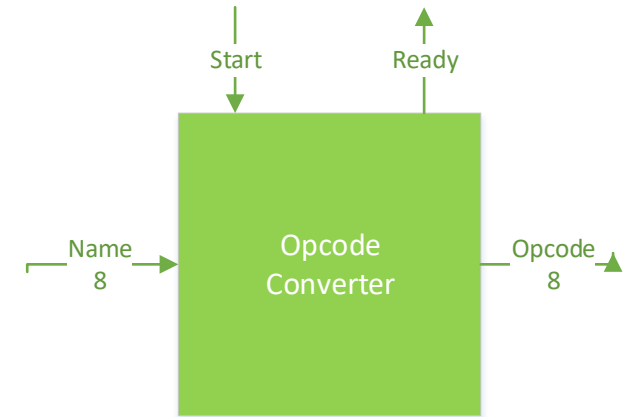
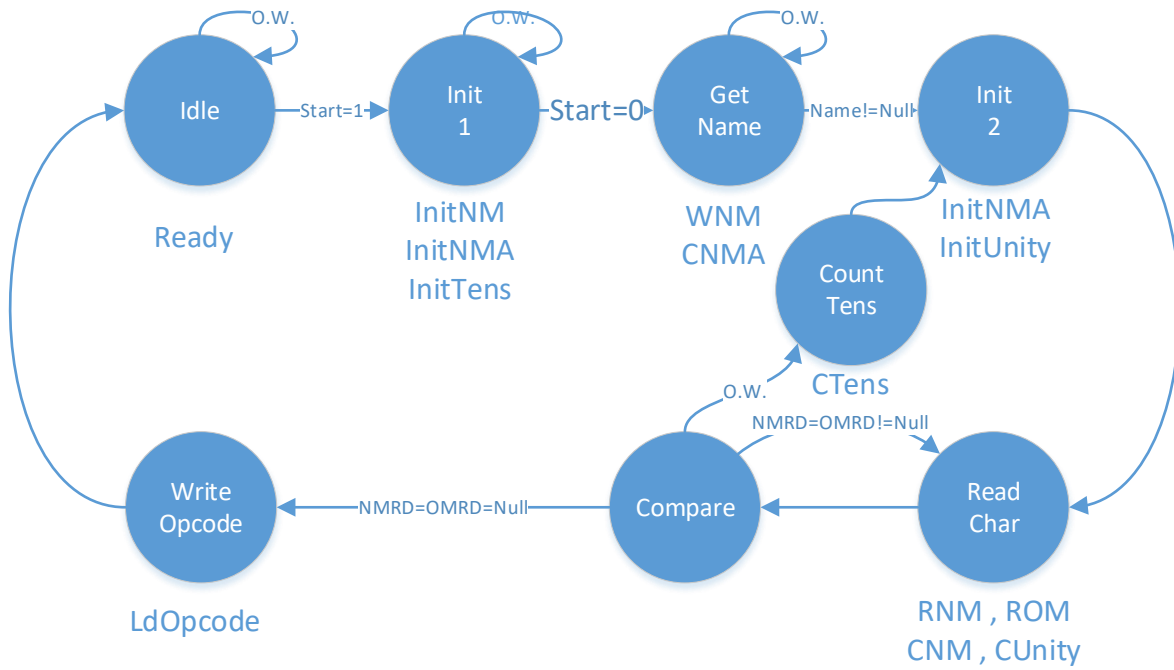
MiniComputer-DataPath

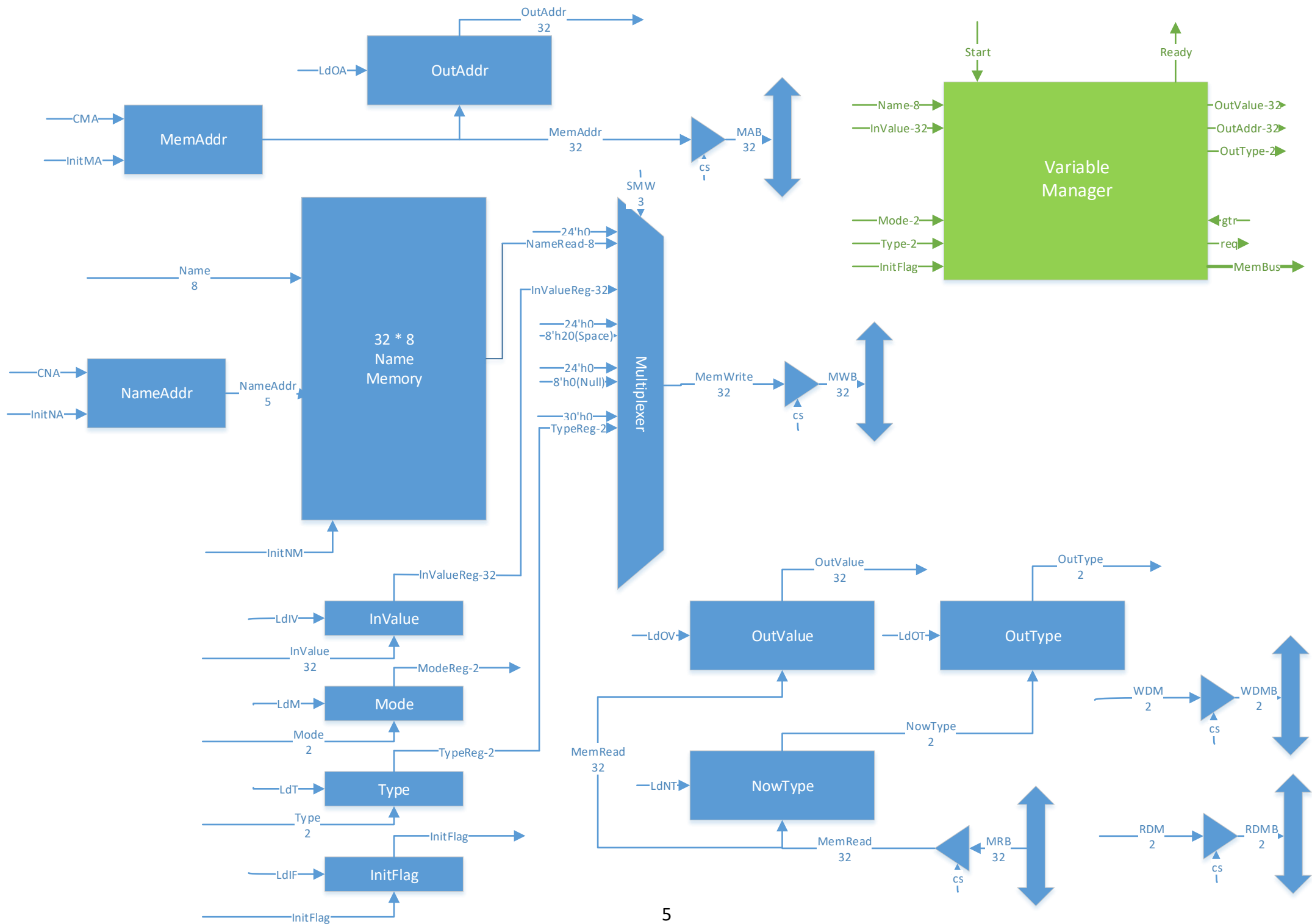




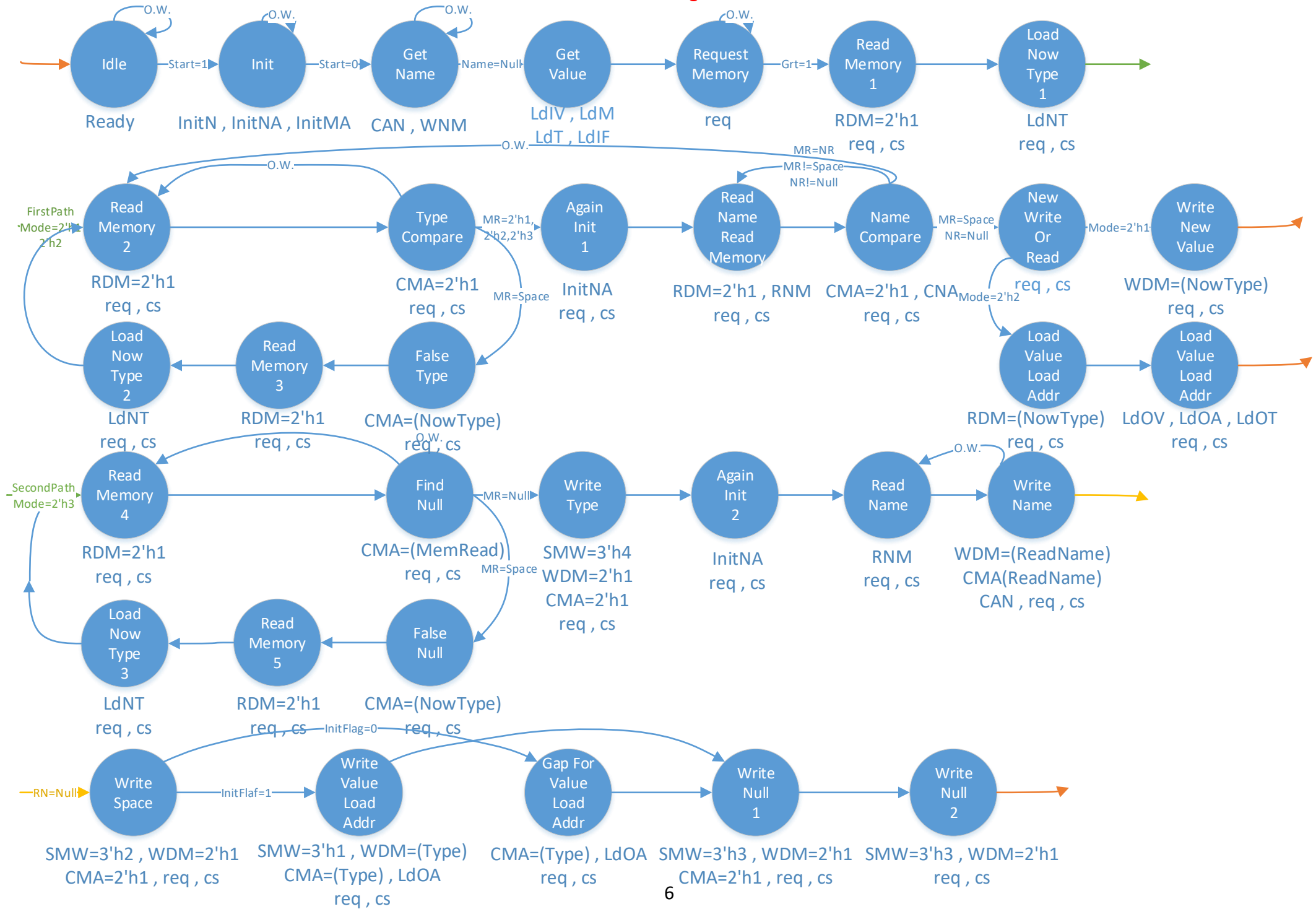
MiniComputer-Controller

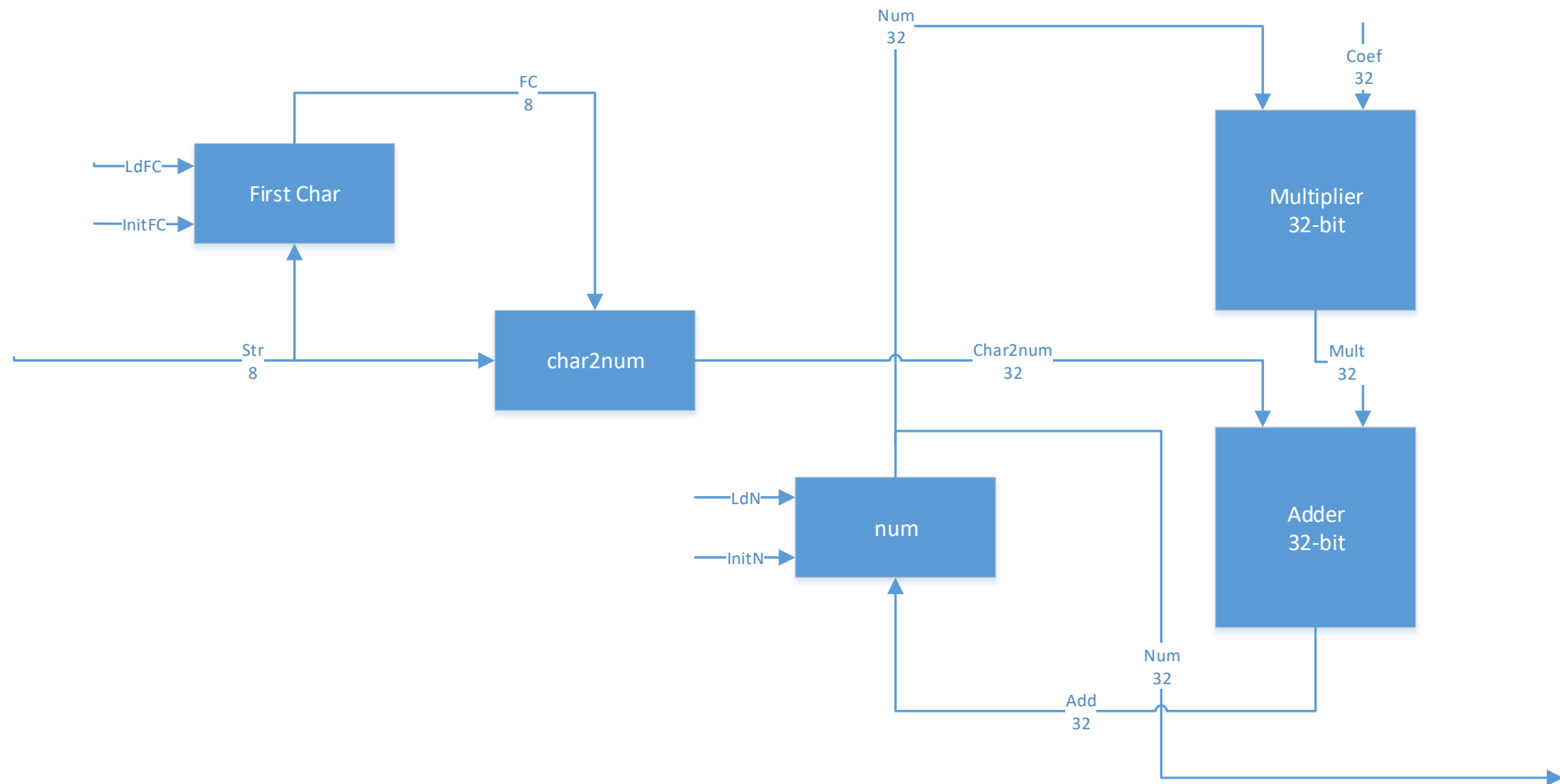
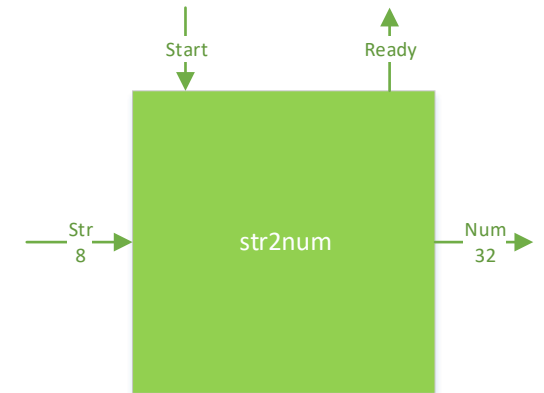
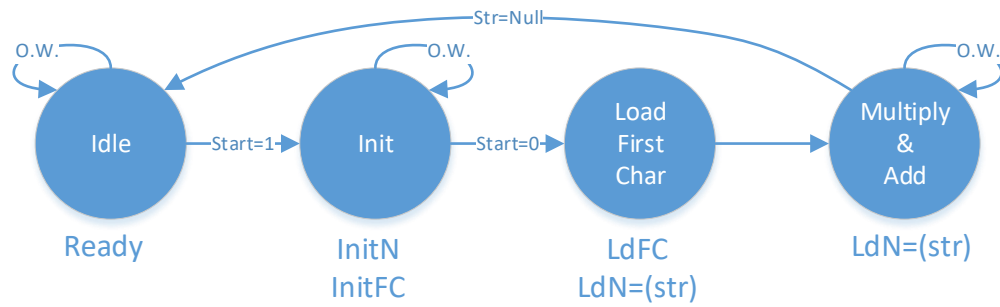


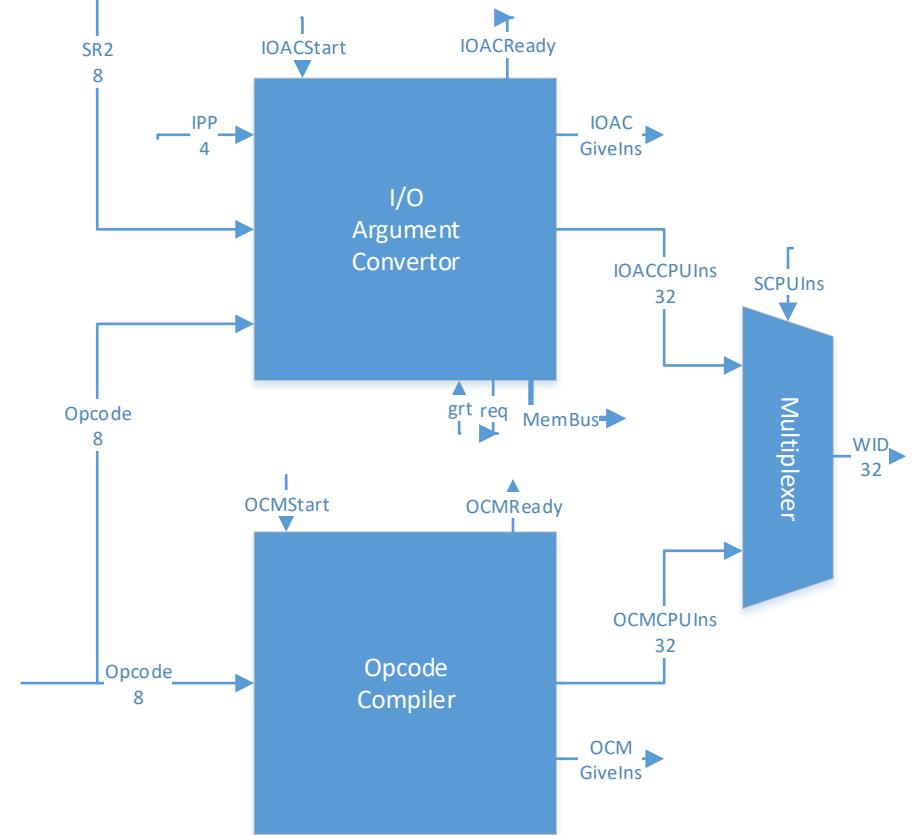
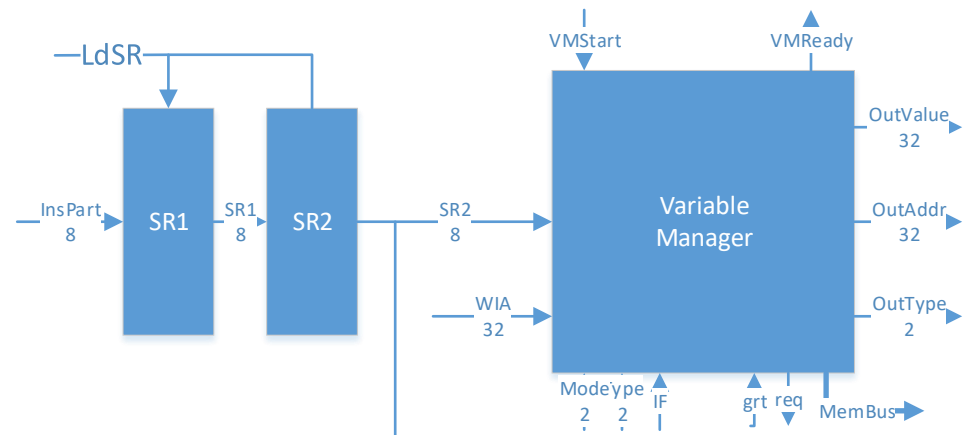


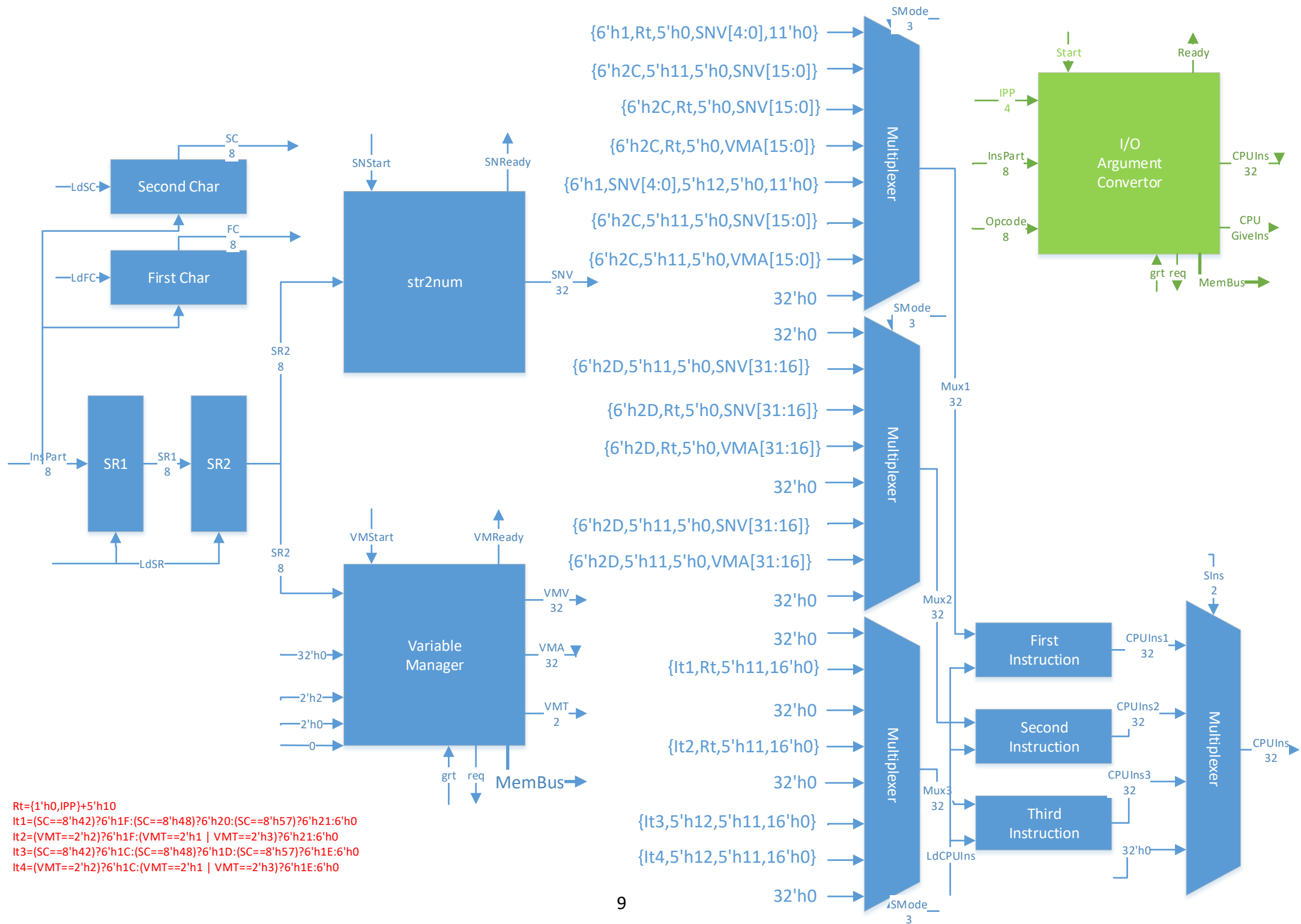


VariableManager-Controller



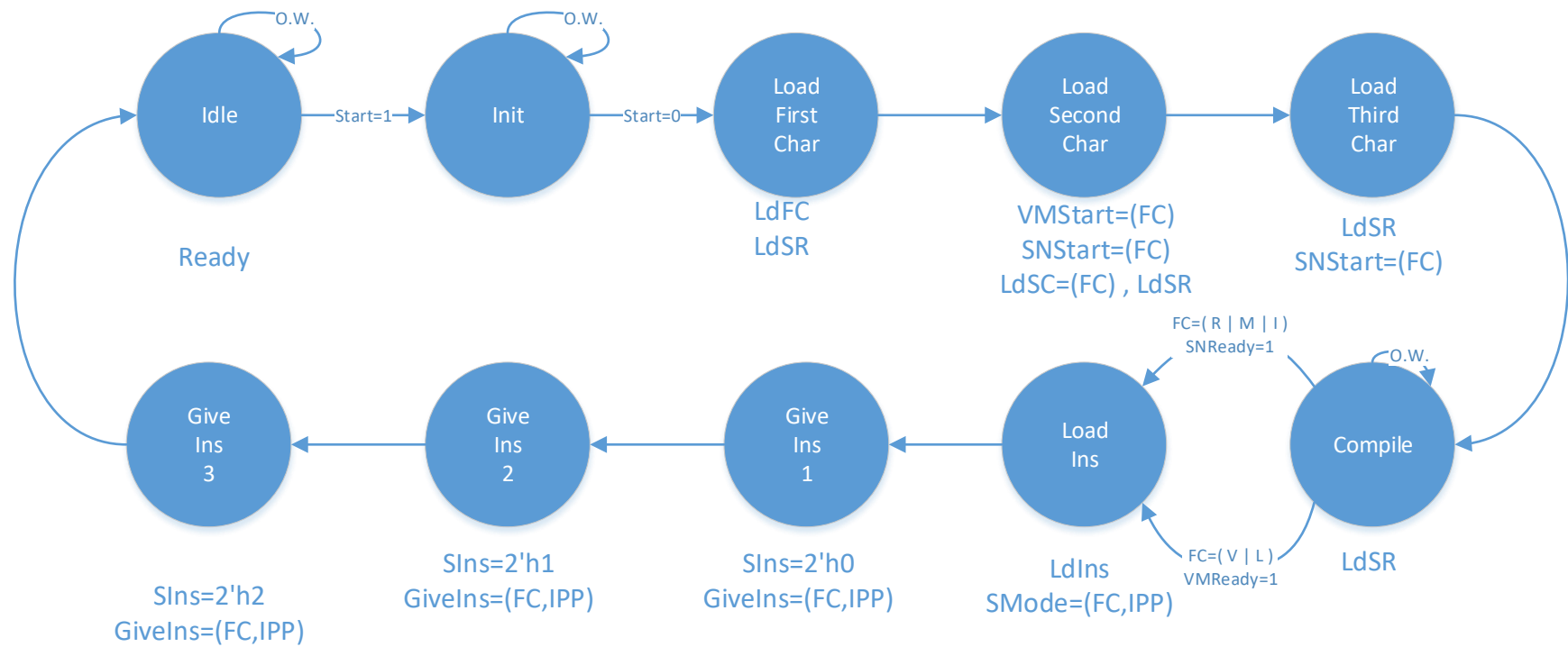


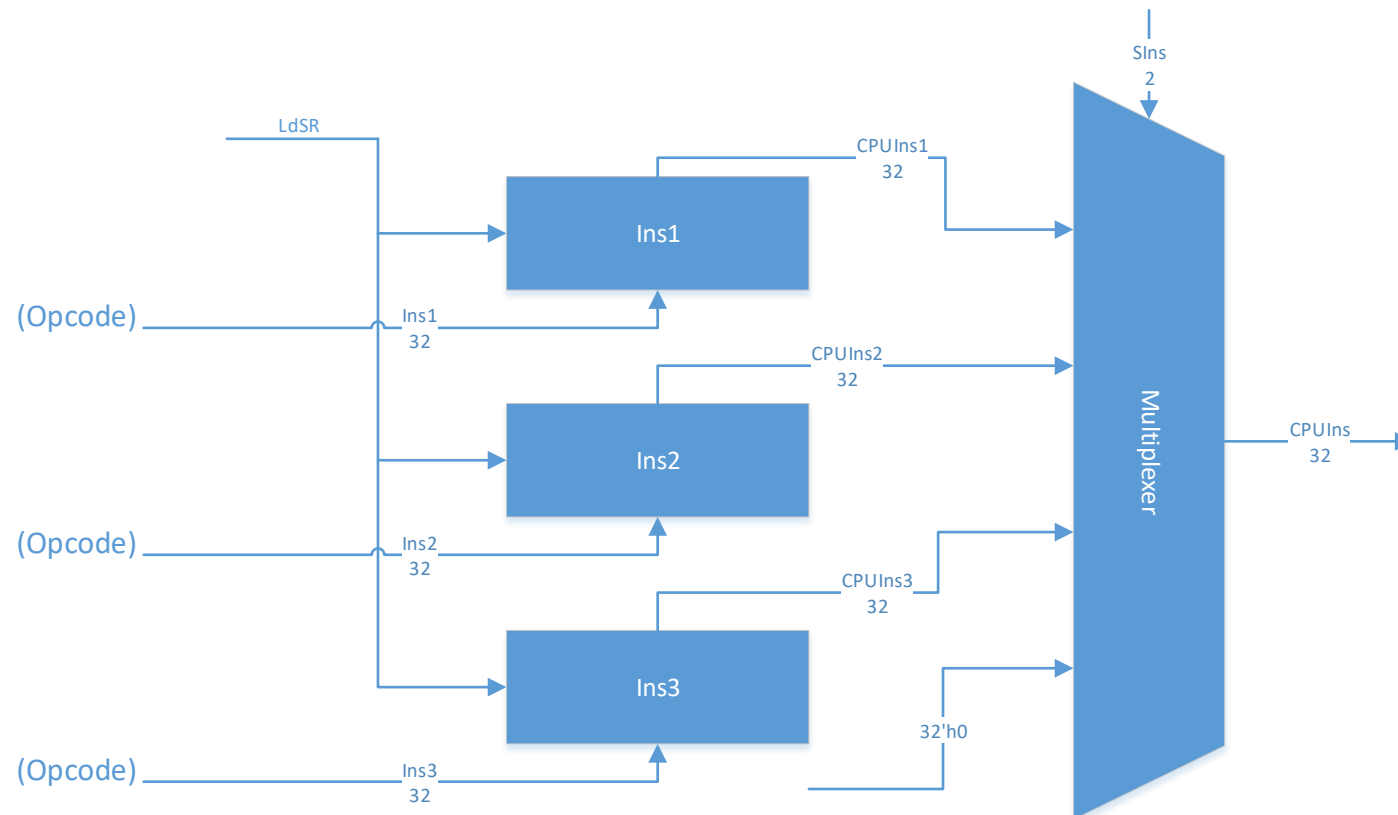
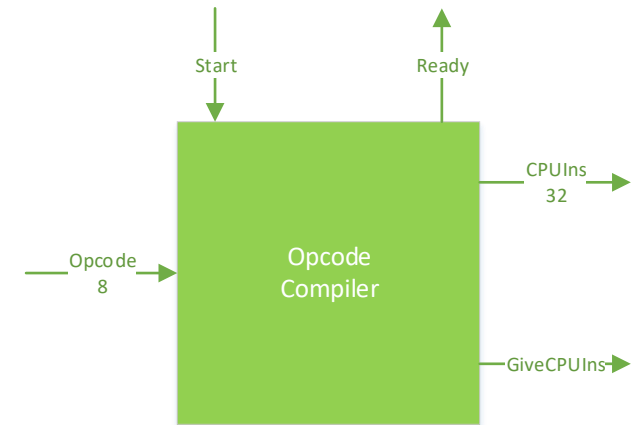
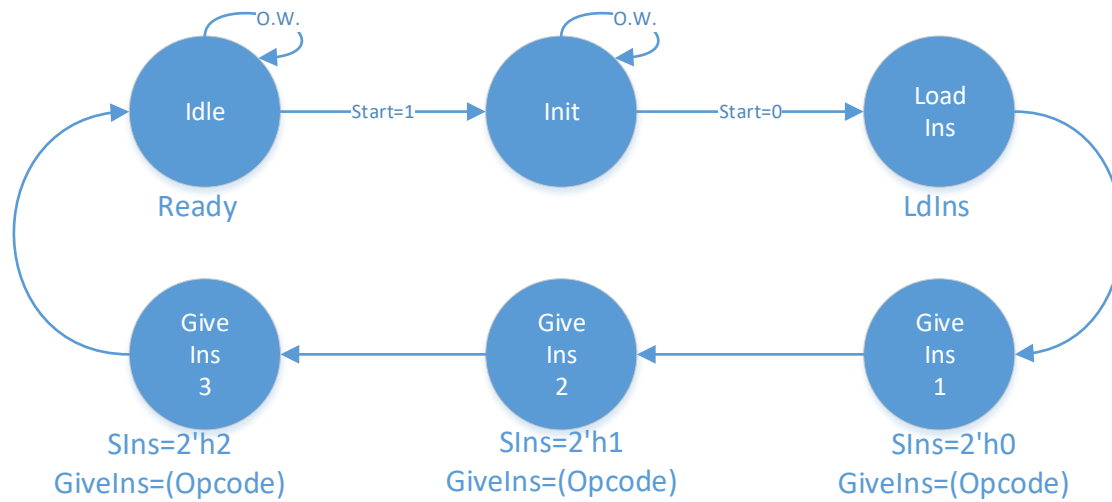


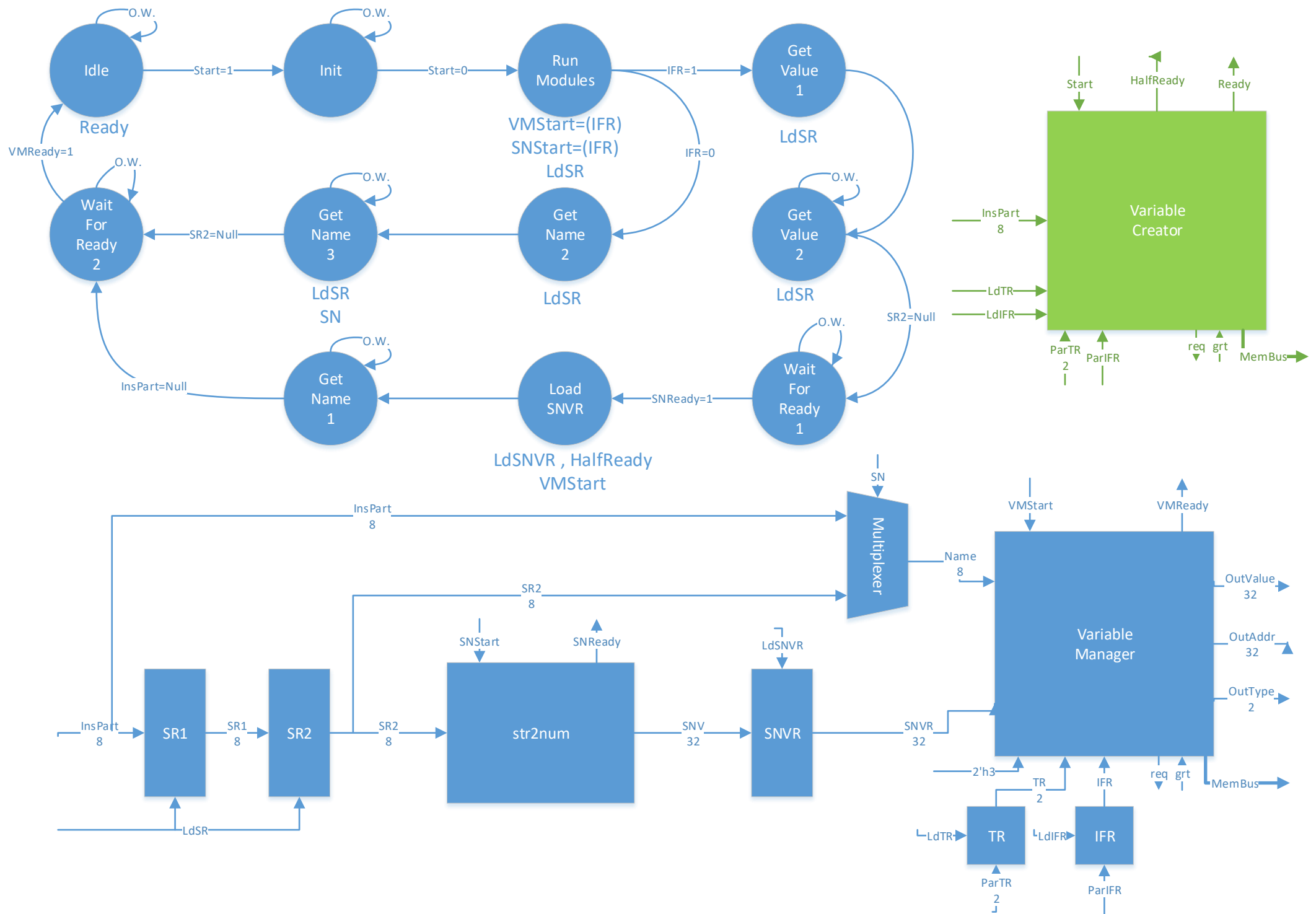


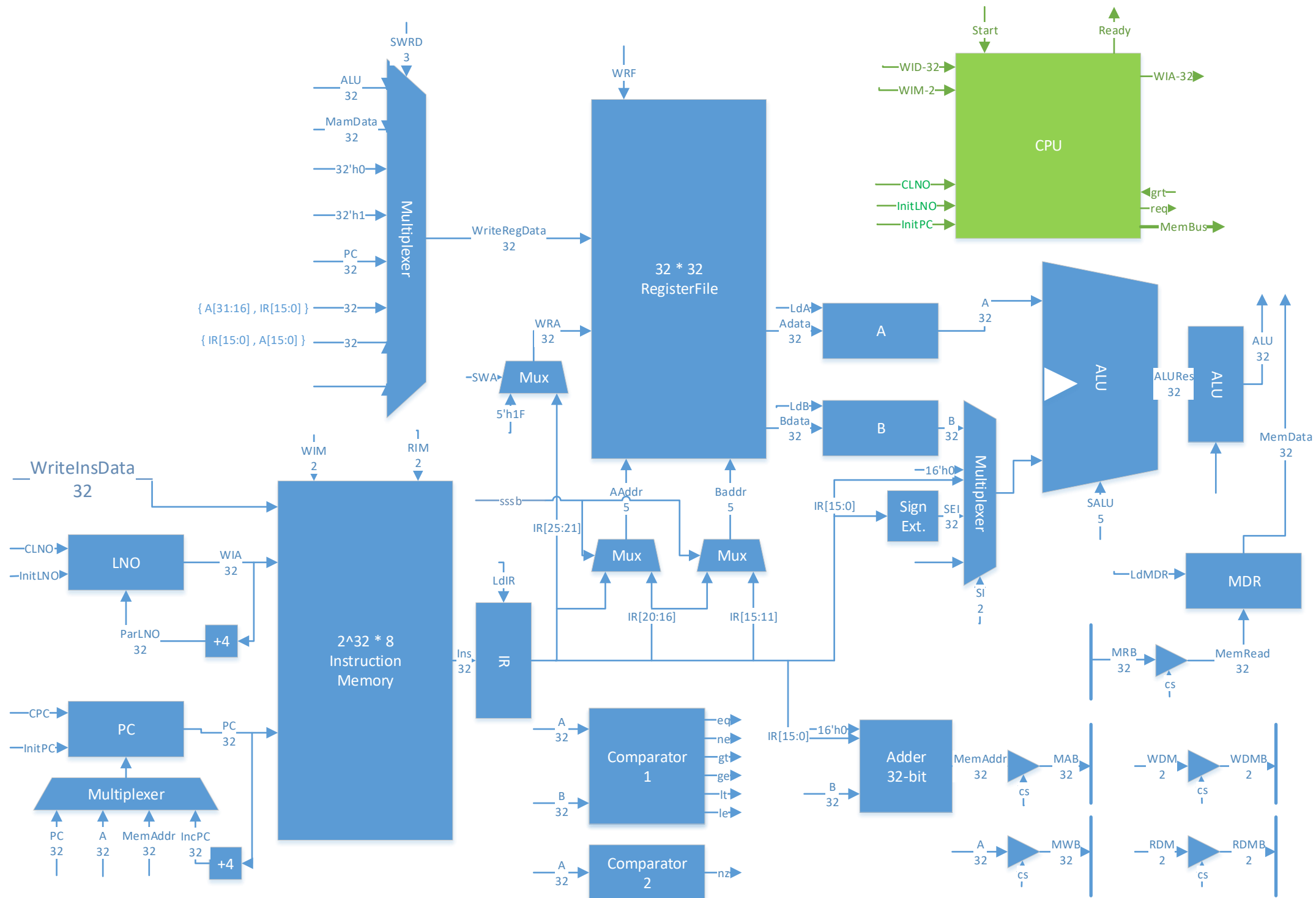
$Rt = \{1'h0, IPP\} + 5'h10$
 $lt1 = (SC == 8'h42) ? 6'h1F : (SC == 8'h48) ? 6'h20 : (SC == 8'h57) ? 6'h21 : 6'h0$
 $lt2 = (VMT == 2'h2) ? 6'h1F : (VMT == 2'h1 \mid VMT == 2'h3) ? 6'h21 : 6'h0$
 $lt3 = (SC == 8'h42) ? 6'h1C : (SC == 8'h48) ? 6'h1D : (SC == 8'h57) ? 6'h1E : 6'h0$
 $lt4 = (VMT == 2'h2) ? 6'h1C : (VMT == 2'h1 \mid VMT == 2'h3) ? 6'h1E : 6'h0$

IOAC-Controller









CPU-Controller

