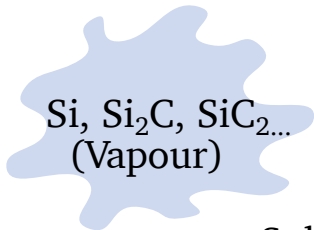


+ ΔT -



SiC source



Substrate