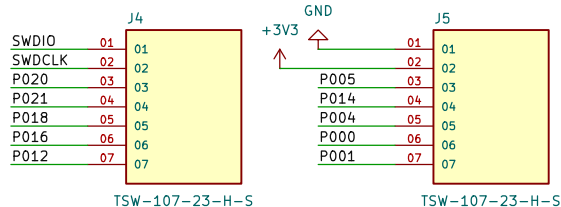
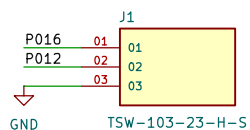


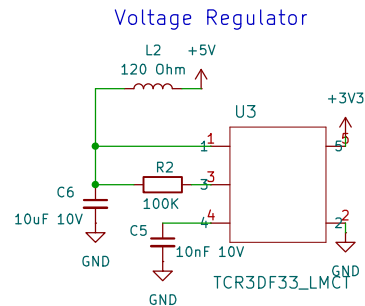
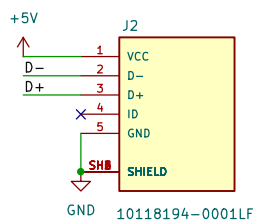
### Headers



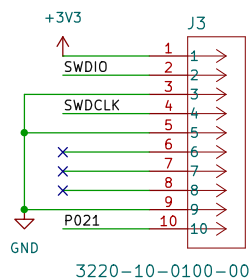
### UART Header



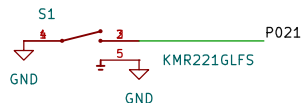
### Micro USB Port



### JTAG



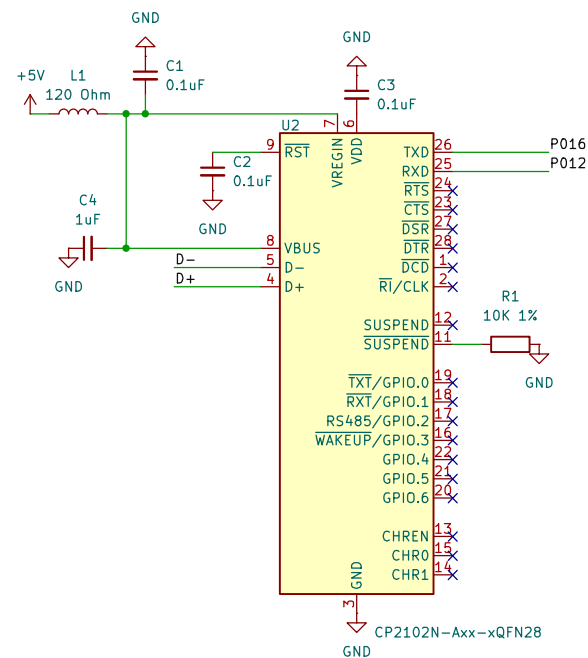
### Reset Button



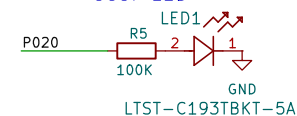
### Fiducials



### USB to UART



### User LED



Matthew T. Toplack Design

Sheet: /  
File: nrf52805\_BLE\_Board.kicad\_sch

**Title: FanstelBC805M-Based\_nRF52805\_Development\_Board**

Size: A4 Date: 2024-06-18

KiCad E.D.A. 8.0.2

Rev: A

Id: 1/1