

	Data Space	Prog Space
0x00 0000	M0 Vector RAM (Enabled if VMAP = 0)	
0x00 0040	M0 SARAM (1K x 16, 0-Wait)	
0x00 0400	M1 SARAM (1K x 16, 0-Wait)	
0x00 0800	Peripheral Frame 0	Reserved
0x00 0D00	PIE Vector - RAM (256 x 16) (Enabled if VMAP = 1, ENPIE = 1)	
0x00 0E00	Peripheral Frame 0	
0x00 1400	CLA Registers	
0x00 1480	CLA-to-CPU Message RAM	
0x00 1500	CPU-to-CLA Message RAM	
0x00 1580	Reserved	
0x00 2000	Reserved	
0x00 4000	USB Control Registers <sup>(A)</sup>	Reserved
0x00 5000	Peripheral Frame 3 (4K x 16, Protected) DMA-Accessible	
0x00 6000	Peripheral Frame 1 (4K x 16, Protected)	
0x00 7000	Peripheral Frame 2 (4K x 16, Protected)	
0x00 8000	L0 DPSARAM (2K x 16) (0-Wait, Secure Zone + ECSL, CLA Data RAM2)	
0x00 8800	L1 DPSARAM (1K x 16) (0-Wait, Secure Zone + ECSL, CLA Data RAM 0)	
0x00 8C00	L2 DPSARAM (1K x 16) (0-Wait, Secure Zone + ECSL, CLA Data RAM 1)	
0x00 9000	L3 DPSARAM (4K x 16) (0-Wait, Secure Zone + ECSL, CLA Program RAM)	
0x00 A000	L4 SARAM (8K x 16) (0-Wait, Secure Zone + ECSL)	
0x00 C000	L5 DPSARAM (8K x 16) (0-Wait, DMA RAM 0)	
0x00 E000	L6 DPSARAM (8K x 16) (0-Wait, DMA RAM 1)	
0x01 0000	L7 DPSARAM (8K x 16) (0-Wait, DMA RAM 2)	
0x01 2000	L8 DPSARAM (8K x 16) (0-Wait, DMA RAM 3)	
0x01 4000	Reserved	
0x3D 7800	User OTP (1K x 16, Secure Zone + ECSL)	
0x3D 7BFA	Reserved	
0x3D 7C80	Calibration Data	
0x3D 7CC0	Get_mode function	
0x3D 7CD0	Reserved	
0x3D 7E80	PARTID	
	Calibration Data	
0x3D 7EB0	Reserved	
0x3D 8000	FLASH (128K x 16, 8 Sectors, Secure Zone + ECSL)	
0x3F 7FF8	128-Bit Password	
0x3F 8000	Boot ROM (32K x 16, 0-Wait)	
0x3F FFC0	Vector (32 Vectors, Enabled if VMAP = 1)	

A. On non-USB devices, 0x00 4000–0x00 4FFF is Reserved.

**Figure 6-1. 28069 Memory Map**