Homework #8

Q1 – what is the difference between buffering and spooling? Where and how they are used?

Q2 - 12.5 from book (Page 525)

Q3 – Compare and contrast the role of device driver vs device controller?

Q4 . 12.6 from book (Page 525)

Q 5- How busy bit and ready bit are used in polling?

Q6 – T/F – When the DMA takes control of the bus system, it communicates directly with the CPU.

Q7 – T/F – PCI bus connects high-speed high-bandwidth device to memory subsystem and CPU.

Q8 – T/F – A process is moved to wait queue when I/O request is made with non-blocking I/O.

Q9 – T/F – “Context switching" means that the OS causes the processor to divide its attention between a series of different user processes.

Q10- T/F – I/O control drivers act as an interface for communication between the CPU and input and output peripherals.