

CSc 137
HW #4
(16 pts)

5.8. Design a Moore sequence recognizer that detects the nonoverlapping sequence "101." Use binary encoded state labels and design and draw the circuit schematic similar to the one shown in Fig. 5.16. (4 pts)

5.9. Design a Mealy sequence recognizer that detects the nonoverlapping sequence "101." Use binary encoded state labels and draw the circuit schematic similar to the one shown in Fig. 5.16. (4 pts)

5.10. Design a Moore sequence recognizer that detects the overlapping sequence "1001." Use binary encoded state labels. . (Step 1. FSD only) (4 pts)

5.11. Design a Mealy sequence recognizer that detects the overlapping sequence "1001." Use binary encoded state labels. . (Step 1. FSD only) (4 pts)