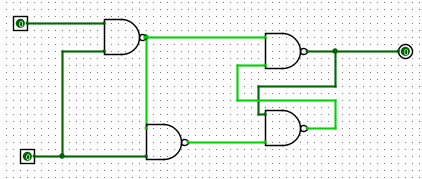
CPU designed following the Ross McGowan "Design a CPU" Course

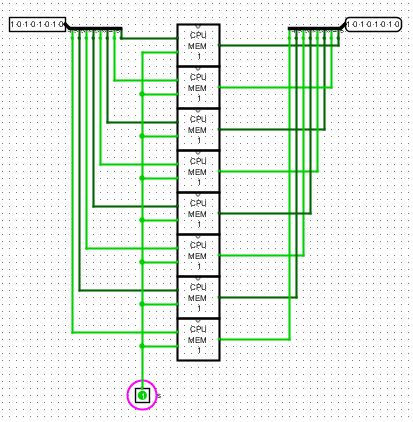
<https://www.udemy.com/course/design-a-cpu>

<https://www.buthowdoitknow.com/cpu_simulator.html>

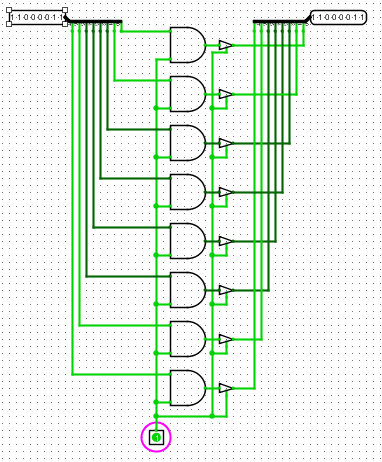
CPU\_MEM\_1



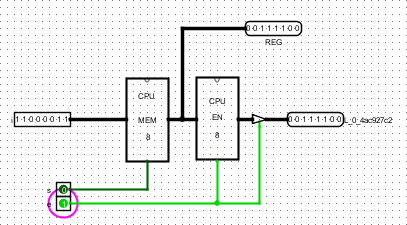
CPU\_MEM\_8



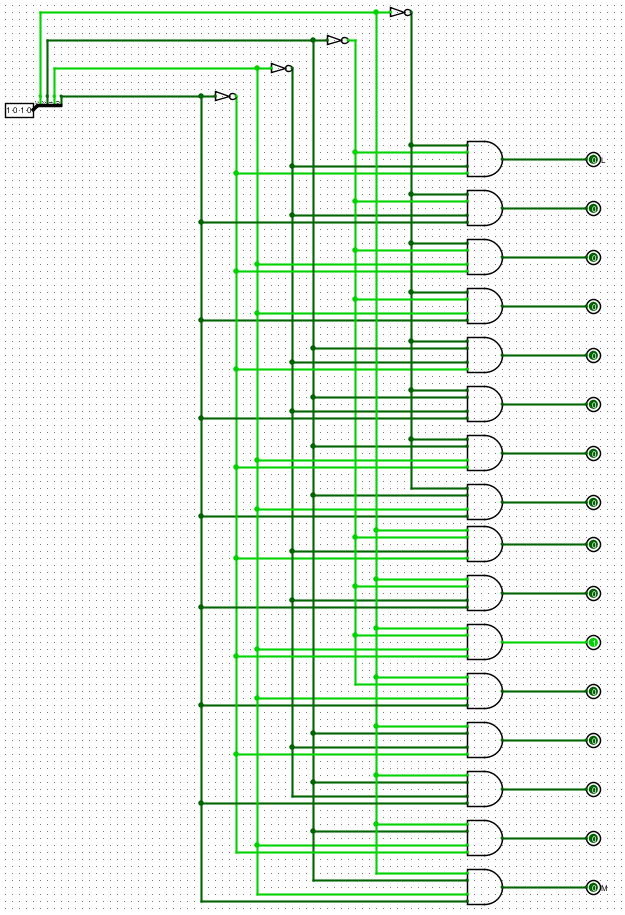
CPU\_ENABLE



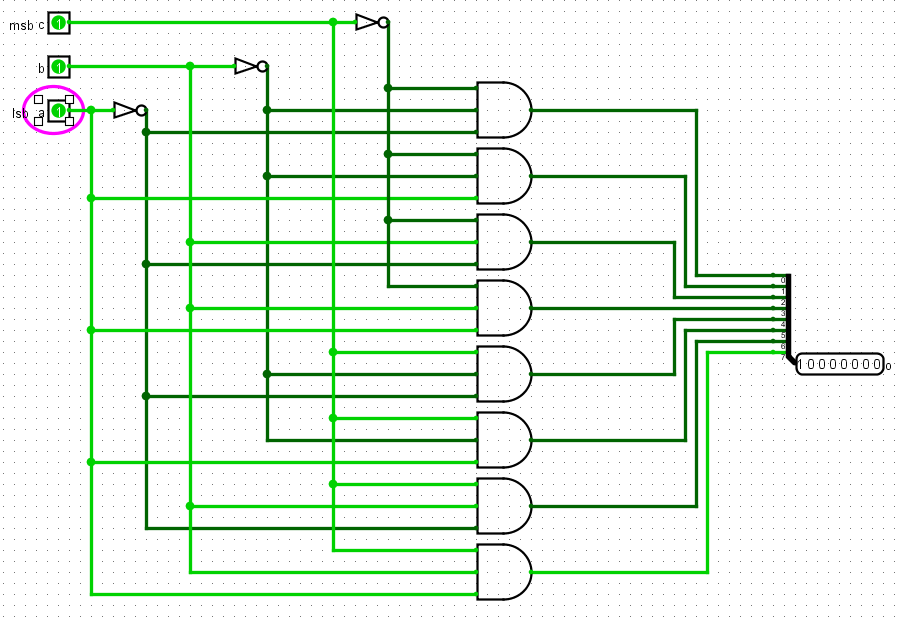
CPU\_REG\_8



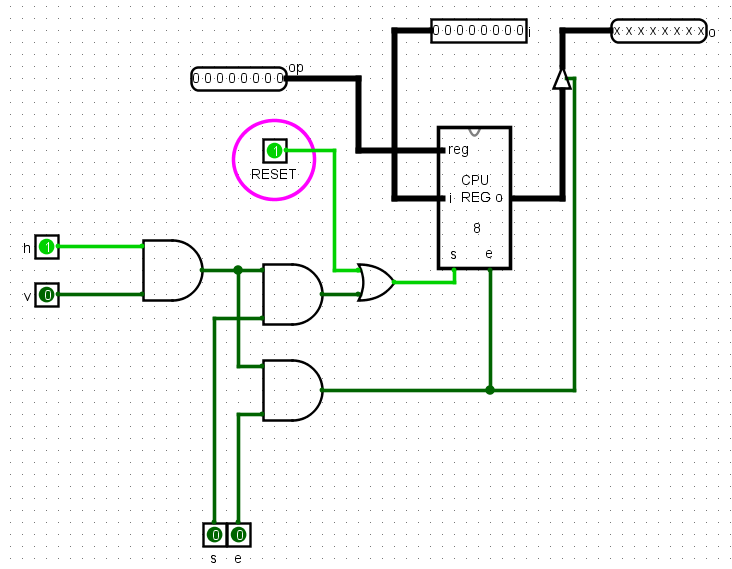
CPU\_DECODER\_4x16



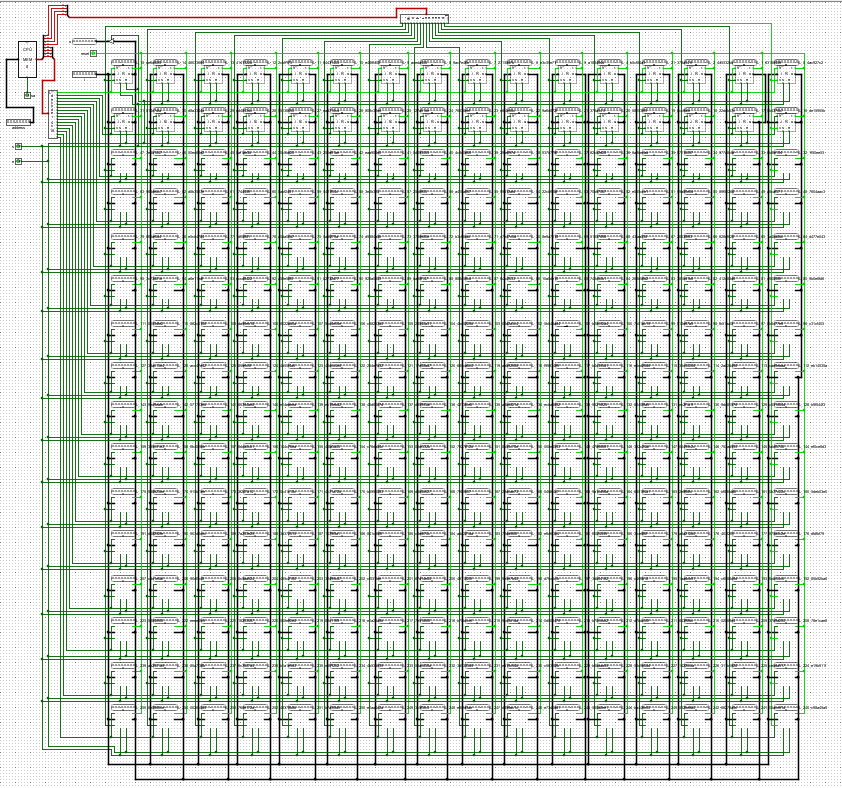
CPU\_DECODER\_3x8



CPU\_REG\_BYTE



CPU\_RAM\_256



CPU\_ADDER

