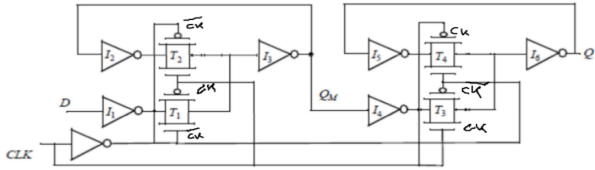
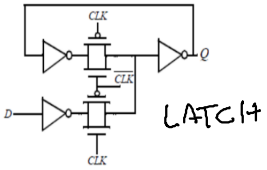


FLIP FLOP

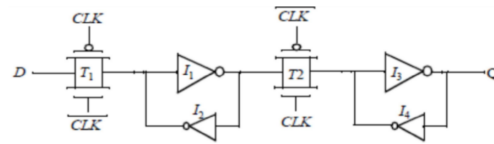


MULTIPLEXER BASED

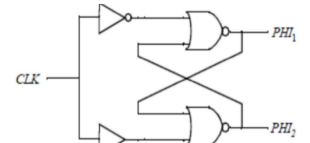


LATCH

BRUTE FORCE (RATIOED)



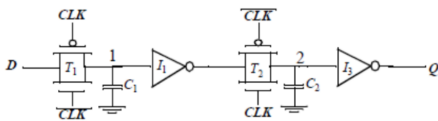
STATIC



CLK OVERLAP  
1-1 SOLVER

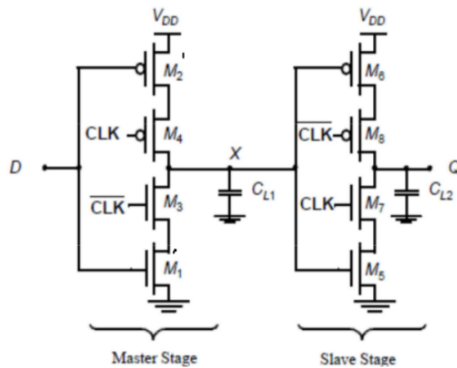
FLIP FLOPS  
TYPE OF IMPLEMENTATION

CLASSIC

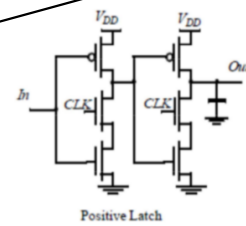


DYNAMIC

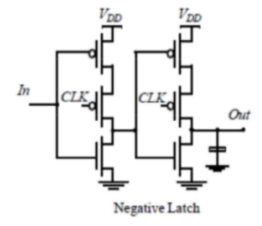
C<sup>2</sup> MOS



TSP

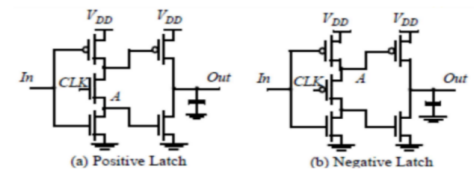


Positive Latch



Negative Latch

TSP "SPLIT-OUT"



(a) Positive Latch

(b) Negative Latch