

# **Parallel implementation of Huffman Code using native C++ threads and FastFlow library**

Parallel and Distributed Systems: Paradigms and Models

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Project Report

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# 1 Introduction

The Huffman code is an efficient lossless compression code based on the probability of each character.

To build the optimal code for a specific text we have to:

1. count the number of occurrences of each character in the text;
2. build the binary tree that represents the code;
3. encode the file.

## 2 Overview

We are facing a problem that can be divided in three stages. In particular it is a *data parallel* task, since we have all input available at the beginning of the computation.

### 2.1 Counting the number of occurrences

As stated above, the first stage is a counting one. The asymptotic sequential complexity of this part is  $\theta(m)$  where  $m$  is the number of characters in the file. From a parallel/distributer point of view this is clearly a *map-reduce* operation.

**Map** The *Map* part can be execute in parallel dividing the file into chunks, the workers count the occurrences in a chunk of the file. This operation has to deal with the disk. If we consider the reading of the disk as a sequential operation things became more difficult because it's no longer a data parallel problem but a stream parallel one. In this setting we can describe the process as `pipe(reading, farm(counting, nw))`, the completion time of this process is the time needed to read the file from the disk, under the assumption that the farm has the right number of workers not to be the bottleneck of the operation. This approach is the one that minimize both the completion time and the number of workers but it cause a lot of communication overhead, needs some tuning of the chunksize to send and of the scheduler's policy and is in general more complex to implement.

If we instead consider the reading of the disk as a data parallel operation that consists in moving data from the disk to the main memory, we can use the *Map Fusion* theorem and transform the program in `map(read-count, nw)`. This solution minimize the communication overhead, the completion time and the complexity of the implementation.

Furthermore, the tests that i did mapping the file in main memory and reading it with multiple threads, showed that the parallelization also improves the performance of the read operation. This is probably due to how the SSD works and the caching systems.

**Reduce** After the *Map* operation we end up with a number of counts vectors equal to the number of chunks the file was divided into (that in our case is equal to the number of workers). The *Reduce* operation is again a parallel one, this time each reducer takes a subset of the alphabet and sums the occurrences of each character in that subset. It's useless to have a number of workers greater than the number of different characters in the file.

## 2.2 Building the binary tree

The second stage is the building of the binary tree. This is a more difficult operation to parallelize since most of the operations are sequential. Furthermore, the complexity of this stage is  $\theta(A \times \log(A))$  where  $A$  is the number of different symbols (128), so basically it is a constant in our case. Tests showed that the time needed for this stage is completely negligible with respect to the other stages.

## 2.3 Encoding the file

The last stage is the encoding of the file. This is a *Map* operation, since each character has to be replaced with its code and written on the disk. We can make a similar reasoning as the one made for the counting stage about the *Map Fusion* theorem.

Unfortunately, the length of the final text can only be known after each character has been encoded (because the encoding of each character has a different length), so the actual writing needs a step of synchronization. I solved this problem dividing this stage in thread parts:

1. *encoding*: each worker encodes a chunk of the file.
2. *balancing*: the encoded chunks sizes are made multiple of 8 and the index where the writing should start is computed. This is a sequential synchronization step but the time needed is negligible.
3. *compressing and writing*: each worker takes a chunk of the encoded file and writes it on the disk grouping the bits in bytes.

It's fundamental to notice that the *balancing* step makes the encoded chunks independent one from another, so the *compressing and writing* can become a parallel operation.

# 3 Implementation

## 3.1 Overheads

**False Sharing** The false sharing problem is avoided since each worker writes on a completely different array: the counting arrays and the chunk-encoding arrays are allocated

by each worker.

**Heap pressure** The access to the heap is mutual exclusive, so an high number of allocation/reallocation can cause a big overhead. The problem is addressed in two ways:

- Trying to use dynamic memory management only when strictly necessary
- Use an alternative allocation library optimized for multithread applications

**Load balancing** Let's suppose a static load balancing. During the counting operation the file is equally divided between the workers i.e. each worker counts the same number of characters. In the reduce phase each worker takes an equal subset of characters and sums the occurrences. In both cases could happen that a worker has to deal with a bigger number with respect to others, but there are only  $+1$  operations that should not depend on the size of the number. In the encoding phase each worker takes a chunk to encode. This part can be really unbalanced if the original file has somewhere a lot of aligned equal characters; in fact, this character will probably have a short code and the worker that encodes that chunk has to do fewer memory reallocations.

**Synchronization** In the FastFlow implementation the synchronization is completely managed by the library. One set of threads is spawned at the beginning and the runtime support manages the queues and the implicit barriers. In the native threads implementation I had to manually manage the synchronization. The easiest way would have been to spawn and join a set of threads for each stage, each time with the assigned function and arguments. This approach would have been really simple but it would have caused a lot of overheads since from some tests on the reference machine, the creation and join of a thread takes about  $70\mu s$ , while the insertion of a task in a shared queue takes about  $1\mu s$  (and the creation of the shared queue takes  $4\mu s$ ).

## 4 Tests

The table 1 shows the time of the various stages of the sequential implementation. The great part of the time is spent on the encoding, compressing and writing phases.

In tables 2 and 3 we can see some measures of the FastFlow implementation and the native threads one. The total does not correspond to the sum of the single stages because they didn't take into account the initialization of the memory and the structures needed. The stages measure refers only to the actual computation while the "total" refers to the time from the start of the program to the end.

<b>Stage</b>	<b>Time</b>
read and count	3216762.6 (3.2 s)
huffman	102.6 (0.000102 s)
encoding	22609779.1 (22.6 s)
compressing and writing	14332681.3 (14.3 s)
<b>Total</b>	40000211 (40 s)

Table 1: Sequential times in usec for 1GB file of random characters. Averaged over 10 runs.

<b>Stage</b>	<b>Time</b>	<b>Speedup</b>
read and count	107107.8 (0.1 s)	30.02 x
huffman	77 (0.000077 s)	
encoding	1257014 (1.3 s)	17.99 x
balancing	28,7 (0.000028 s)	
compressing and writing	2582697.7 (2.5 s)	5.55 x
<b>Total</b>	4760915,7 (4.8 s)	8.40 x

Table 2: Parallel times with FastFlow implementation, in usec, for 1GB file of random characters on a 32 physical core machine. Averaged over 10 runs.

<b>Stage</b>	<b>Time</b>	<b>Speedup</b>
read and count	148591,3 (1.4 s)	21.64 x
huffman	95.7 (0.000095 s)	
encoding	1333768 (1.3 s)	16.95 x
balancing	27.2 (0.000027 s)	
compressing and writing	2867285.9 (2.8 s)	4.99 x
<b>Total</b>	4504757,6 (4.5 s)	8.87 x

Table 3: Parallel times with native threads implementation, in usec, for 1GB file of random characters on a 32 physical core machine. Averaged over 10 runs.