

ECE 2060 Lab 1

Introduction to Lab Equipment

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Abstract

This lab served as a practical to familiarize students with the instruments that Electrical and Computer Engineers use to test their designs. Students learned how to use a signal generator, oscilloscope, power supply, and Quartus FPGA software.

INTRODUCTION

Near the beginning of the semester at The Ohio State University Department of Electrical and Computer Engineering (ECE). Students in ECE 2060 Digital Design get a refresher or primer on how to use Laboratory Equipment. Students will be using the Equipment throughout the semester to measure various signals so the primer/refresher is welcome. In this paper I introduce some of the data that came from playing around with the equipment, and some of the screenshots from the oscilloscope.

PROCEDURE AND RESULTS

Students gathered the supplies they needed.¹ There are two parts to the lab. Part I explores operation of the oscilloscope and function generator. The function generator was set to 1.000 kHz, Peak to Peak Voltage of 2 Volts and a Duty Cycle of 30%.

Here is a screenshot from the oscilloscope measuring the function generator's signal:

MSO-X 2012A, MY52160561: Wed Aug 30 03:32:49 2017

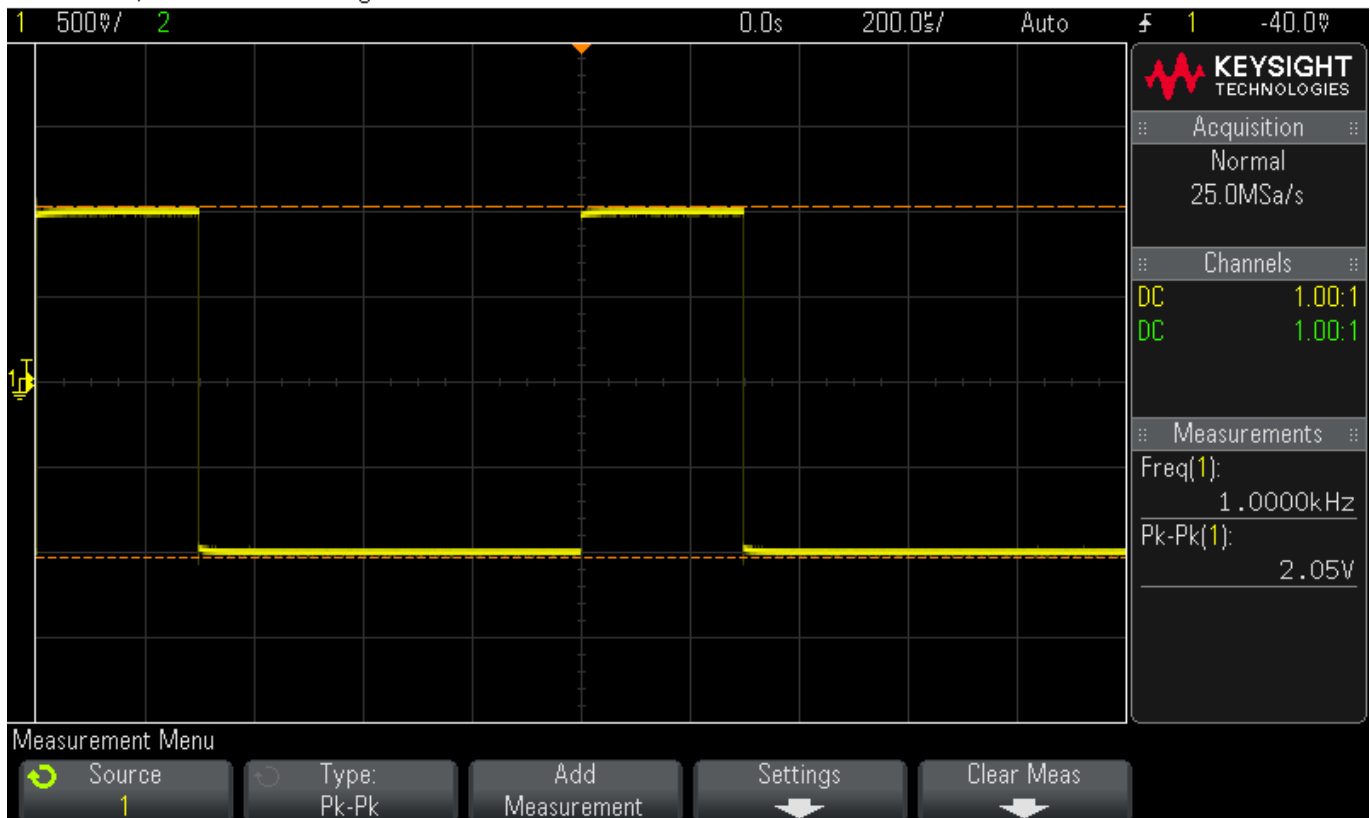


Figure 1. Function Generator Screenshot

¹See [1] the for a list of supplies used and a detailed procedure.

Figure 1 data has been summarized in Table I

Table I
FUNCTION GENERATOR DATA

Measurement	Value	Units
Horizontal Sweep	200	$\frac{\mu s}{division}$
Vertical Sweep	500	$\frac{mV}{division}$
Peak to Peak Voltage	2.05	Volts
Frequency	1.0000	kHz

The data was written into a csv and plotted in Matlab with plotData.m.

```

1 % plotData.m
2 %No editing of .csv necessary for this script
3 M1 = xlsread('scope_2.csv');
4 plot(M1(:,1), M1(:,2))
5 %Plot 1st and 2nd column in .csv
6 file title('Matlab Plot Title')
7 xlabel('X-axis Variable (usually time)')
8 ylabel('Y-axis Variable (usually voltage)')
9 %Fill in sensible values for your plot limits
10 in axis()
11 axis([xmin xmax ymin ymax])

```

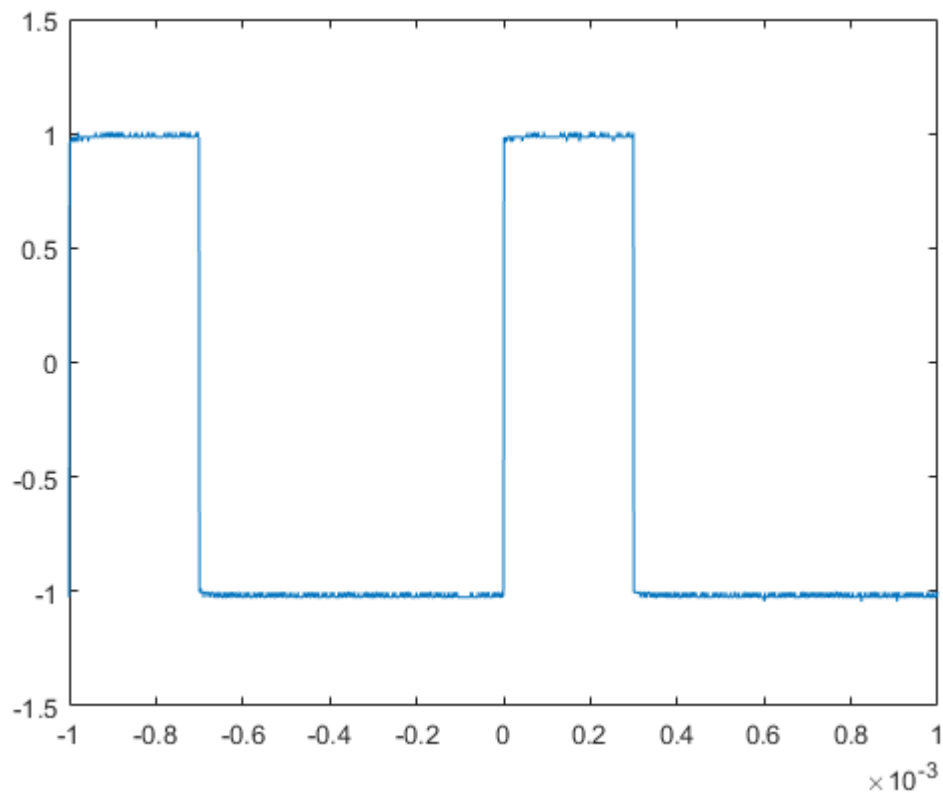


Figure 2. Function Generator Data Plot

In Figure 2 you'll notice some noise on logical high and logical low, this is expected.

The next activity was to measure the output of pin 1 on a FPGA board. First measured with the probe the screen looked like Figure 3 and data summarized in Table II.

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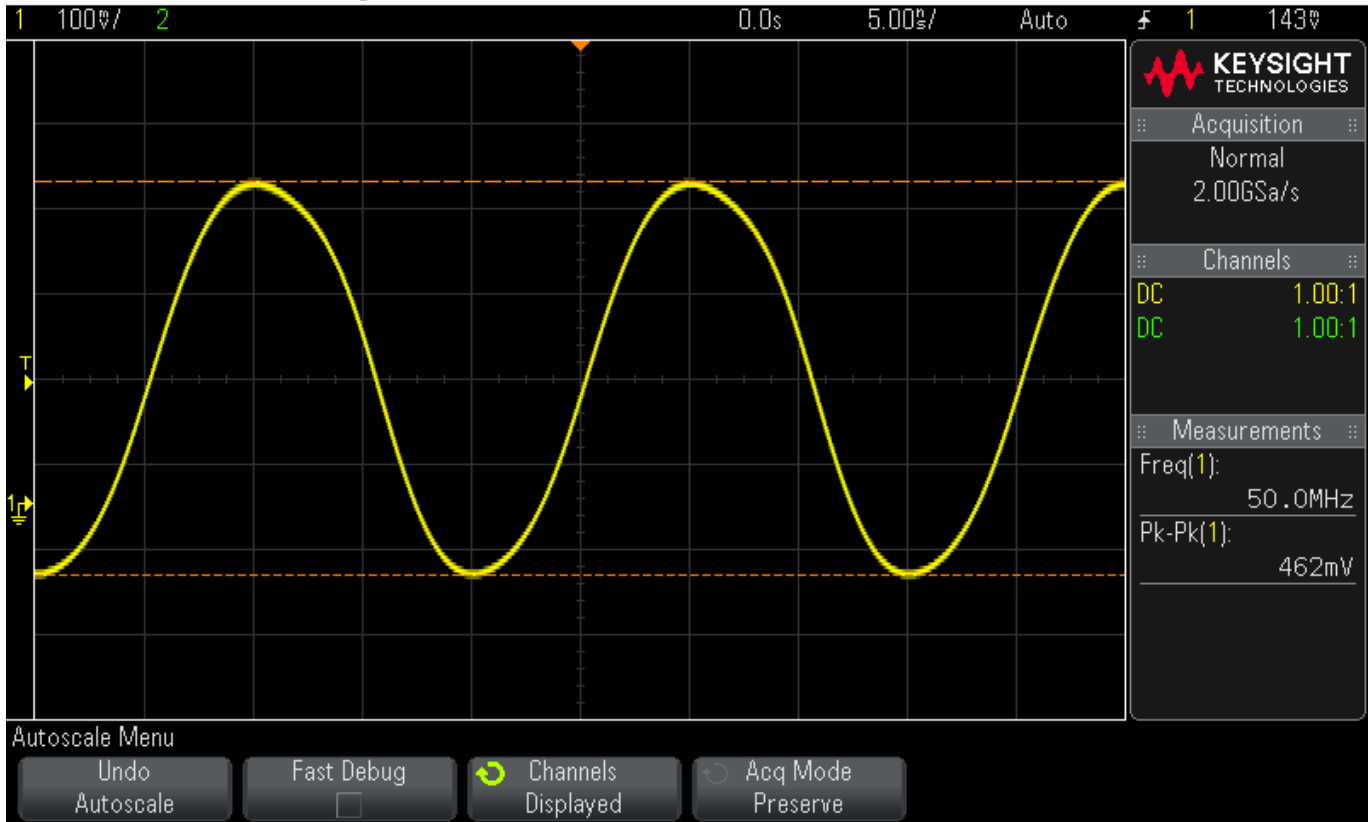


Figure 3. FPGA Analog Probe

Table II
FPGA ANALOG PROBE DATA

Measurement	Value	Units
Horizontal Sweep	5.00	$\frac{ns}{division}$
Vertical Sweep	100	$\frac{mV}{division}$
Peak to Peak Voltage	462	mV
Frequency	50.0	MHz

The analog probe was then switched to a bnc to header adapter. Data is summarized in Table III.

MSO-X 2012A, MY52160561: Wed Aug 30 03:58:52 2017

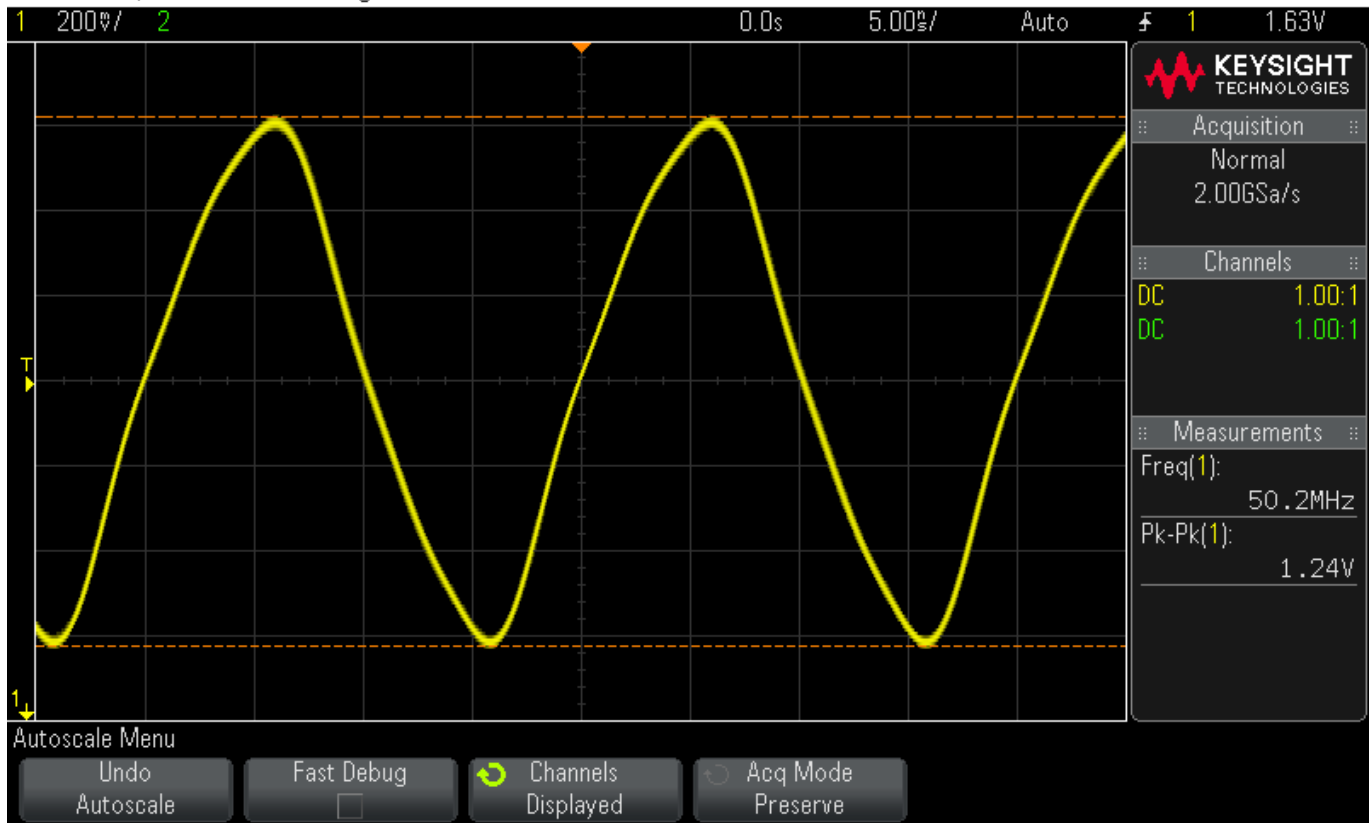


Figure 4. FPGA BNC Probe

Table III
FPGA BNC PROBE DATA

Measurement	Value	Units
Horizontal Sweep	5.00	$\frac{ns}{division}$
Vertical Sweep	200	$\frac{mV}{division}$
Peak to Peak Voltage	1.24	Volts
Frequency	50.2	MHz

The probe was then switched out for the digital probe with results in Figure 5.

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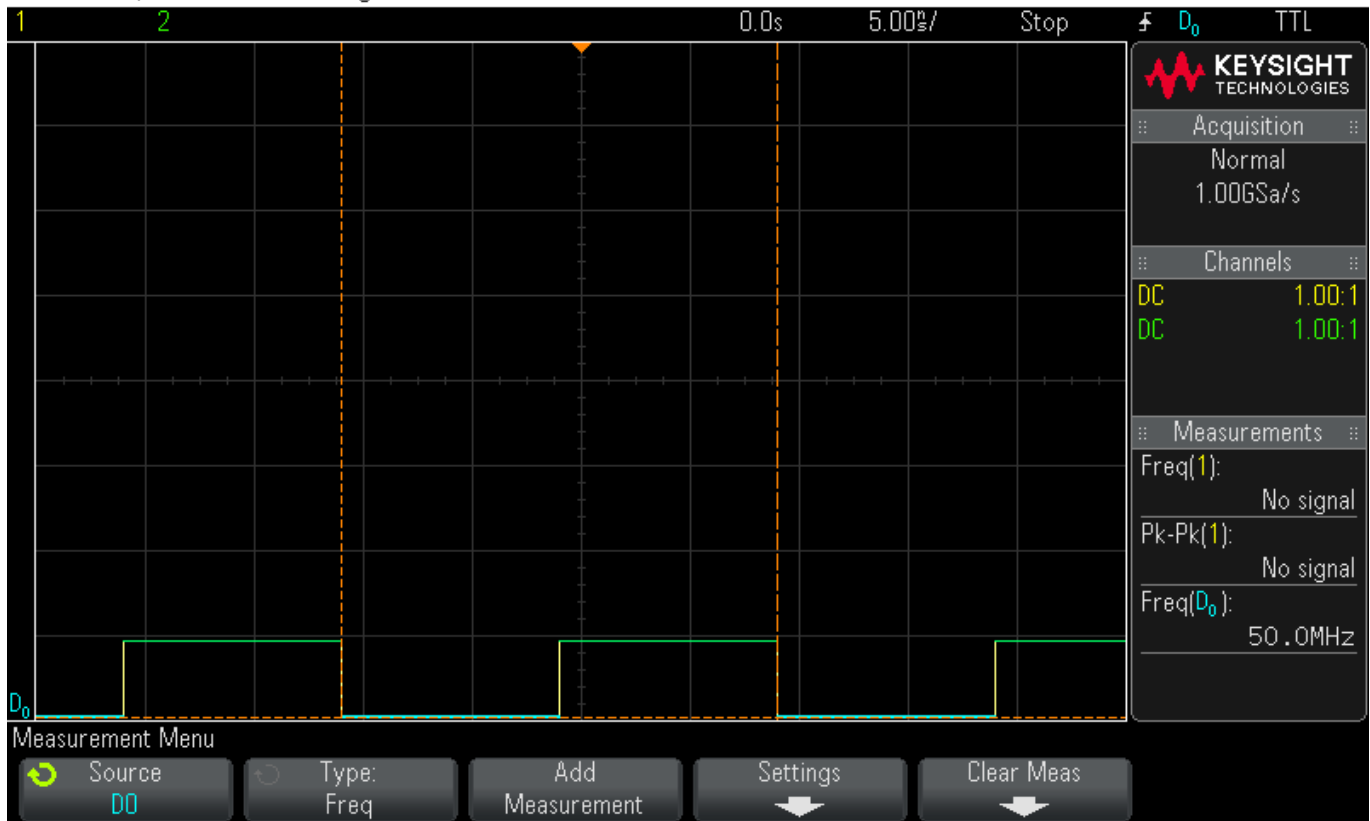


Figure 5. FPGA Digital Probe

In Figure 5 we observe a 50.0 MHz duty cycle.

DISCUSSION

The horizontal units on the oscilloscope are either nanoseconds or microseconds per division. You can change the setting by turning a knob near the top left of the oscilloscope called the "Horizontal Knob." The setting can be read near the top of the screen.

The Vertical Units on the oscilloscope are Volts or mV per division. You change the setting by turning the Vertical Amplifier knob. The setting can be read in the top right corner of the oscilloscope's screen.

The equation for period is $T = \frac{1}{f}$. With a 50MHz frequency the period is $T = \frac{1}{50 \times 10^6}$ or 20 nanoseconds.

Comparing Figure 3 to Figure 5 we see the the digital signal is much cleaner than the analog signal. This digital signal will be more useful for digital logic.

CONCLUSION

Technically all signals are analog, however one way to think about digital signals is that they are the first abstraction needed to do logic and computation. The goal of the lab was to gain experience with lab equipment and present some simple measurements, so there isn't a whole lot to conclude; however in later papers I'll present some more findings that will more interesting to the academic community.

ACKNOWLEDGEMENTS

We'd like to thank the TA's for helping us learn digital logic.

REFERENCES

- [1] G. Chapman. Introduction to the lab equipment readmefirst. [Online]. Available: <https://u.osu.edu/ece2060labs/files/2016/09/Lab-1-Introduction-to-the-Lab-Equipment-ReadMeFirst-Rev-C-qaivq5.pdf>