

ECE 2560 Quiz 4

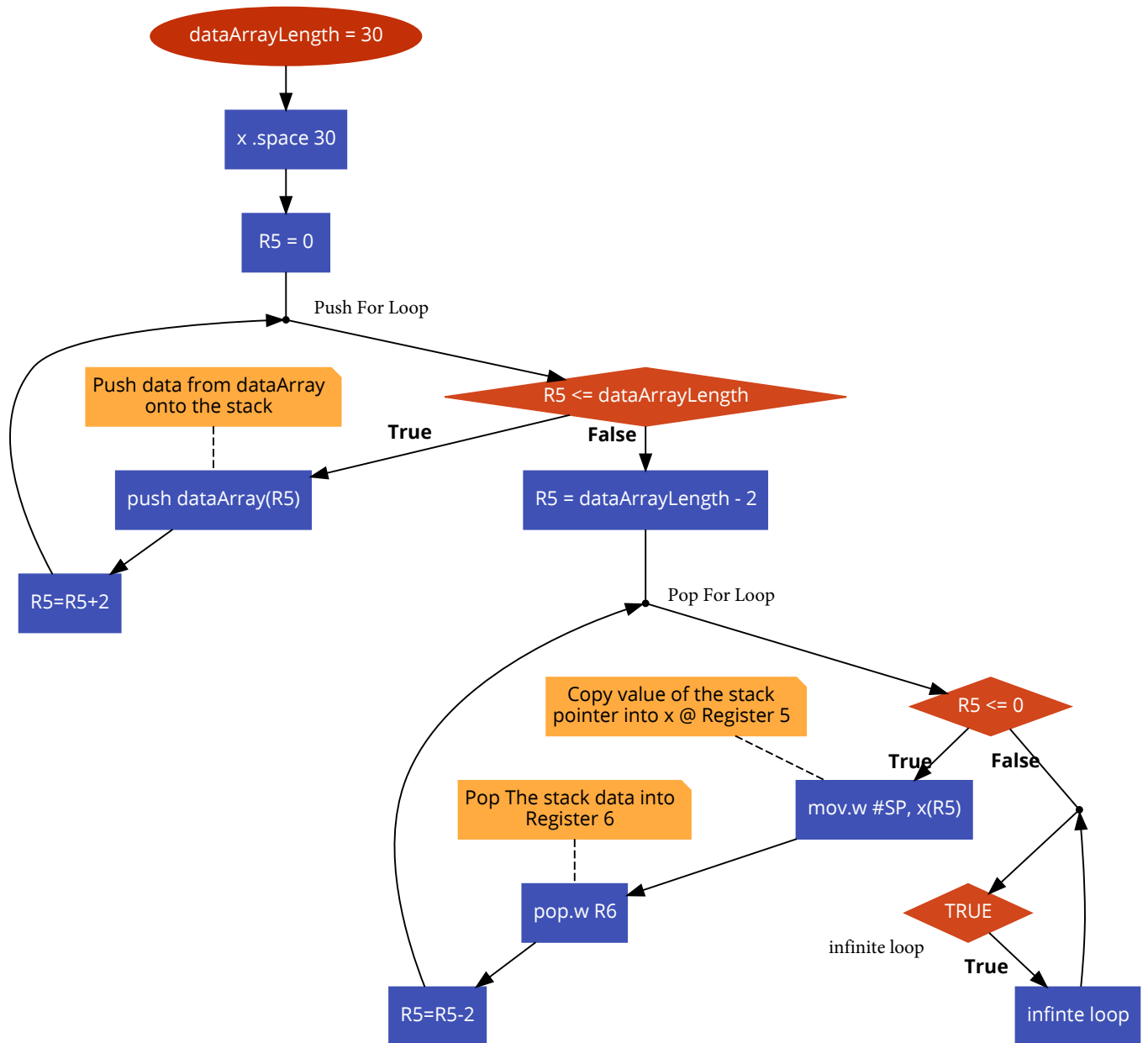
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Question 1:

```
1 |
2 | dataArrayLength = 30;
3 | x: .space 30 // Initalize 12 words of RAM
4 |
5 | for ( R5 = 0; R5 <= dataArrayLength; R5=R5+2) {
6 |     push dataArray(R5); // Push data from dataArray onto the stack
7 | }
8 |
9 | for(R5 = dataArrayLength - 2; R5 <= 0; R5=R5-2){
10 |     mov.w #SP, x(R5); // Copy value of the stack pointer into x @ Register 5
11 |     pop.w R6; // Pop The stack data into Register 6
12 | }
```

Question 2:



Question 3:

```

1 ;
2 ; MSP430 Assembler Code Template for use with TI Code Composer Studio
3 ;
4 ;
5 ;
6 ; .cdecls C,LIST,"msp430.h" ; Include device header file
7 ;
8 ;
9 ; .def RESET ; Export program entry-point to
10 ; ; make it known to linker.
11 ;
12
13 .data ; data region
14 .retain
15 .retainrefs
16 x: .space 30
17 ;
18
19 .text ; Assemble into program memory.
20 .retain ; Override ELF conditional linking
21 ; and retain current section.
22 .retainrefs ; And retain any sections that have
23 ; references to current section.
24
25 ; ----- Fixed Data in ROM Creation -----
26
27 dataArray: .word 0x1111, 0x2222, 0x3333, 0x4444, 0x5555, 0x6666, 0x7777
28 .word 0x8888, 0x9999, 0xAAAA, 0BBBB, 0CCCC, 0xDDDD, 0xEEEE
29 .word 0xFFFF
30
31 dataArrayLength: .word 0x001e
32 zero: .word 0x000
33 two: .word 0x002
34 ;
35 RESET mov.w #_STACK_END,SP ; Initialize stackpointer
36 StopWDT mov.w #WDIPW|WDTHOLD&WDTCIL ; Stop watchdog timer
37
38
39 ;
40 ; Main loop here
41 ;
42
43 mov.w &zero, R5 ; Move Zero into R5
44
45 push_for_loop:
46 cmp.w &dataArrayLength, R5 ; Compare R5 to the number 8
47 jge exit_push_for_loop ; Exit For loop
48
49 push.w dataArray(R5) ; Push Data onto the Stack
50 add.w &two, R5 ; Add 2 to R5
51 jmp push_for_loop ; Restart Loop
52 exit_push_for_loop:
53
54 sub.w &two, R5 ; Adjust R5 to (dataArrayLength - 2) to not include
55 ; _STACK_END in x
56 pop_for_loop:
57 cmp.w &zero, R5 ; Compare R5 to the number 0
58 jge istrue ; Jump to is true if R5 >= 0
59 jmp exit_pop_for_loop ; Exit Loop if R5 <= 0
60 istrue:
61 mov.w SP, x(R5) ; Move SP onto Heap (x)
62 pop.w R6 ; Pop Stack Data into R6
63
64 sub.w &two, R5 ; Decrement R5 by 2
65 jmp pop_for_loop ; Restart Loop

```

```
66 exit_pop_for_loop :
67
68
69 loop:      jmp    loop;
70
71 ;-----
72 ; Stack Pointer definition
73 ;-----
74         .global __STACK_END
75         .sect    .stack
76
77 ;-----
78 ; Interrupt Vectors
79 ;-----
80         .sect    ".reset"                ; MSP430 RESET Vector
81         .short   RESET
```

Registers	
Name	Value
Core Registers	
PC	0xC072
SP	0x0400
SR	0x0005
V	0
SCG1	0
SCG0	0
OSCOFF	0
CPUOFF	0
GIE	0
N	1
Z	0
C	1
R3	0x0000
R4	0x0FF7
R5	0xFFFFE
R6	0x1111
R7	0xFCF7
R8	0xFEED
R9	0x4B6F
R10	0x0000
R11	0xB7FB
R12	0x545F
R13	0xF78E
R14	0xFBFF
R15	0x8FFF
Special Function	