Matthew Gillespie

matt.gillespie24@gmail.com
856.745.3184

Skills

Languages
JavaScript, TypeScript, Python, .NET, HTML, CSS, C, SQL
Frontend Technologies
Angular, Vue.js, Embedded JavaScript (EJS), Bootstrap
Backend Technologies
Node.js, Express, REST, Cloud Foundry, Ubuntu, PM2
Database Technologies
MongoDB and Mongoose, SQLite, Access

Work Experience

Siemens Industry Inc. **January 2017 – March 2020**

Full-Stack Engineer **January 2018 – March 2020**
Jersey City, New Jersey

- Owned the development of the Siemens Edge Data Collector Unit (DCU), an IoT Gateway
  - Led feature development for the Edge DCU
  - Developed a Node.js library to connect the Edge DCU to the Siemens cloud platform, MindSphere
  - Reworked the Edge DCU's project structure to leverage asynchronous features of Node.js
  - Expanded the Edge DCU's front-end display using HTML, CSS, Bootstrap, and Embedded JavaScript (EJS)
  - Created a reliable production environment for the Edge DCU using Ubuntu Server and PM2
  - Created future implementation strategies for Agile, SCRUM, and/or DevOps within development group
- Led the development of a cloud hosted building management application
  - Front-End: Vue.js and Vuex
  - Back-End: Node.js, Mongoose, and MongoDB
- Developed Cloud Foundry hosted applications to visualize power distribution data in MindSphere
- Represented Siemens during customer installations and exceeded customer expectations

ELDP (Leadership Program) Rotation 2: Digital Manufacturing Engineer **July 2017 – January 2018**
Grand Prairie, Texas

- Developed a Windows Form Application using .NET in order track manufacturing progress
- The application does the following:
  - Interfaces with SAP and tracks labor for sales orders
  - Provides the line supervisor visual feedback in order to optimize manufacturing process

ELDP (Leadership Program) Rotation 1: Electronics R&D Engineer **January 2017 – July 2017**
Norcross, Georgia

- Developed a test algorithm using C in order to optimize a production line
- The algorithm does the following:
  - Replaces In-Circuit Testing (ICT) and reduces production test time by >90%
  - Reduces the number of required test points, which allows for PCB size reduction and cost savings

Education

The Pennsylvania State University, University Park, Pennsylvania Cumulative GPA: 3.70/4.00
Bachelor of Science, College of Engineering
Major: Electrical Engineering
Bachelor of Arts, College of Liberal Arts
Major: German Graduated: December 2016