

601.445/601.645: Practical Cryptographic Systems

Side Channel Attacks

Instructor: Matthew Green

Housekeeping:

- Grades for A1 back after class
- A2 ongoing
- Weekly HW assignment given out tonight

Current Events

2nd Death Near Seattle Adds to Signs Virus Is Spreading in U.S.

Officials see growing indications that the coronavirus has been spreading undetected for weeks. A cluster of cases at one nursing home have made Kirkland, Wash., a focus of concern.



C

IOTA Going Back Online in Ten Days

After a two-week network outage, the IOTA Foundation has released a fix to its wallet that will allow users to move their cryptocurrency to safety.



Current Events

Cryptology ePrint Archive: Report 2020/241

Separate Your Domains: NIST PQC KEMs, Oracle Cloning and Read-Only Indifferentiability

Mihir Bellare and Hannah Davis and Felix Günther

Abstract: It is convenient and common for schemes in the random oracle model to assume access to multiple random oracles (ROs), leaving to implementations the task (we call it oracle cloning) of constructing them. The first part of the paper is a case study of oracle cloning in KEM submissions to the NIST Post-Quantum Cryptography standardization process. We give key-recovery attacks on some submissions arising from mistakes in their implementations. We also find other submissions using oracle cloning methods whose validity is unclear. Motivated by this, the second part of the paper gives a theoretical treatment of oracle cloning. We give a definition of what is an "oracle cloning method," formalize domain separation, and specify and study many oracle cloning methods, including common domain-separating ones, giving some general results to justify (prove read-only indifferentiability of) certain classical cloning methods. We are not only able to validate the oracle cloning methods used in many of the unbroken NIST PQC KEMs, but also able to specify and validate oracle cloning methods that may be useful beyond that.

Category / Keywords: Post-Quantum Cryptography, NIST, Key Encapsulation, Public-Key Encryption, Random Oracles, Domain Separation, Indifferentiability, Composition

Current Events

- Titan M chip in Android phones

Here's what the key generation code for that looks like in Aegis. Excerpt from KeyStoreHandle.java:

```
1 KeyGenerator generator = KeyGenerator.getInstance(KeyProperties.KEY_ALGORITHM_AES,  
2 generator.init(new KeyGenParameterSpec.Builder(id, KeyProperties.PURPOSE_ENCRYPT |  
3 .setBlockModes(KeyProperties.BLOCK_MODE_GCM)  
4 .setEncryptionPaddings(KeyProperties.ENCRYPTION_PADDING_NONE)  
5 .setUserAuthenticationRequired(true)  
6 .setRandomizedEncryptionRequired(true)  
7 .setKeySize(CryptoUtils.CRYPTO_AEAD_KEY_SIZE * 8)  
8 .build());
```

Current Events

```
1 diff --git a/app/src/main/java/com/beemdevelopment/aegis/crypto/KeyStoreHandle.java
2 index cfale57..00cae52 100644
3 --- a/app/src/main/java/com/beemdevelopment/aegis/crypto/KeyStoreHandle.java
4 +++ b/app/src/main/java/com/beemdevelopment/aegis/crypto/KeyStoreHandle.java
5 @@ -57,6 +57,7 @@ public class KeyStoreHandle {
6         .setEncryptionPaddings(KeyProperties.ENCRYPTION_PADDING_NONE)
7         .setUserAuthenticationRequired(true)
8         .setRandomizedEncryptionRequired(true)
9 +
10        .setIsStrongBoxBacked(true)
11        .setKeySize(CryptoUtils.CRYPTO_AEAD_KEY_SIZE * 8)
12        .build());
```

To test this change, I went through Aegis' setup process, set a password and enabled biometric

Current Events

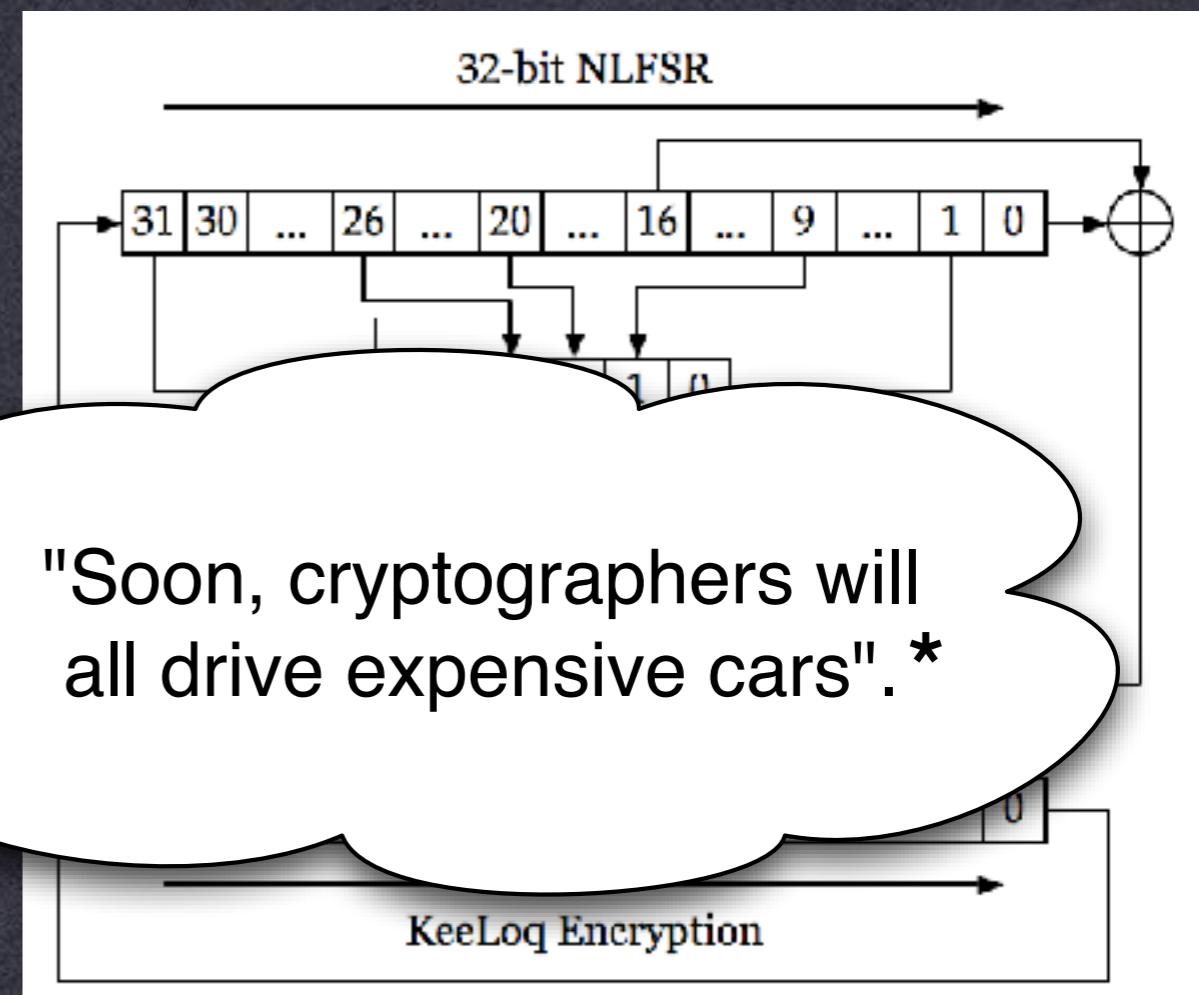
```
1 com.beemdevelopment.aegis.db.slots.SlotIntegrityException: javax.crypto.AEADBadTa
2     at com.beemdevelopment.aegis.db.slots.Slot.getKey(Slot.java:57)
3 ...
4 Caused by: javax.crypto.AEADBadTagException
5     at android.security.keystore.AndroidKeyStoreCipherSpiBase.engineDoFinal(Andrc
6     at javax.crypto.Cipher.doFinal(Cipher.java:2055)
7 ...
8 Caused by: android.security.KeyStoreException: Signature/MAC verification failed
9     at android.security.KeyStore.getKeyStoreException(KeyStore.java:1292)
10    at android.security.keystore.KeyStoreCryptoOperationChunkedStreamer.doFinal(K
11    at android.security.keystore.AndroidKeyStoreAuthenticatedAESCipherSpi$BufferA
12    at android.security.keystore.AndroidKeyStoreCipherSpiBase.engineDoFinal(Andrc
13    at javax.crypto.Cipher.doFinal(Cipher.java:2055)
14 ...
```

Current Events

```
1 plaintext: 746869732069732061207465737420737472696e67
2 ciphertext: d62a2349d993632dddabc30a4a2c8ab7ba2608c5f2
3 tag: fd6b38cba35ea579918f3b5ec1863e4b
4 nonce: f102f60a0ef39e310c5f9c4c
5 decrypted: 746869732069732061207465737420737472696e67
6
7 plaintext: 746869732069732061207465737420737472696e67
8 ciphertext: 0dd0adde0dd0adde0dd0adde0dd0adde585301005d
9 tag: 995d100cc5d068a83e7ecf13c49e92eb
10 nonce: cffb9148cb5154232c957ab7
```

KeeLoq

- Designed by Kuhn & Smit
 - Block cipher: 64-bit key, 32-bit block
 - 528 rounds
 - Mostly used for door opening
 - Direct attacks*:
 - A. 2^{16} data & 2^{51} operations
 - B. 2^{32} data & 2^{27} operations
 - C. Indesteege et al.:
(65 min to get data,
218 CPU days to process)

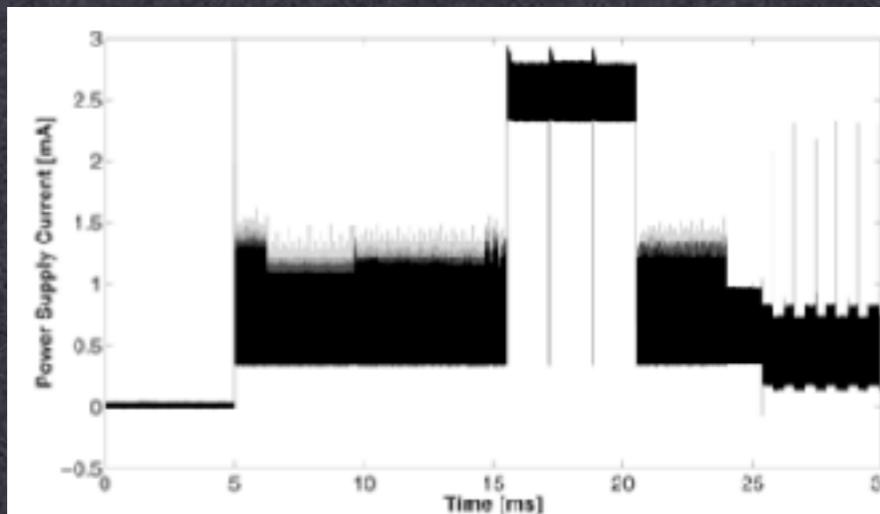


KeeLoq

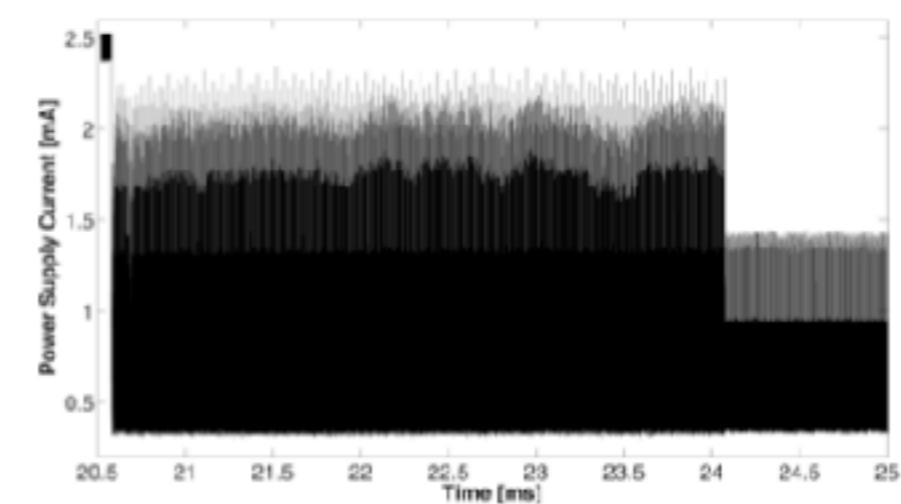
- **Newer attack, CRYPTO 2008**
 - **Requires physical access to device**
 - **Recovers secret key after 10-60 reads**
 - **How?**

KeyLoq

- CRYPTO 2008
 - Doesn't directly attack the cipher
 - Instead, measures how the cipher works in operation.
 - In this case, use power consumption

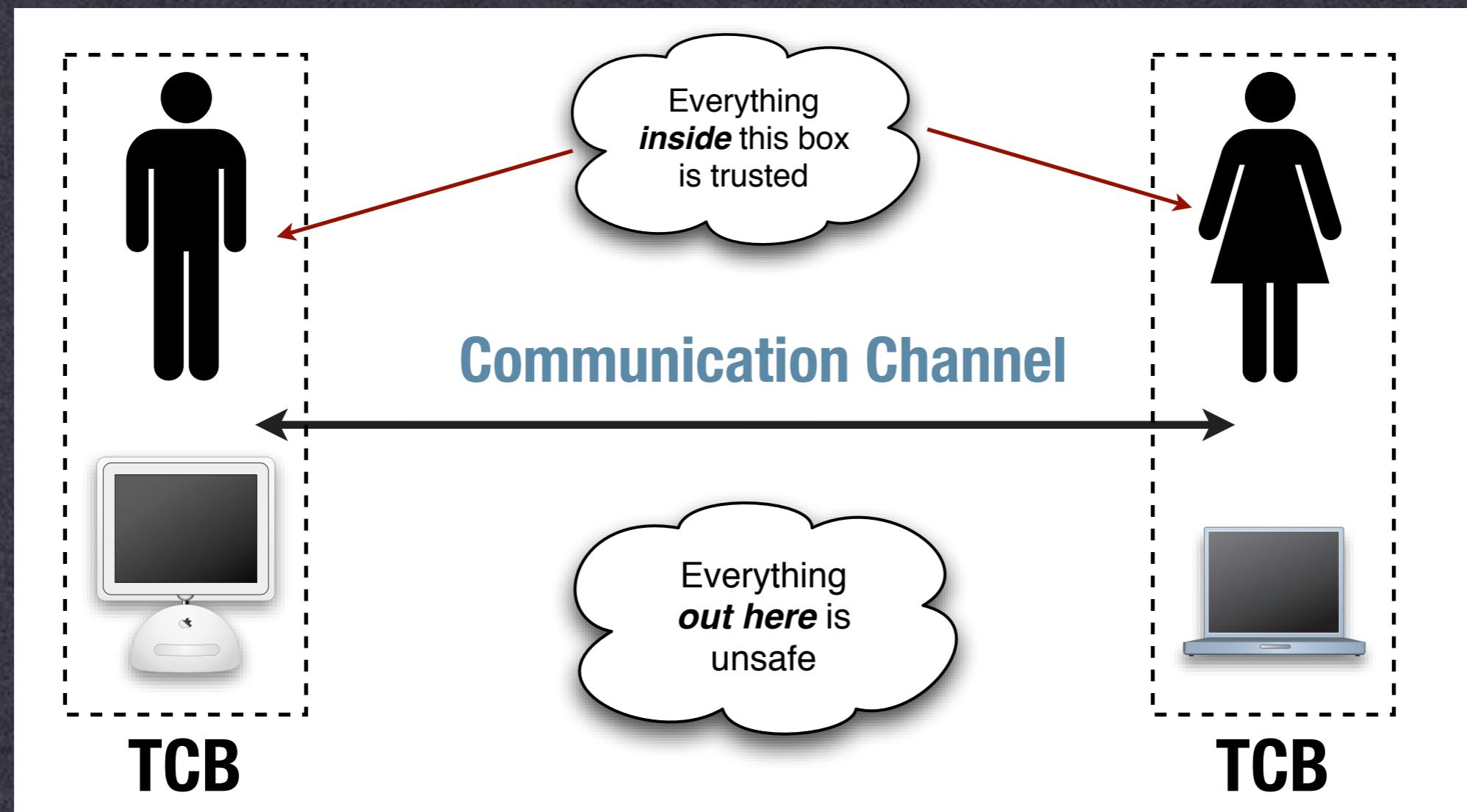


(a) From power up to start sending

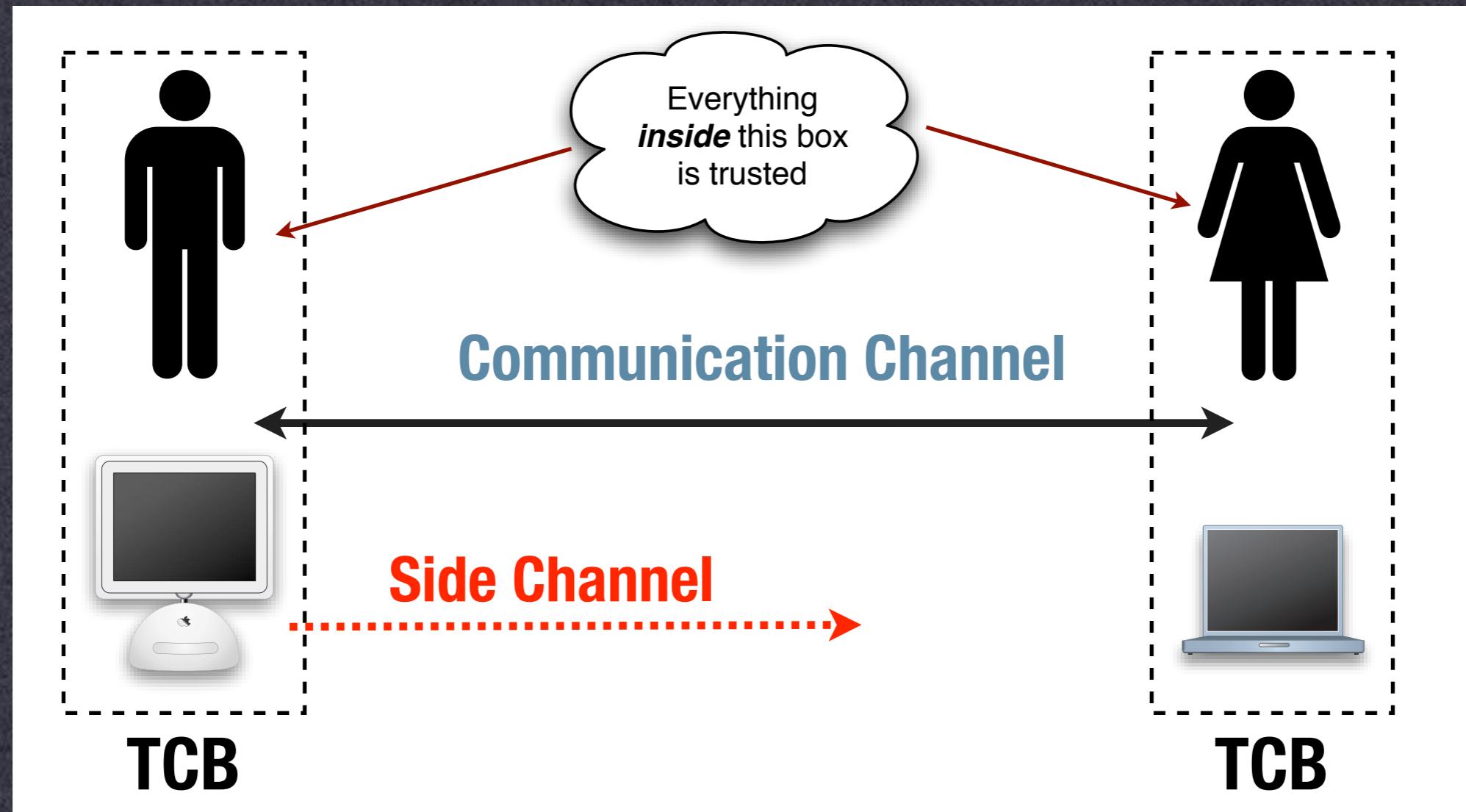


(b) Encryption part

Review

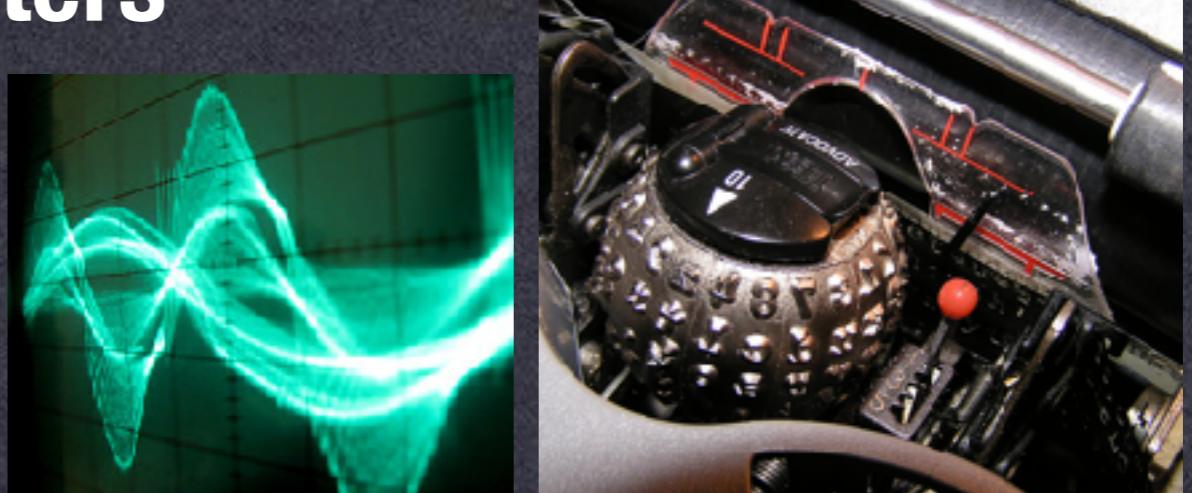


Side Channels



Side Channels

- Some history:
 - 1943: Bell engineer detects power spikes from encrypting teletype
 - 1960s: US monitors EM emissions from foreign cipher equipment
 - 1980s: USSR places eavesdropping device inside IBM Selectric typewriters



Selectric image by Flickr user relvax used under a Creative Commons license.

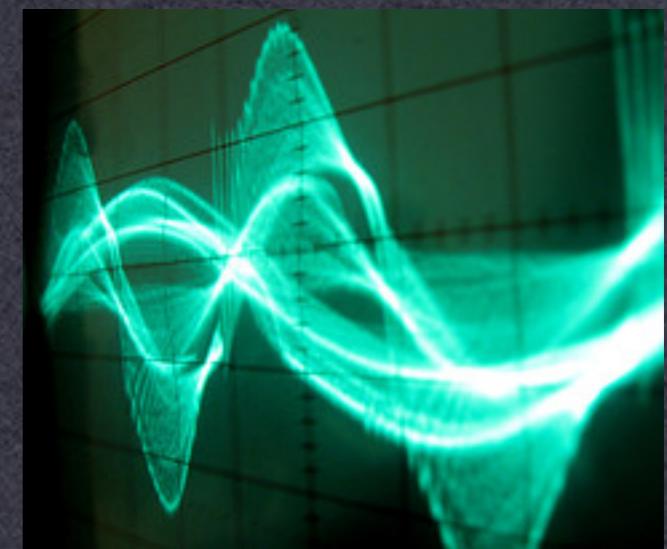
Side Channels

- Some history:
 - 1990s: Paul Kocher demonstrates timing attacks, power analysis attacks against RSA, Elgamal



Common Examples

- Timing
- Power Consumption
- RF Emissions
- Audio



MAC verification

- How does one verify a MAC?

RSA Cryptosystem

$$N = p \cdot q$$

$$e \cdot d \equiv 1 \pmod{(p-1)(q-1)}$$

Encryption

$$c = m^e \pmod{N}$$

Decryption

$$m = c^d \pmod{N}$$

RSA Cryptosystem

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Encryption

$$c = m^e \pmod{N}$$

Decryption

$$m = c^d \pmod{N}$$

$$m = \underbrace{c * c * c * \cdots * c}_{\text{d times}} \pmod{N}$$

Modular Exponentiation

$$m = c^d \bmod N$$

$$d = 1010101001110101011001001001001$$



```
modpow(c, d, N) {  
    result = 1;  
  
    for (i = 1 to |d|) {  
        if (the ith bit of d is 1) {  
            result = (result * c) mod N;  
        }  
  
        c = c2 mod N;  
    }  
  
    return result;  
}
```



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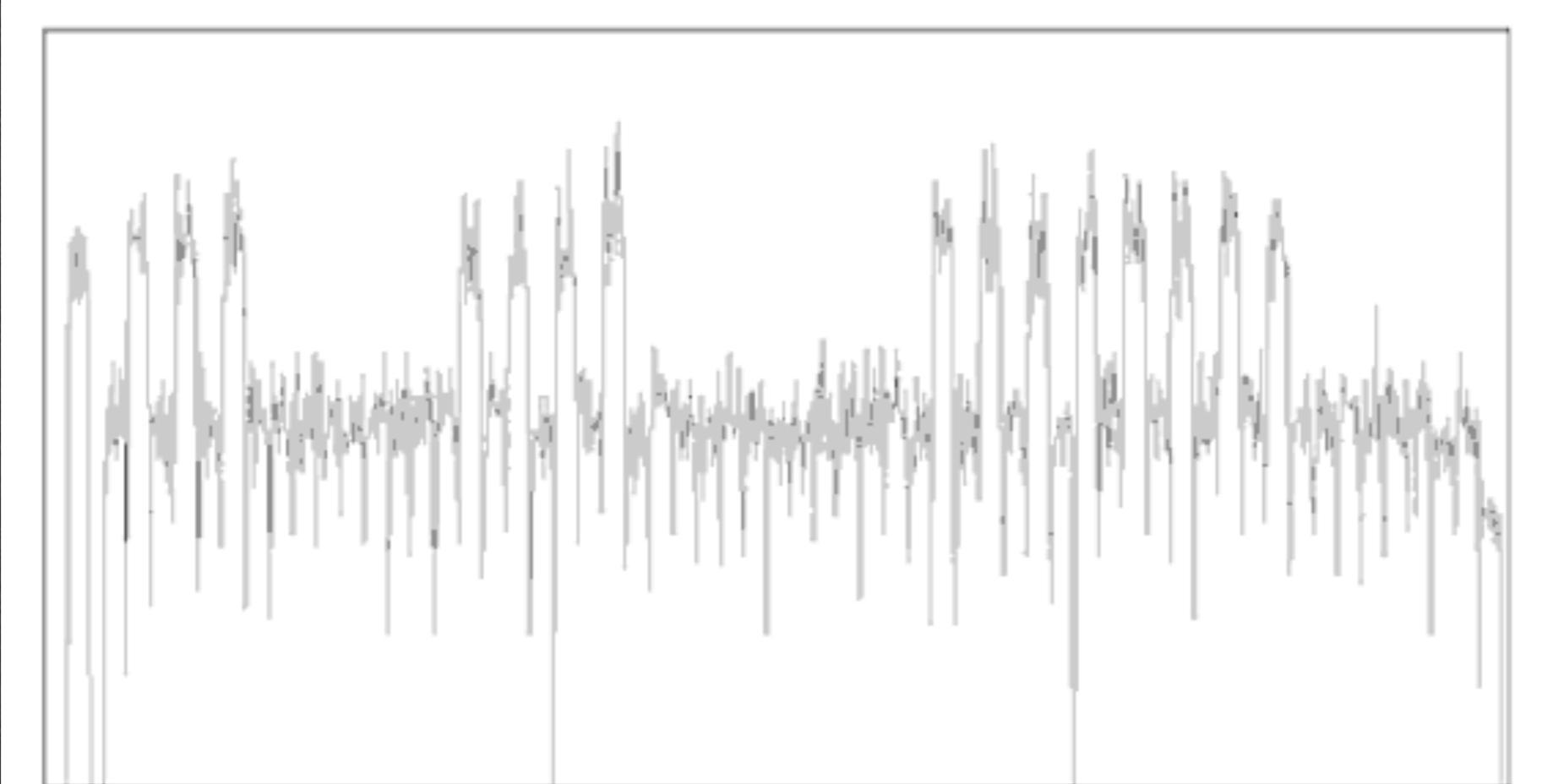
$d = 1010101001110101011001001001001$



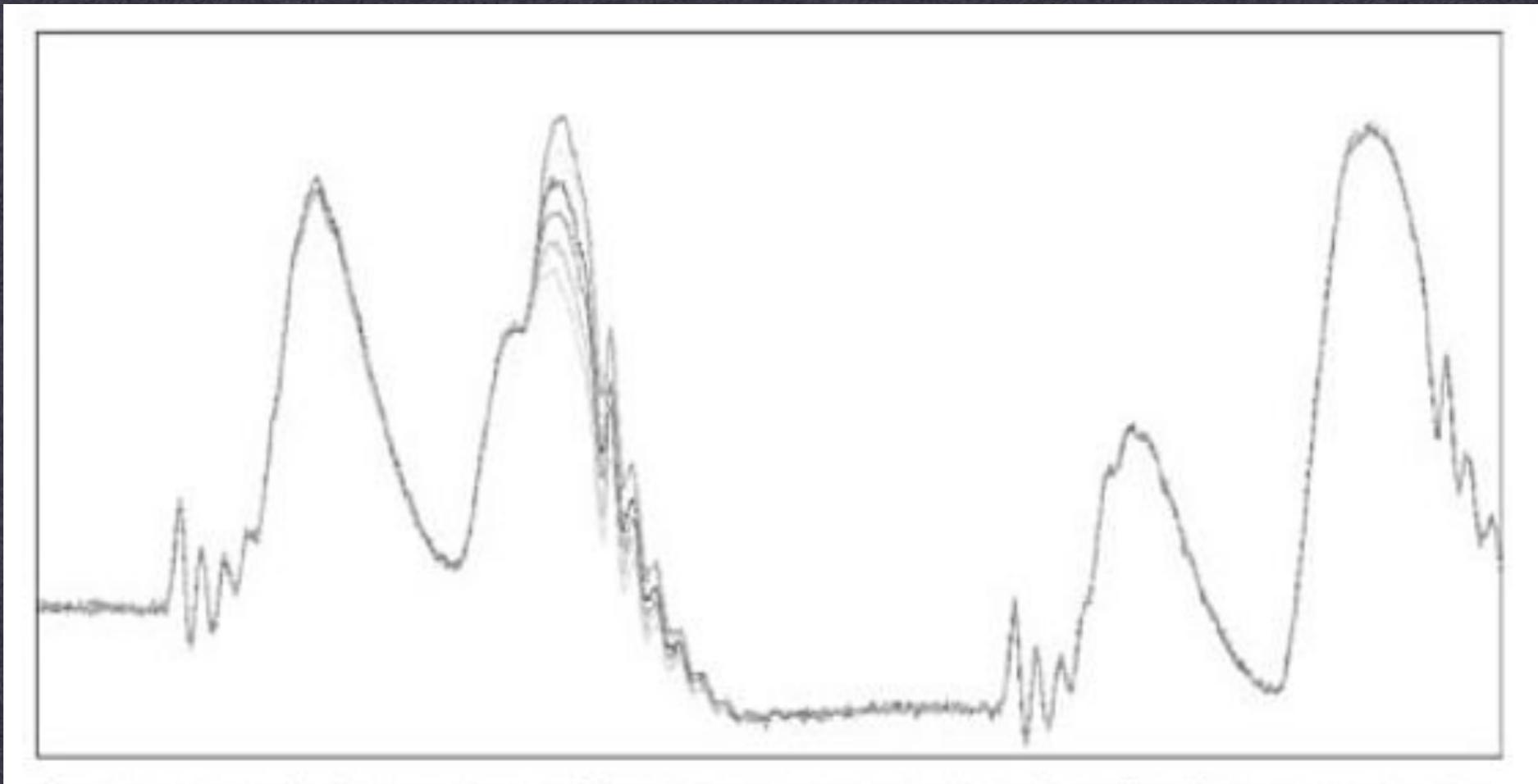
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    result = 1;  
  
    for (i = 1 to |d|) {  
        if (the ith bit of d is 1) {  
            result = (result * c) mod N; Expensive Operation  
        }  
  
        c = c2 mod N;  
    }  
  
    return result;  
}
```



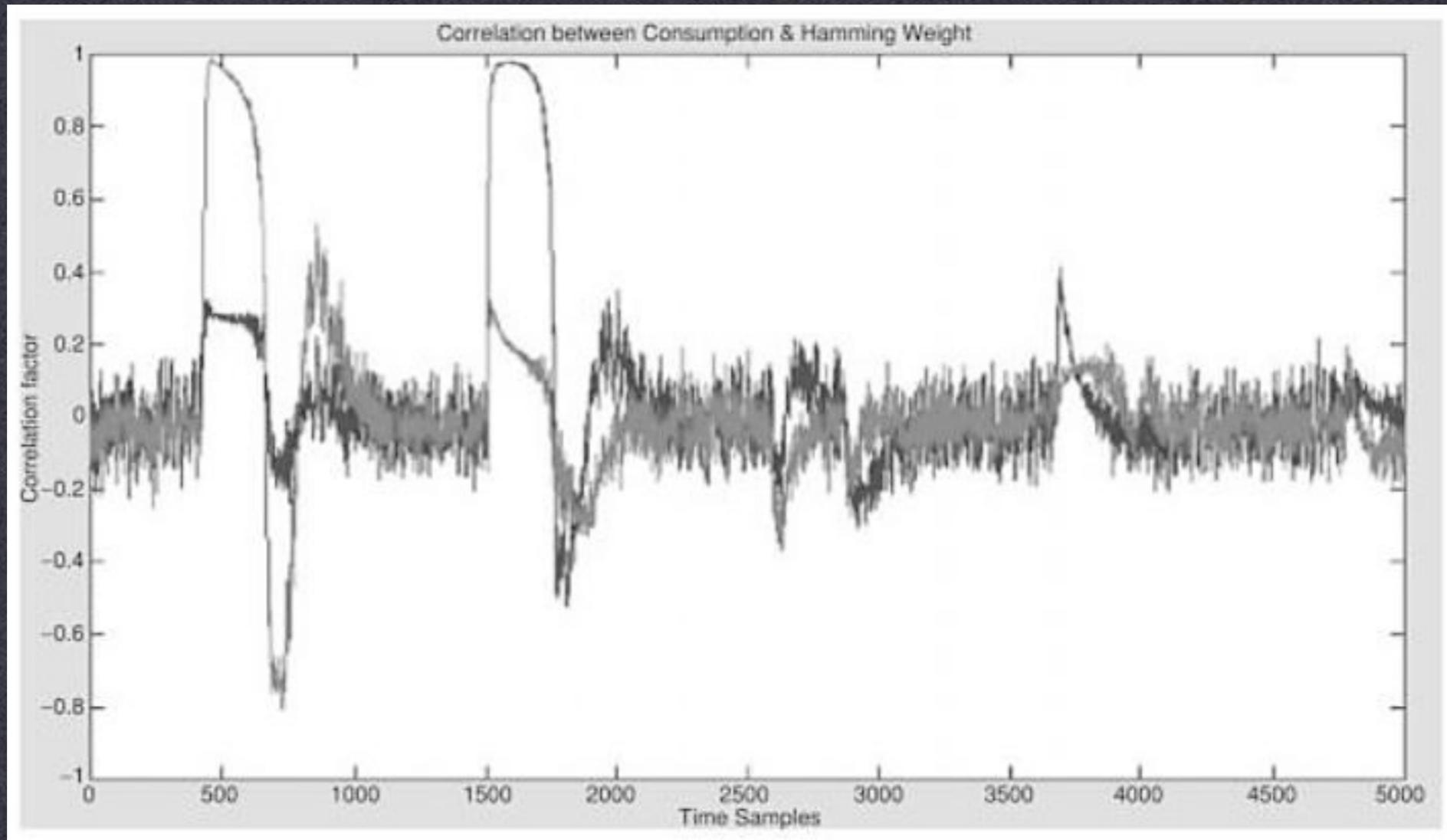
Simple Power Analysis



Differential Power Analysis



Differential Power Analysis



Modular Exponentiation

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            result = (result * c) mod N;  
        }  
  
        c = c2 mod N;  
    }  
  
    return result;  
}
```

Expensive Operation

Modular Exponentiation

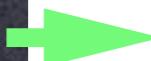
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Expensive Operation



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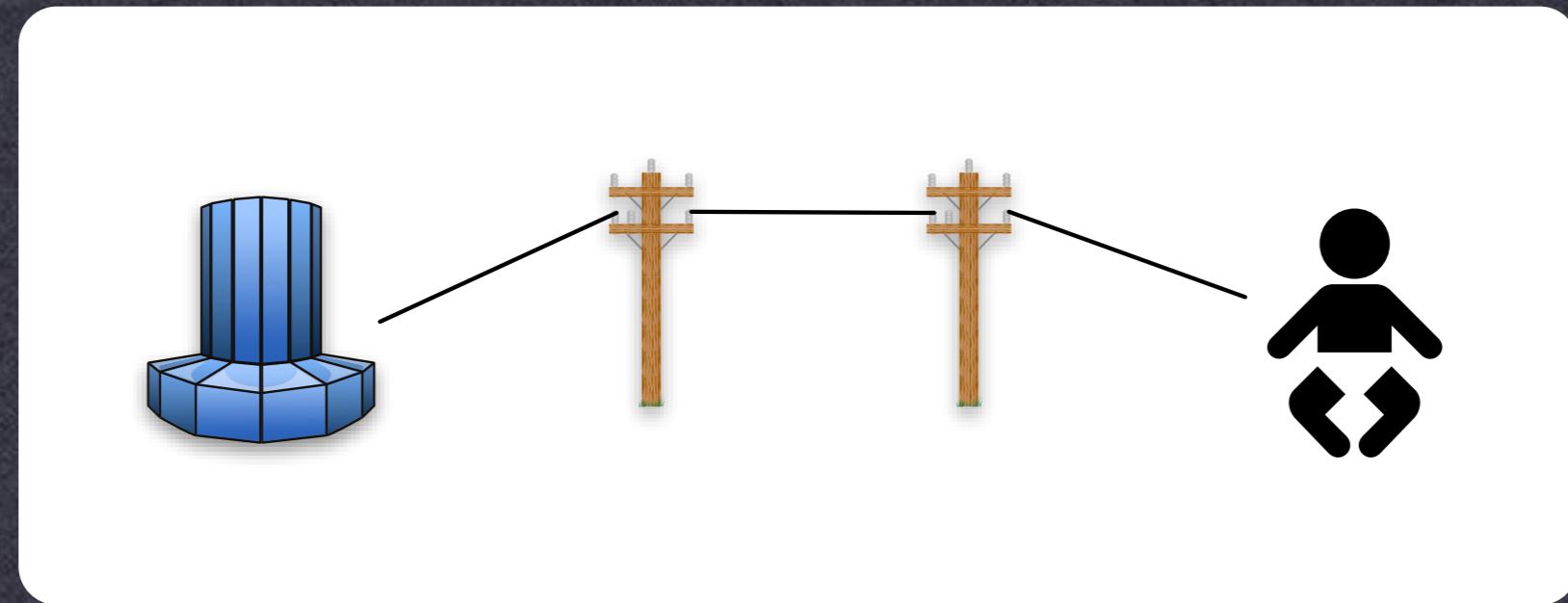


Kocher's Timing Attack

- Assume that for some values of $(a * b)$, multiplication takes unusually long
- Given the first b bits, compute intermediate value “result” up to that point
- If the next bit of $d = 1$, then calc is $(\text{result} * c)$
- If this is expected to be slow, but response is fast then the next bit of $d \neq 1$

Remote Timing Attacks

- Boneh & Brumley
 - Remote attack on RSA-CRT as implemented in OpenSSL
 - Optimization, uses knowledge of p, q



CRT

- If one knows the remainder of division by several integers (p, q) then one can compute the remainder of division by products ($p*q$)
 - assuming p, q are co-prime
- Why does this help us?

Solutions

- Quantization:
 - All operations take the same time
 - Hard to do without sacrificing performance
- Blinding:
 - Prevents attacker from selecting ciphertext (that is processed with the secret key)

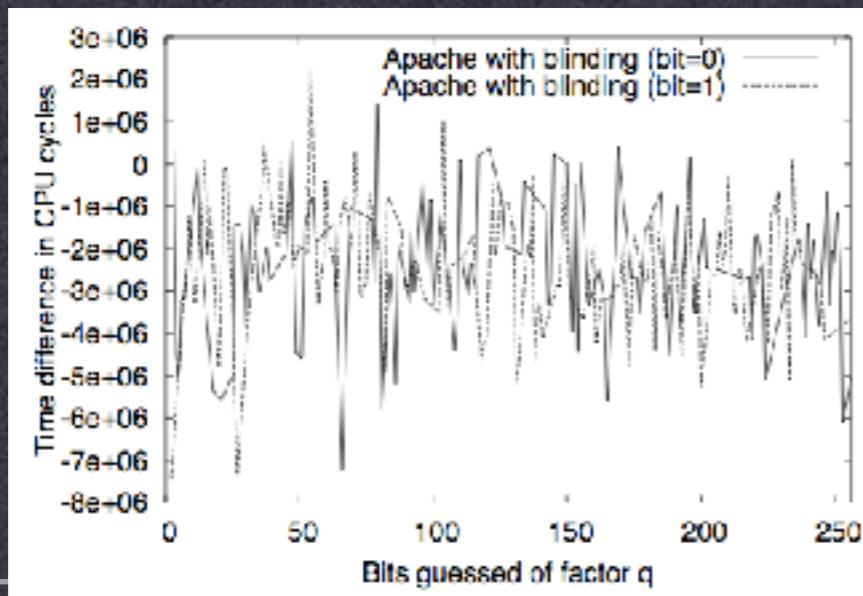
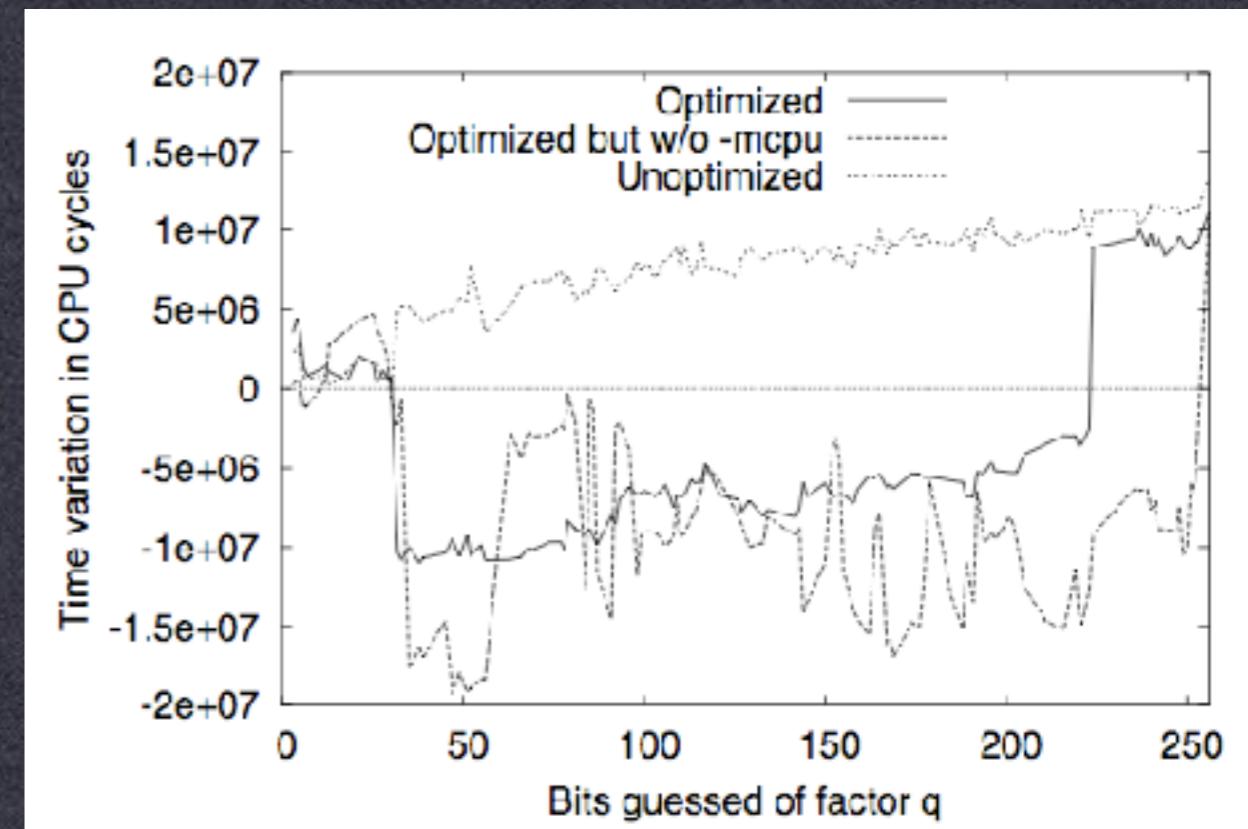
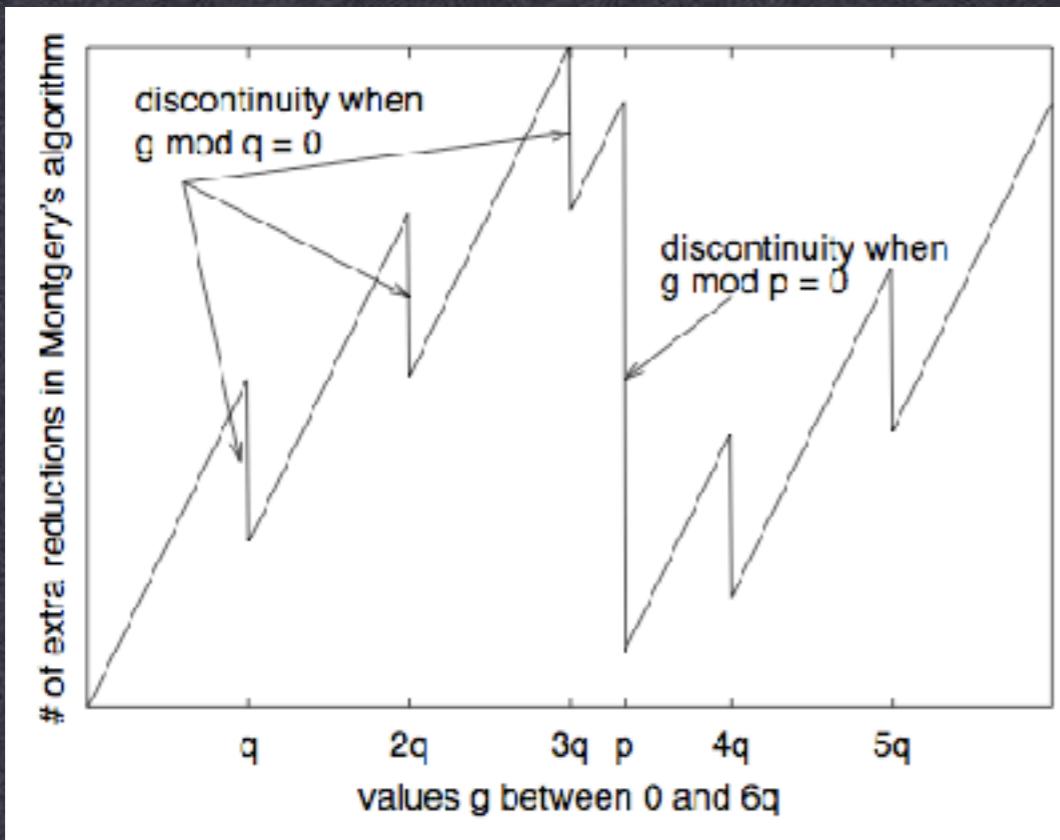


Image Source: Boneh, Brumley: Remote Timing Attacks are Practical

Remote Timing Attacks

- Boneh & Brumley
 - By repeating the timing measurements, they were able to extract a secret key after several million samples



Source: Boneh, Brumley: Remote Timing Attacks are Practical

Windowed Exponentiation

$$m = c^d \bmod N$$

$d = 1010101001110101011001001001001$

- Handles the input in larger “chunks”
 - Fixed or variable sized
 - Can speed up by pre-computing some values
 - e.g., c^2, c^3, \dots, c^{16}

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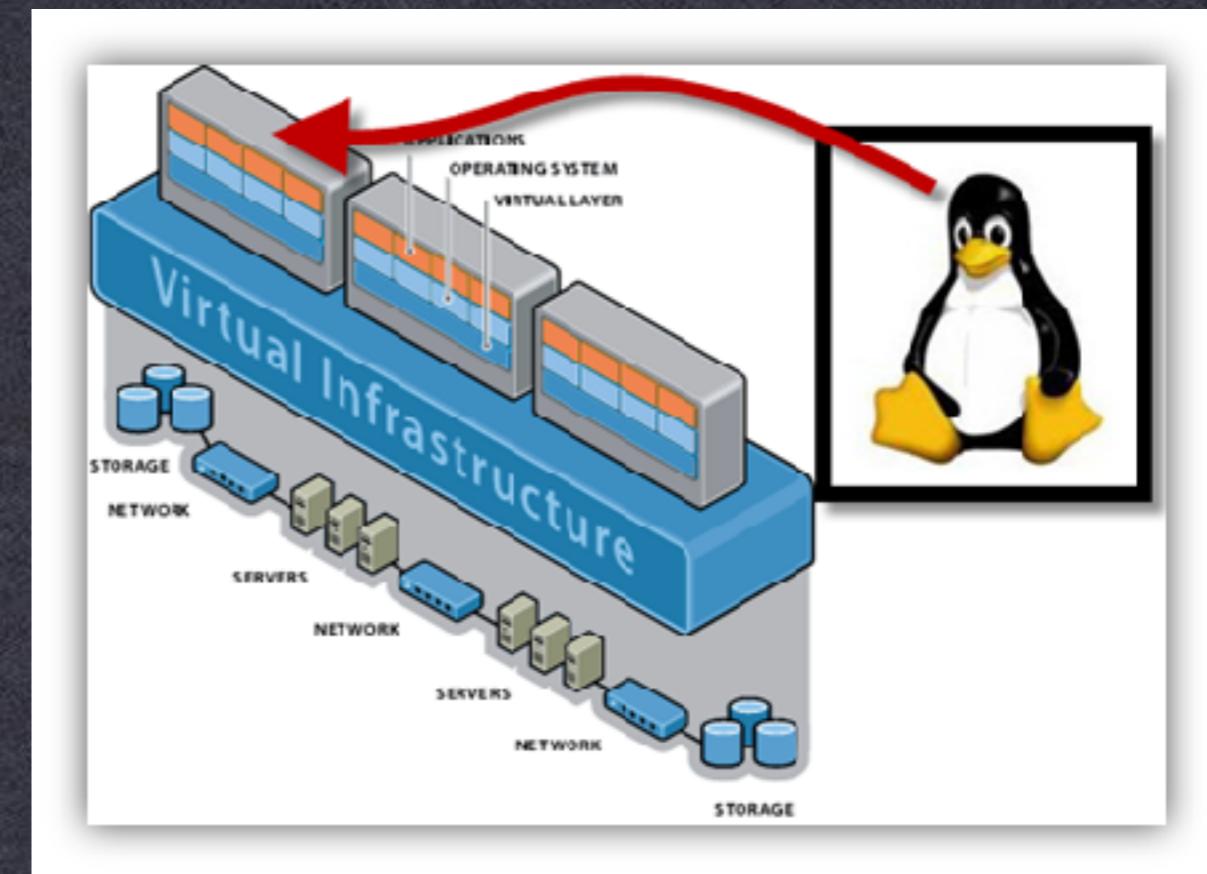
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Cache Timing Attacks

- Observation
 - When we use pre-computed tables, this implies a memory access
 - This takes time
 - Unless the data is cached!



Hypervisor attacks

- Observation
 - This applies to code too!

```
SquareMult( $x, e, N$ ):  
    let  $e_n, \dots, e_1$  be the bits of  $e$   
     $y \leftarrow 1$   
    for  $i = n$  down to 1 {  
         $y \leftarrow \text{Square}(y)$  (S)  
         $y \leftarrow \text{ModReduce}(y, N)$  (R)  
        if  $e_i = 1$  then {  
             $y \leftarrow \text{Mult}(y, x)$  (M)  
             $y \leftarrow \text{ModReduce}(y, N)$  (R)  
        }  
    }  
    return  $y$ 
```

The 9 Lives of Bleichenbacher’s CAT: New Cache ATtacks on TLS Implementations

Eyal Ronen*, Robert Gillham†, Daniel Genkin‡, Adi Shamir¶, David Wong§, and Yuval Yarom†**

*Tel Aviv University, †University of Adelaide, ‡University of Michigan, ¶Weizmann Institute, §NCC Group, **Data61

Listing 2. Pseudocode of RSA_1

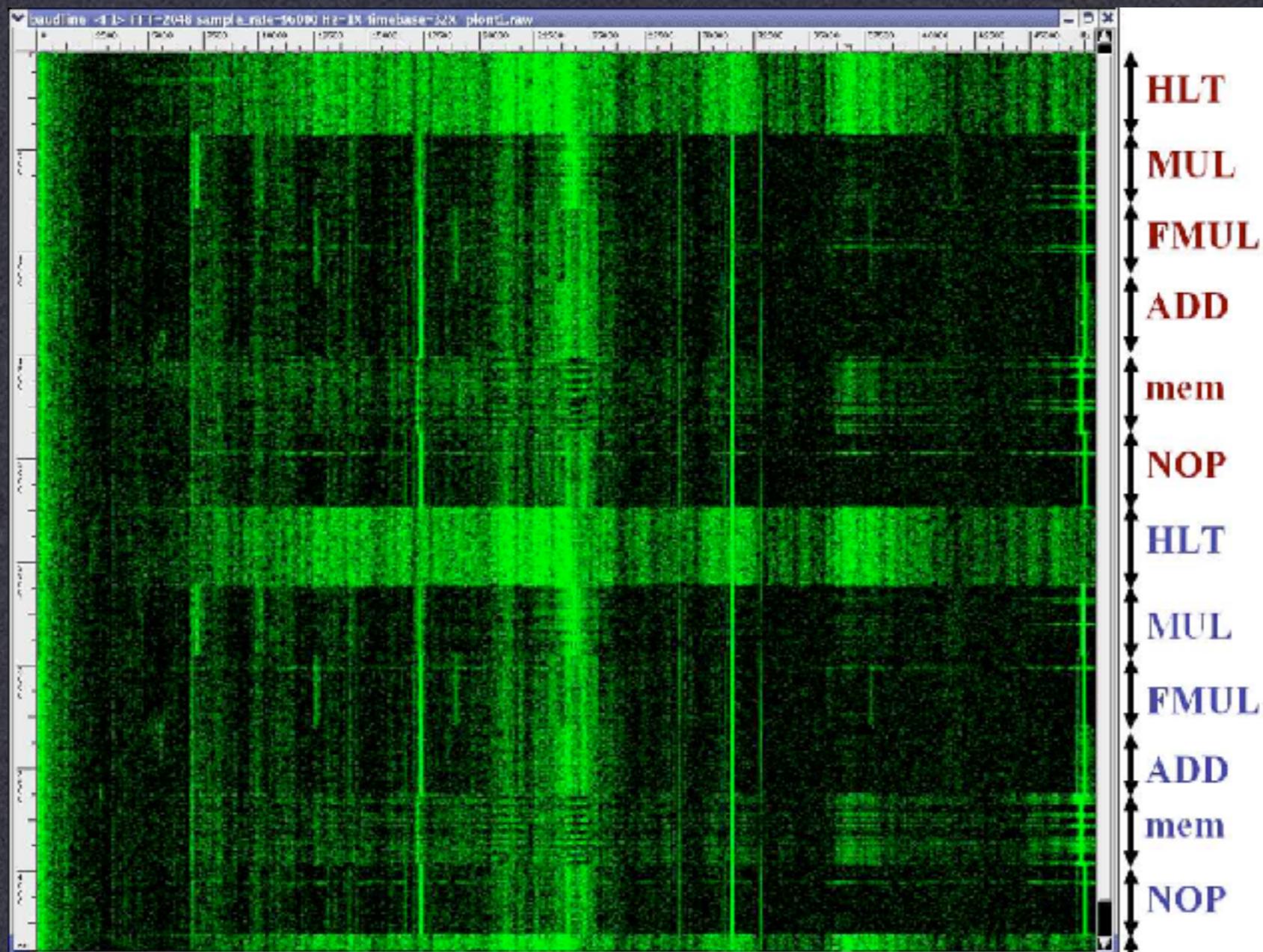
```
1 int SSLDecodeRSAKeyExchange(keyExchange, ctx) {
2     keyRef = ctx->signingPrivKeyRef;
3     src = keyExchange.data;
4     localKeyModulusLen = keyExchange.length;
5     ... // additional initialization code omitted
6
7     err = sslRsaDecrypt(keyRef, src,
8         localKeyModulusLen,
9         ctx->preMasterSecret.data,
10        SSL_RSA_PREMASTER_SECRET_SIZE, &outputLen);
11    if(err != errSSLSuccess) {
12        /* possible Bleichenbacher attack */
13        sslLogNegotiateDebug("SSLDecodeRSAKeyExchange:
14                                RSA decrypt fail");
15    } else if(outputLen !=
16              SSL_RSA_PREMASTER_SECRET_SIZE) {
17        sslLogNegotiateDebug("SSLDecodeRSAKeyExchange:
18                                premaster secret size error");
19        // not passed back to caller
20        err = errSSLProtocol;
21    }
22    if(err == errSSLSuccess) {
23        ... // (omitted for brevity)
24    }
25    if(err != errSSLSuccess) {
26        ... // (omitted for brevity)
27        sslRand(&tmpBuf);
28    }
29    /* in any case, save premaster secret (good or
30       bogus) and proceed */
31    return errSSLSuccess;
32 }
```

attack [69], as implemented in the Mastik toolkit [68].

Monitoring Locations. To reduce the likelihood of errors, we monitor both the call-site to RSAerr (Line 25 of Listing 2) and the code of the function RSAerr. Monitoring each of these locations may generate false positives, i.e. indicate access when the plaintext is PKCS #1 v1.5 conforming. The former results in false positives because the call to RSAerr shares the cache line with the surrounding code, that is always invoked. The latter results in false positives when unrelated code logs an error. By only predicting a non-conforming plaintext if *both* locations are accessed within a short interval, we reduce the likelihood of false positives. We note that this technique is very different to the approach of Genkin et al. [30] of monitoring two memory locations to reduce false negative errors due to a race between the victim and the attacker [6]. Unlike us, they assume access if *any* of the monitored locations is accessed.

Experimental Results. Overall, our technique achieves

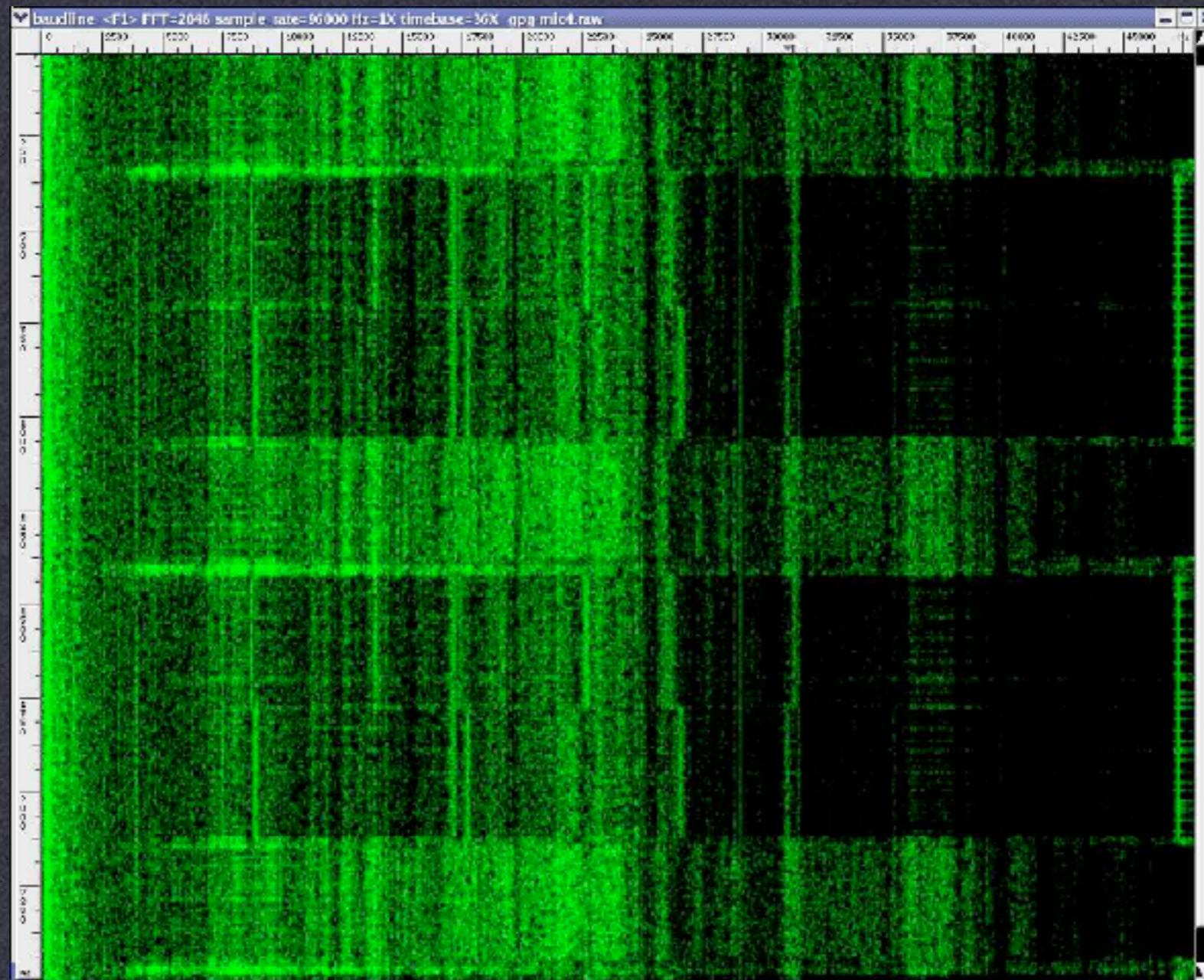
Acoustic Side Channels



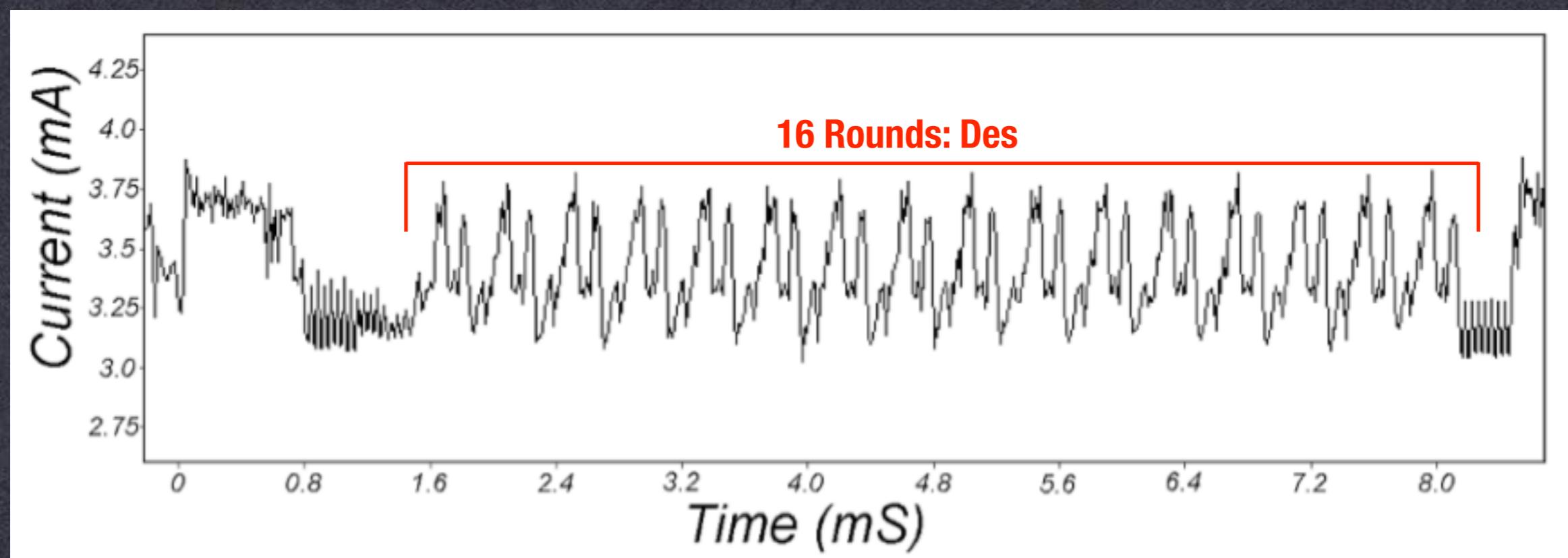
Acoustic Side-Channels

GPG RSA-CRT
signature #1:

(repeated)



Reverse Engineering



Back to KeyLoq

- KeyLoq attack has an unhappy coda:
 - Turns out that all keys for a given manufacturer are derived from the same MK
$$k = pad(ID, seed) \oplus MK$$
 - Key derivation function based on XOR :(
 - By observing 2 interactions between key/car we can derive the MK and thus steal any car