## CS 452 - K2

#### **Team Members**

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## Repository:

https://git.uwaterloo.ca/mg-cs452/cs452/-/tree/k2?ref\_type=heads

Commit SHA for RPS: f1ae331ff476b170f52489dd3ca6dd72c02f7ba1

Commit SHA for measurements: 71427f8e9be10cd233f48e14d2da5ad51e600ad3

# Send-Receive-Reply

The send-receive-reply is implemented as explained in the lecture such that data flows when both sender and receiver are ready and there is no asynchronous buffering.

## **Design Choices**

- For implementation, we added four new fields in our task descriptor: status, SendData, ReceiveData, and a sender queue of TIDs.
- There are five valid statuses: inactive, ready, send-wait, receive-wait, and reply-wait.
- The SendData object stores the five parameters of the Send() function and the ReceiveData object stores the 3 parameters of the Receive() function. Furthermore, to effectively allocate and reclaim SendData and ReceiveData instances, they are implemented with intrinsic linkage such that the struct is defined to have a pointer to the next object.
- The sender queue is implemented as a circular buffer to ensure O(1) push and pop operation and reuse of memory

### Send

When a user task returns to kernel with a send syscall, here are the action the kernel performs

- Verify that the recipient TID is an active user task by checking the status flag of the task descriptor. If it's not, reschedule the current task with a -1 return.
- Verify that the sender status is ready and recipient task status is either ready or receive-wait. If it's not, reschedule the current task with a -2 return.
- Get the next free SendData object and populate the fields with the syscall arguments
- Add a pointer to this SendData object to the current task's descriptor
- If the recipient is in ready state, or in other words, doing something else,
  - Add the current task into the sender queue of the recipient task
  - Set the status to send-wait
- If the recipient is in receive-wait,
  - Do some sanity checks
  - Set the TID pointer of the recipient's ReceiveData to the current task's TID and copy the message from the send buffer to the receive buffer
  - Reclaim the ReceiveData object
  - Set the sender status to reply-wait and the recipient status to ready and reschedule the recipient task.

### Receive

When a user task returns to kernel with a receive syscall, here are the action the kernel performs

- Perform sanity checks to ensure current task is in ready status
- If there is no tasks in the current task's sender queue,
  - Get the next free ReceiveData object and populate the fields with the syscall arguments
  - Add a pointer to this ReceiveData object to the current task's descriptor
  - Set the current task's status to receive-wait
- If there is one or more task in the sender gueue.
  - Pop the next sender
  - Sanity check the TID and task status
  - Get sender task's task descriptor from the TID
  - Copy the data from send buffer to receive buffer
  - Set the current task's status to ready and the sender task's status to reply-wait
  - Reschedule the current task

## Reply

When a user task returns to kernel with a receive syscall, here are the action the kernel performs

- Perform sanity check on the TID and both tasks' statuses
- Get sender task's task descriptor from the TID
- Copy data from the replier's data buffer to the sender's reply data buffer
- Set sender task's status to ready (and replier's status stays ready)
- Reschedule both tasks

## Name Server

In order to allow user-tasks to determine their own identifier as opposed to the system designated TID, we implement a name server that resolves TID to user-defined names and vice versa using two functions.

## **Design Choices**

- As a default starting point, we assume that the name server's TID is always 1, or in other words, it is always the first task being scheduled.
- When RegisterAs() encounters a name that already exists in the mapping, we overwrite it and the previous TID associated to the name now has no name registered.
- When Whols() is called with an unregistered name, we assume that this is a result of a bug. Thus the program logs an error and halts.
- User tasks are not expected to register or query for names that are null-terminated but the names stored in the name server mapping will always be null-terminated.
- The maximum name length is set to 20 characters. If a user task attempts to register or query for a name longer than that, the system will log an error and the name in the resulting mapping will get truncated but will still be null-terminated. Thus, assuming there doesn't exist another name with the same first 20 characters, the functionality should not be affected by the truncation.
- Under our current assumption of there never being more than 20 tasks running simultaneously, we have decided that the name server will not benefit greatly from having a O(1) query time data structure such as a hashmap. Instead, we have an array of length 20 (max number of simultaneously active tasks) that map to the corresponding names.

### Name Server Task

The name server task is designed to be a task that infinitely waits to receive queries from other tasks and never exits.

It first stack-allocates 2D array of size 20 (maximum number of simultaneously active tasks) by 20 (maximum size of task name). All names are initialized to "\0". It then enters a polling loop starting with a Receive function. If the intended message sent is greater than the maximum size of task name defined, the string is truncated to length 20 and terminated with a null character.

If the message is received with a starting character of "r" (for RegisterAs),

- Run str\_cmp on every existing name in the mapping to check if the name in the message already exists. If it does, overwrite it to "\0".
  - Although this is an inefficient operation, we have decided that RegisterAs() will only be called once per task. Thus, the maximum number of operations is 20 (max # of tasks that will call RegsterAs) \* 20 (length of mapping array) \* 20 (max length of task names) = 8000 which is insignificant at runtime.
- Linearly set the name mapped to the current task's TID to the name in message, terminated by a null character.
- Reply with a buffer of size 0.

If the message is received with a starting character of "w" (for Whols),

- Run str\_cmp on every existing name in the mapping to find the TID that is registered under the name in the message.
- If the name is not found, reply with a buffer of size 0 indicating an error.
- If the name is found, stack-allocate a buffer of size 1 storing the resulting TID as a char.

## RegisterAs()

RegisterAs() takes one parameter of the name the current task is to be registered as. It then stack-allocates another char buffer of size 1+len(name), sets the first character to 'r' (representing a RegisterAs call to the name server) then linearly copies the name into the new buffer.

Although this is an inefficient operation, we have decided that RegisterAs() will only be called once per task. Thus, the maximum number of operations is 20(max # of tasks) \* 20(max length of task names) = 400 which is insignificant at runtime.

We then make a Send syscall with this new char array as the msg buffer and no reply buffer. Send will then return the intended reply length. If the reply length is not 0, we log the error and halts since the name server is designed to reply with a char array of size 0 upon RegisterAs(). If the Send syscall failed with a return value of <0, RegisterAs() also returns -1 indicating a fail.

## Whols()

Whols() takes one parameter of the task name to be queried. It then stack-allocates another char buffer of size 1+len(name), sets the first character to 'w' (representing a Whols call to the name server) then linearly copies the name into the new buffer.

Although this is an inefficient operation, we have decided that Whols() will be called at maximum by each task for every other task. Thus, the maximum number of operations is 20(max # of tasks) \*19(max # of other currently active tasks) \* 20(max length of task names) = 7600 which is insignificant at runtime.

We then make a Send syscall with this new char array as the msg buffer and a reply buffer of size 1 which should be filled with the TID value as a char. Send will then return the intended

reply length. If the reply length is not 1, we log the error and halts since the name server is designed to reply with a char array of size 1 upon Whols().

If the Send syscall failed with a return value of <0, RegisterAs() also returns -1 indicating a fail.

## Game Server

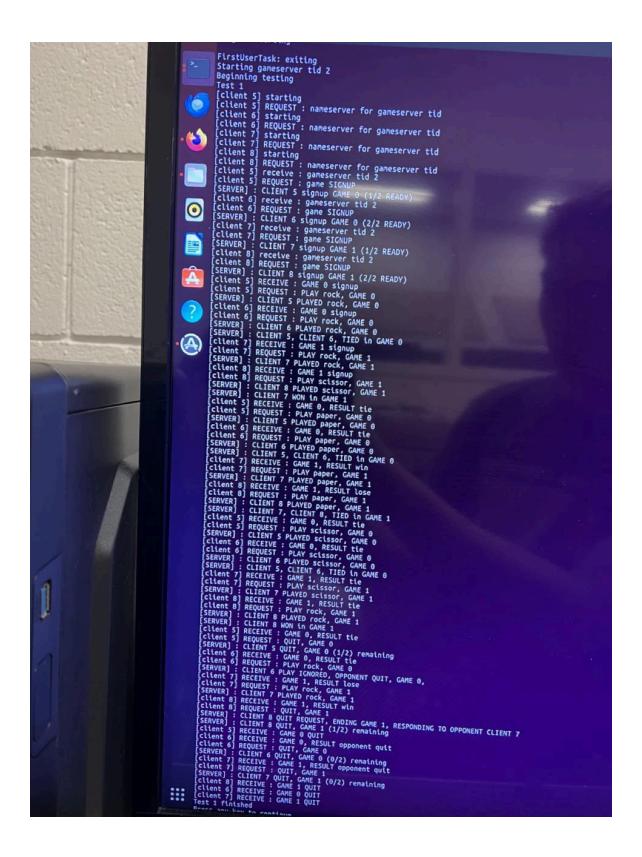
The game server is an infinite loop that receives messages from the game clients and acts on them respectively. There is a max of 5 games and when games are done, they are recycled to allow future players to use an old game. Each of the tests are a user task at a higher priority than the game clients, allowing all the game clients to be created first. However, since each client first calls MyTid(), it may be preempted before the "[client x] starting" print statement.

### **RPS Game Test Results**

### Test 1:

Create(3, &rps\_quit); // task 5 Create(3, &rps\_move\_quit); // task 6 Create(3, &rps\_move\_quit); // task 7 Create(3, &spr\_quit); // task 8

In the first test, we created 4 clients all at the same priority. At a high level, the first 2 clients (5, 6) always tie with each other by playing ROCK, PAPER, SCISSORS. However, the first will attempt to quit and then second will attempt to play first and then quit. The last 2 clients (7,8) win, tie, and lose respectively where the first client plays ROCK, PAPER. SCISSORS and the last client plays SCISSORS, PAPER, ROCK. Afterward, Client 5 quits first, causing 6 to receive a quit result and later quit. On the other hand, 8 quits after 7 makes a final move. The server then responds to 7, indicating 8 quit, allowing 7 and 8 to quit.



At the beginning, all 4 clients query our nameserver for the game server tid. Since all 4 clients are at the same priority, they act like a FIFO queue due to using the current time as a tiebreak.

After, 5 and 6 are registered in game 0, 7 and 8 are registered in the next game. All clients begin playing. 5 and 6 always tie with both choosing ROCK, PAPER, SCISSOR in each respective round. 7 and 8 choose ROCK, SCISSOR in the first round with a win and lose result respectively. Next round they play PAPER, PAPER resulting in a tie. In the last round, they play SCISSOR, ROCK with a lose and win result. After the final rounds for each pair of clients, they both attempt to quit.

For 5 and 6, 5 requests to quit, and 6 requests to play ROCK. The server receives 5's request to quit responds, and then sees 6's request to play. The server then responds to 6 indicating that 5 quit.

In opposite order of 5 and 6, 7 requests to play, and 8 requests to quit. The server receives 7's request to play then sees 8's request to quit. The server allows 8 to quit and then responds to 7 indicating that 8' quit.

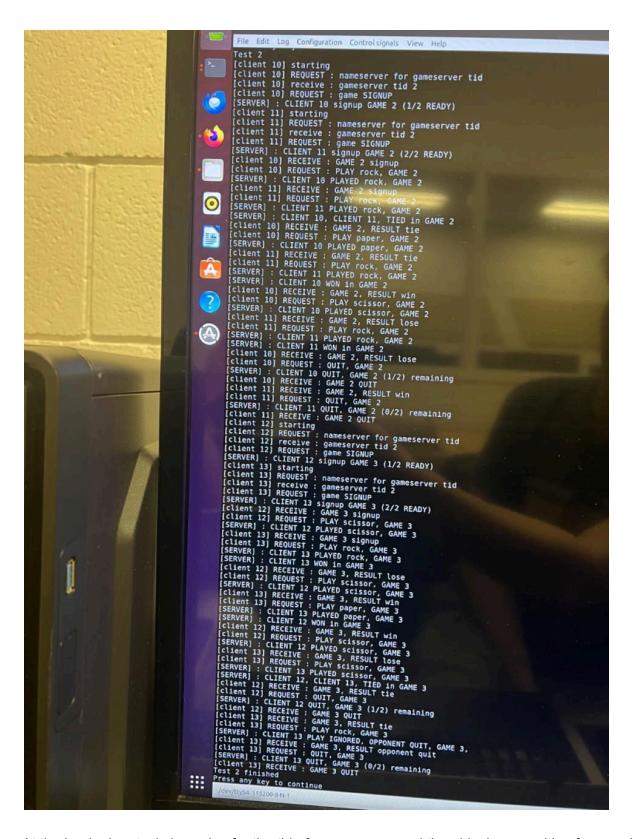
Eventually, both 6 and 7 also guit after being notified that their partner guit.

### Test 2:

Create(4, &rps\_quit); // task 10 Create(5, &rock\_quit); // task 11 Create(6, &scissor\_quit); // task 12 Create(7, &rps\_move\_quit); // task 13

In the second test, we have 4 tasks in decreasing priority levels. Task 10 will play ROCK, PAPER, SCISSOR at the highest priority level, task 11 will only play ROCK at a lower priority. Task 12 will only play SCISSOR at a lower priority and Task 13 will play ROCK, PAPER, SCISSOR, ROCK.

Given the priority scheme, tasks 10 and 11 will play completely until they are finished. At which the lower priority clients, 12 and 13 will then play their game.



At the beginning, task 1 queries for the tid of game server and then blocks on waiting for a reply from the game server. Task 1 is able to execute all the way until blocking on a reply due to its high priority. Afterward, task 2 does the same as task 1 and registers to the game server. At this

point, the game server has 2 valid clients and responds to them both allowing them to execute their play. Since these 2 clients have higher priority than the others, clients 12 and 13 wait until they are completely done before starting their game. For clients 10 and 11's games, we expect ROCK, ROCK in round 1 leading to a tie. PAPER, ROCK in round 2 leading to a win, lose respectively. In the last round, we expect SCISSORS, ROCK leading to a lose, win again. In the end, both quit at the same time and we see this illustrated in the above output.

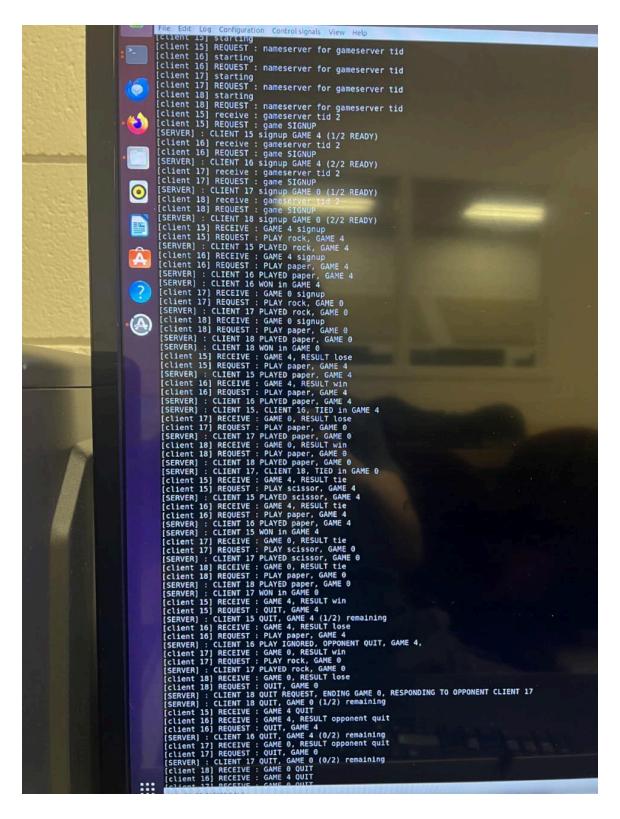
After the first pair are done, clients 12 and 13 begin their execution. We expect, SCISSOR, ROCK in round 1 for a lose, win. In round 2, we expect SCISSOR, PAPER for a win, lose, and in the last round we expect a SCISSOR, SCISSOR for a tie respectively for clients 12 and 13. Afterward, 12 quits and 13 attempts to play ROCK. 13's play is ignored and replied to with the partner quitting. 13 should then quit as well.

### Test 3:

Create(3, &rps\_quit); // task 15 Create(3, &paper\_move\_quit); // task 16 Create(3, &rps\_move\_quit); // task 17 Create(3, &paper\_quit); // task 18

In the final test, we have 4 tasks at the same priority levels. Task 15 will play ROCK, PAPER, SCISSOR at the highest priority level, task 16 will only play PAPER and make an extra PAPER move before quitting. Task 17 will play ROCK, PAPER, SCISSOR, ROCK, and task 18 will only play PAPER. However, since we only have a max of 5 active games, we should expect the final game to wrap around and have game id of 0.

The order of tasks should match Test 1.



All tasks fetch the nameserver after each other due to the same priority level and both begint their games after registering. However, note that since we allow a max of 5 games, clients 17 and 18 are put into game 0 again. For clients 15 and 16 we should expect ROCK, PAPER for a

lose win in round 1. Then clients 17 and 18 do the same. For round 2, clients 15 and 16 should play PAPER, PAPER resulting in a tie. This also occurs for 17 and 18 right after. Lastly, 15 and 16 should play SCISSOR, PAPER resulting in a win, lose respectively. Similarity for 17 and 18. At this point, 15 quits but 16 attempts to play again. 16's move is ignored and replied to with the opponent quit result. This also happens to 17 and 18 in a flipped order.

# Changes in Kernel Design:

Changes from our K1 submission. Aside from these, all other kernel design decisions can be found in our K1 description.

## Syscall Argument Passing

As mentioned in our K1 submission, we planned on changing the process in which user tasks pass arguments to the kernel. Previously we were using scratch registers (x9 & x10), however, we hoped to use the intended parameter registers (x0-x7) instead.

In our K2 implementation, the syscall parameters are moved to the user task registers starting from x0 as planned. When the kernel attempts to access those values, it simply reads from the current task descriptor register array.

During development, we noticed that compiler optimization level -O2 copies and runs the assembly code generated for some functions directly into the call sites of those functions as opposed to branching as one would expect. Thus, when we attempt to move the parameter values into the argument registers using inline assembly, we end up losing some parameter values because they are stored out of order in the same registers x0-x7. To mitigate this, we first move all the values into scratch registers x9 onwards, then move them back into x0-x7 in order.

## Create() Error Handling

We added the missing create() return values for when an invalid priority value is provided and when the kernel is out of task descriptors.

## **SRR Performance Measurements**

SRR performance test code can be found in the same git repo under branch k2-measurements or under this SHA: 71427f8e9be10cd233f48e14d2da5ad51e600ad3

The Send-Receive-Reply implementation is identical to the one in branch k2 but we removed all uart\_dprintf() statements to get a more accurate measurement of performance. We observed a speed increase of approximately 10 microseconds from these removals even though the print function is implemented to not actually print unless the debug flag is on.

## Methodology

To test the runtime of a complete SRR cycle, I create two tasks with the same priority. Sender\_task is responsible for sending and for measuring time since the SRR operation ends in a return in the Send() function while receiver\_task is responsible for receiving and immediately replying.

In order to account for the variability of the performance, we run the SRR functions a total of n=1000 times per configuration. We decided that this is a high enough number of repetition since we observed no difference between results using an n value of 1000, 2000, and 5000. To reduce the impact of the counter function overhead, we get the difference before the clock count at the beginning and the clock count after all 1000 iterations. Furthermore, we also measure and log the clock runtime itself prior to each experiment but it has never exceeded 1 microsecond and is thus insignificant to our experiment result.

### Observations

From the 48 configurations we have experimented on, we have observed these key properties:

- When observing results without optimization, we notice that the runtime has a linear relationship with message size. Specifically, SRR has approximately 280-300 microseconds of overhead and when the message size increases by 1 byte, the runtime increases by ~3.2 microseconds. Notably, this property does not hold true for the optimized results.
- Turning on optimization (level O3) has a significant impact on SRR performance. Furthermore, the level of effect optimization has is positively correlated to the message size, indicating that it efficiently reduces the runtime of linearly copying of one buffer into another:

4 bytes: ~20% of noopt
 64 bytes: ~13% of noopt
 256 bytes: ~7% of noopt

- Modifying the execution order of sender-first vs. receiver-first has no observable impact on runtime.
- Turning on instruction cache when optimization is turned on seems to reduce runtime by ~9%. Other than that, changing the cache setup has no observable impact on SRR runtime.

Appendix: K1 documentation

## CS 452 - K1

#### **Team Members**

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Repository: https://git.uwaterloo.ca/mg-cs452/cs452

Commit SHA: 745f77084b8b7e9041cb3a1658a64725bb0f4ee1

# Kernel Design:

Our kernel design is based on a microkernel where code is non-interruptable and synchronous. System calls are kept short and fast by doing as little as possible on the kernel side.

The kernel is comprised of the following core components: a scheduler, a context switcher, an exception handler, and an exception code handler.

Before diving deep, we'll look at our kernel's high-level flow.

### **Kernel Main**

After kernel initialization, the **scheduler** finds the next task to run based on task priority. The kernel **context switches** to that task, saving all the required information about the current kernel state and loading the task state. Afterward, either once the user task finishes or makes another system call, we hit our exception handler. The **exception handler** does the opposite of the initial context switch by saving the task state and loading the previous kernel state. The kernel resumes where it last left, and we appropriately respond to the exception code with our **exception code handler**. The code handler takes the exception code and executes various functionalities such as creating new tasks, yielding, finding information, etc based on the exception code. This process repeats from the top until there are no tasks to run.

## Scheduler

The scheduler is a generic priority queue that takes in a fixed-sized array and a custom comparator. In our case, the fixed-sized array is an array of task frame pointers, and a function to compare task priorities.

The priority queue is implemented as a min heap to provide O(log(n)) operations for pushing and popping the minimum element. These time complexities are guaranteed from a binary heap implementation with siftup and siftdown operations.

Since task priorities go from high to low starting at 0, a min heap suited our use case. For tasks that have the same priority, the timestamp of when a task is added to the queue is used for tiebreaks, where the oldest have a higher priority. This results in a FIFO queue for tasks with the same priority.

#### Context Switcher

The context switcher is an assembly function that stores registers for the kernel and loads registers of the scheduled task.

It begins by grabbing the address of the kernel task frame. Registers x0-x30 (note x30 is the link register) and the stack pointer are stored using offsets from the address of the kernel frame with help from C struct memory alignment. We don't need to store the pc because after switching back into the kernel, we want the code to jump directly to the Ir address which is immediately after the context switching function. We also don't need to store any special registers such as eIr\_1, spsr\_1, etc in our kernel because these values are specific to an individual user task.

Afterward, the address of the current task frame is loaded. Registers x0-x30, the stack pointer, the pc, and spsr have been set from task initialization or a previous system call, and are loaded into their respective registers.

Specifically for loading our task, we store wherever a task is supposed to resume into elr\_el1 in such that when we do an exception return, the exception return will jump to the correct spot. To illustrate, for the first context switch, we store the task's function address in elr\_el1.

For other special registers:

We store the user task's SP into sp\_el0 because since we are in the kernel, using "sp" will refer to sp\_el1.

Lastly, we also set spsr\_el1 to the correct bits such that SError and IRQ bits are masked and the sp\_el0 bit is set.

## **Exception Handler**

The exception handler is composed of 2 parts: the exception vector table and a c function. As part of kernel initialization, the VBAR (vector base address register) is populated with the start of our vector table label so that for every exception, we have a defined exception handler.

As a result, from a system call using SVC, we've defined a c function for that specific exception to jump to.

In that c function, we enter our kernel again and do the opposite of what our initial context switch does.

We store all registers x0-x30, sp, elr\_el1, spsr\_el1 into our current task frame, and load our previously saved kernel frame x0-x30 and sp registers.

## **Exception Code Handler**

The exception code handler is a group of "if" statements that check for various exception codes immediately after the exception handler and return to the kernel main. The specific action depends on the type of exception. As an example, for create, we allocate a new task frame, initialize it, and add it and the original task to the scheduler queue. Furthermore, for exception types that need to pass in certain arguments, registers x9 and x10 are used and for returning values, x0 is used. Note the reason why we are currently not using x0-x7 for parameters is because intermediary function calls that overwrite x0-x7. In order to compensate for this, we would have to revamp how we store passed in arguments, which is something we will look into for the next assignment. However, for our current use case other scratch registers x9-x18 satisfy our current use case.

# **Memory Layout**

.stack (kernel stack)

.text.boot

.text.evt (exception vector table)

.text (code)

.rodata

.data

.bss

start of user task stack base

## Memory Allocation/Task Frames

There is no dynamic memory allocation in this kernel at this point. All memory allocations are done at initialization. The most "dynamic" structure we have is our array/list of task frame descriptions.

Each of our task frames are given a base stack address to represent their stack. In order to have contiguous memory and decouple from the need of a memory allocator, each of these task frames are intrinsically linked to the next task frame through a field which is a pointer to the next task frame.

Instead of utilizing the stack directly with push and pop operations in order to save user and kernel registers, we have opted into storing our registers with a global pointer to a memory address, representing our task frame, on the kernel stack. We have done this in order to more effectively debug our code by being able to examine specific registers globally.

### Kernel Initialization

Our kernel initialization comprises of the following steps

- Initialization the base of our exception vector table
- GPIO + uart initialization
- Kernel task frame initilziation and setting global pointer to kernel task frame (usage in context switching)
- User task initializations (setting TID's and stack base addresses)
- Scheduler (heap) initialization
- First task initialization

## **System Parameters**

- Maximum of 20 user task frames (excluding task frame for
  - After taking a look at our future use case of a train system, we estimated each train would need ~5 tasks (controller, input, output, sensor, etc) which at most we'd probably have ~3 trains at a time. This is a total of 15 tasks, which leaves 5 other tasks for our kernel including our clock and other items. This was a lower bound estimate.
- Kernel stack base address is 0x7FFF0
  - To minimize potential kernel stackoverflow issues, we've ensure that our stack can "grow" downwards until 0x0, such that when we do reach stackoverflow, we should be able to handle that promptly or automatically through a hardware error.
- Each user stack size is 1024 bytes
  - This was chosen as a rough estimate by checking the runtime stack size of our kernel which was < 1024 bytes. As a result, we thought this would be a decent start for our user task size

- First user stack base address is 0x8D9A0 (grows upwards for every base stack address, so next stack base address would be 0x8DDA0 that grows downwards)
  - With our current stack size, the lowest address a user stack "should" touch is 0x8D5A0 which is far higher than our highest piece of memory defined in our linker, bss, which ends at 0x82f88. As a result, we this stack base should be adequate for our usage especially for A1.

## **Bugs and Limitations:**

- Creating more than 20 task frames (without reclaiming a task frame)
- No check for stack overflow for kernel or user tasks

# **Output Explanation:**

#### Output:

```
MESS:00:00:25.646432:0: uart: Set PLUII
MESS:00:00:25.652757:0: uart: Baud
                                           change
MESS:00:00:25.654817:0: uart: Baud
                                           change
MESS:00:00:25.673201:0: genet: GENET STOP: 0
Program starting
Created: 1
Created: 2
MyTid: 3, MyParentTid: 0
MyTid: 3, MyParentTid: 0
Created: 3
MyTid: 4, MyParentTid: 0
MyTid: 4, MyParentTid: 0
Created: 4
FirstUserTask: exiting
MyTid: 1, MyParentTid: 0
MyTid: 2, MyParentTi
MyTid: 1, MyParentTid:
MyTid: 2, MyParentTid:
 /dev/ttyS0 115200-8-N-1
```

Lets walkthrough the output. At the beginning of our kernel, we create a root task that creates these 4 sub tasks.

Initially, our root task is at priority 2. The root task first tries to create its first task at priority 3. Since priority 3 is less than our root task's priority, we continue and get our first line of output

#### Created: 1

The root task creates a new task at priority 3 as well. Since that is also less than our root's priority, we again continue and get out second line of output with a new TID.

#### Created: 2

The root task now creates a new task at priority 1. Since that is greater than our current root task's priority, we are pre-empted and the new task runs to get our new line of input.

#### MyTid:3, MyParentTid: 0

At this point, the task yields control back to the kernel. However, since it has the highest priority, it is rescheduled and re-prints.

#### MyTid:3, MyParentTid: 0

Once the task is finished, it exits, which allows us to context switch back into our root task to get our next output line.

#### Created: 3

Simialr to task 3, the root task now creates another task at priority 1. Since that is also greater than our current root task's priority, we are pre-empted and the new task runs to get our next line of input.

#### MyTid:4, MyParentTid: 0

Afterwards, the task yields and is rescheduled due to its high priority of 1 causing us to come back to this task and print the next line.

#### MyTid:4, MyParentTid: 0

Afterwards, task 4 exits and the root task is rescheduled and outputs.

#### Created: 4

Now, since the root task is finished, the root task exits and prints the following.

FirstUserTask: exiting

However, we are not done yet as we have two tasks, 1 and 2, still in our queue due to their low priority. Since these two tasks are at the same priority, the timestamp of when they are added to the queue is used to tiebreak these tasks. Because we always scheduled the oldest first to

provide FIFO behaviour, task 1 should run and does.

MyTid:1, MyParentTid: 0

Task 1 yields and is added back to our queue. However, unlike tasks 3 and 4, task 1 has the same priority as task 2 and using our timestamp tiebreak, task 2 is scheduled instead by being

the oldest task.

MyTid:2, MyParentTid: 0

Task 2 now yields, and using similar logic as above, task 1 is now scheduled.

MyTid:1, MyParentTid: 0

As this task now exits, the last task to be scheduled is task 2.

MyTid:2, MyParentTid: 0