

Figure : Fetch-Decode States and LDA\_IMM Execution

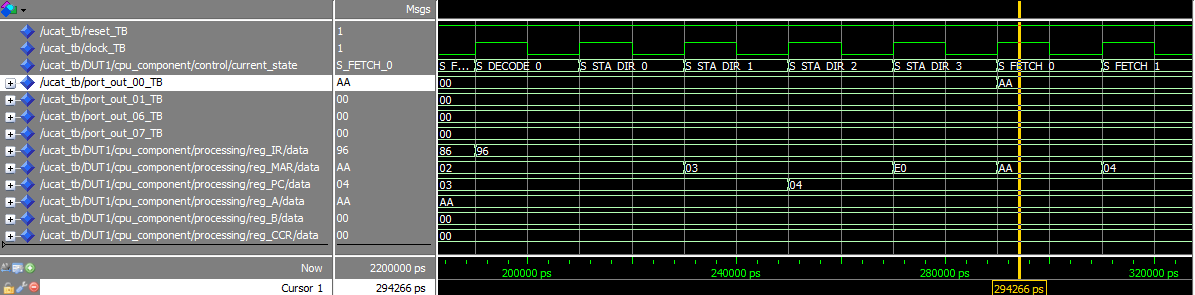


Figure : STA\_DIR Execution

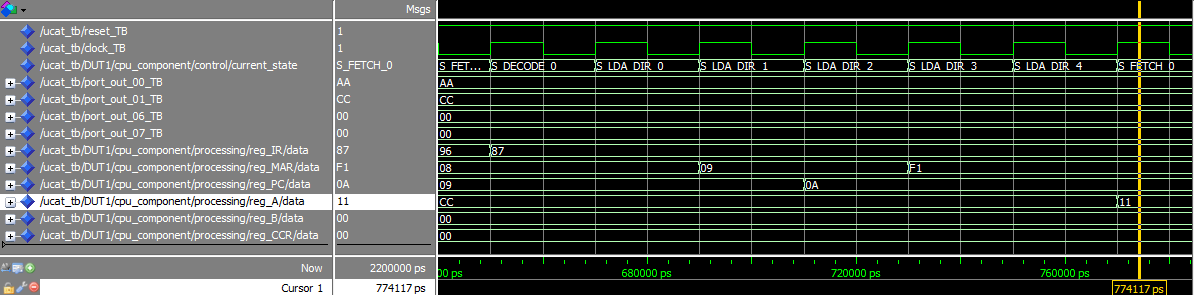


Figure : LDA\_DIR Execution

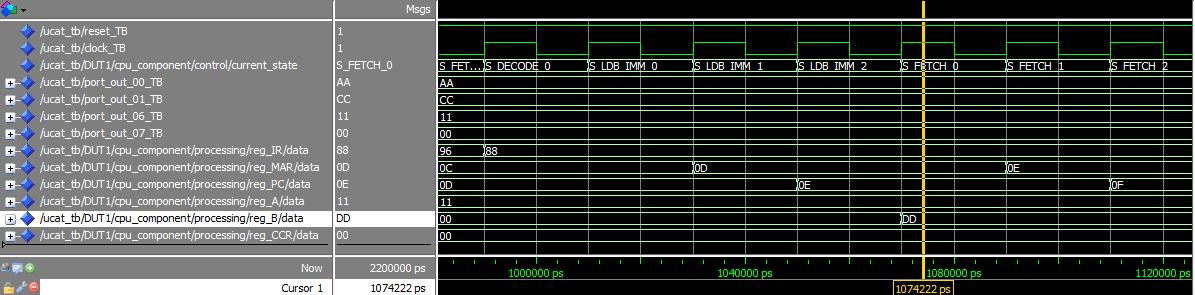


Figure : LDB\_IMM Execution

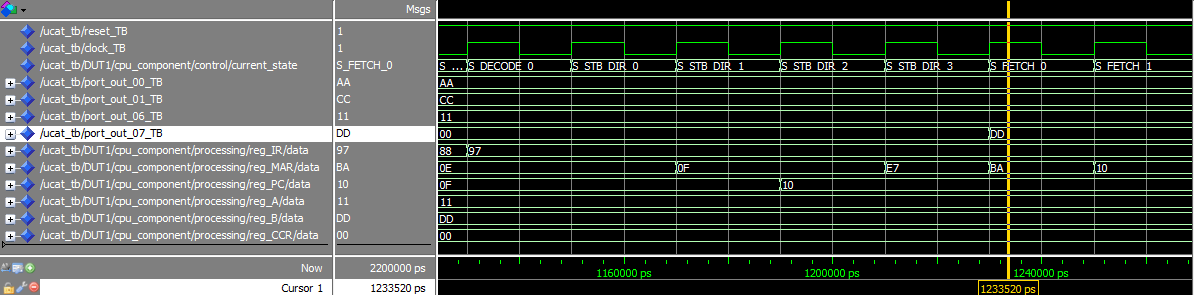


Figure : STB\_DIR Execution

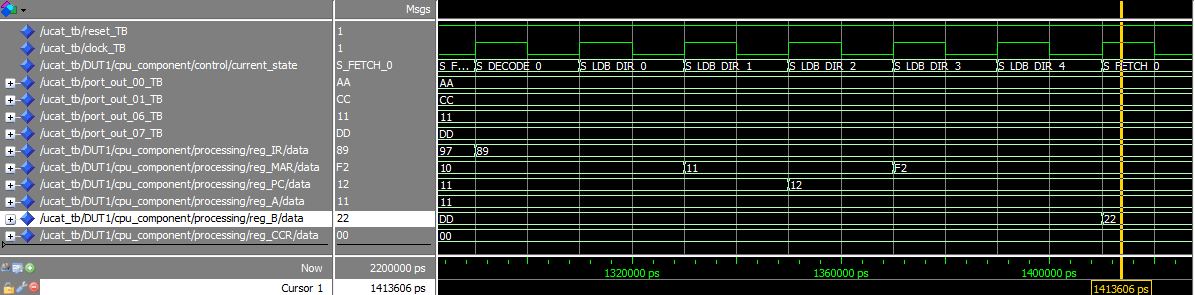


Figure : LDB\_DIR Execution

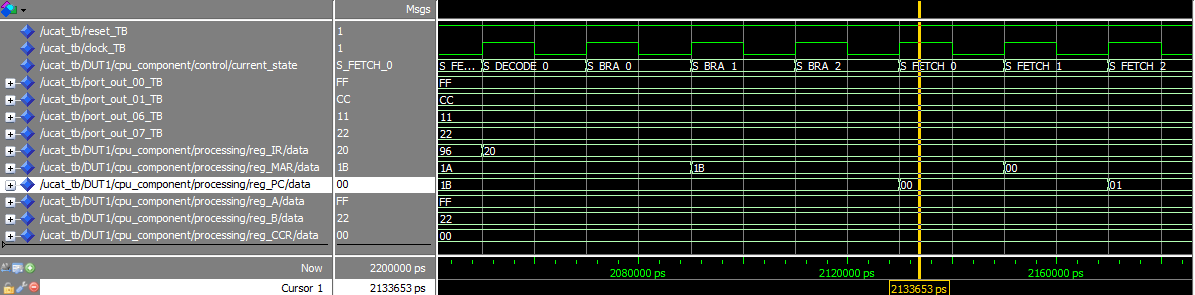


Figure : BRA Execution

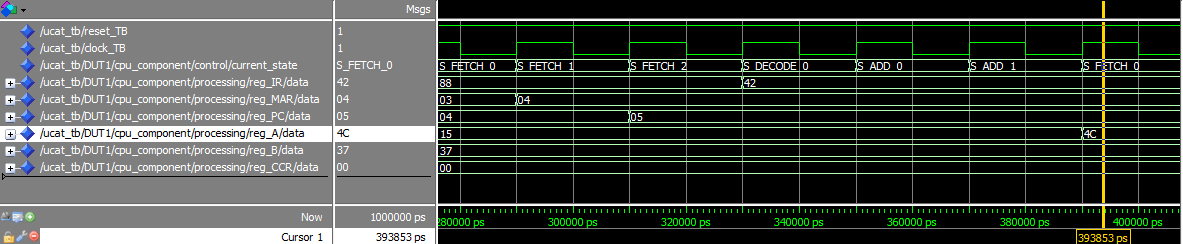


Figure : ADD\_AB Execution

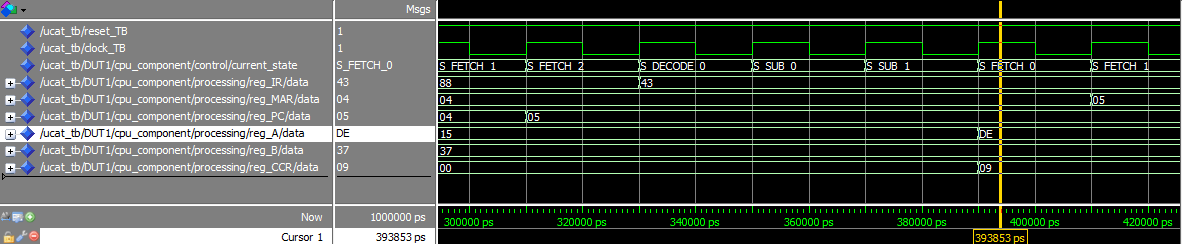


Figure : SUB\_AB Execution

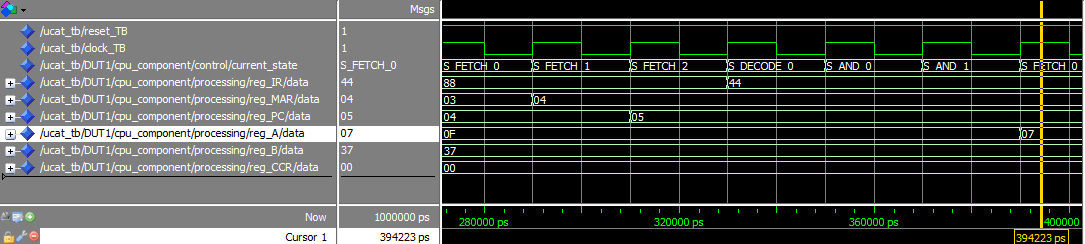


Figure : AND\_AB Execution

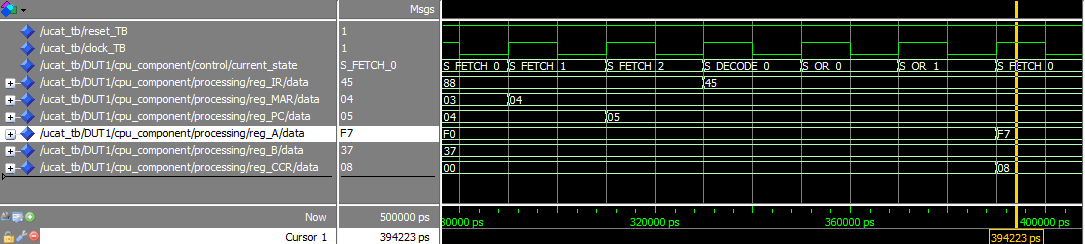


Figure : OR\_AB Execution

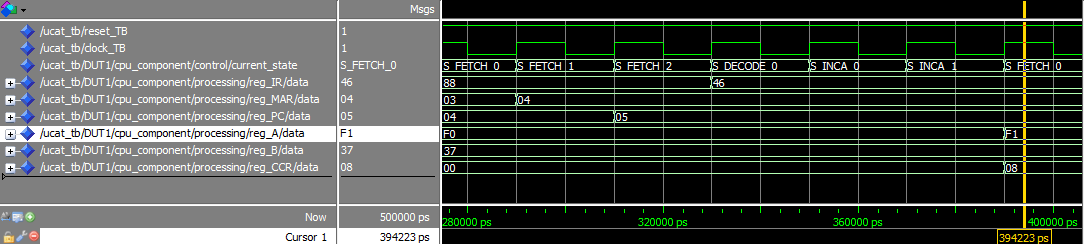


Figure : INCA Execution

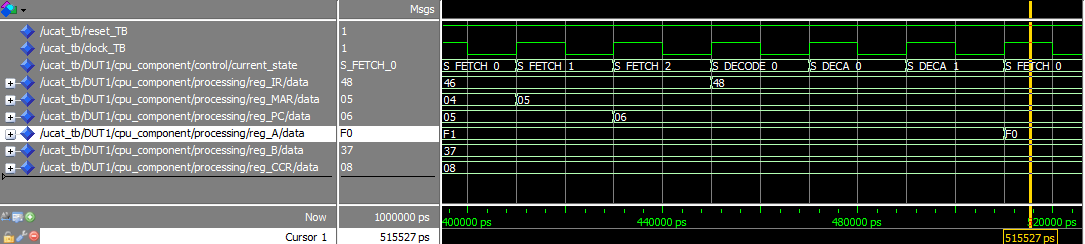


Figure : DECA Execution

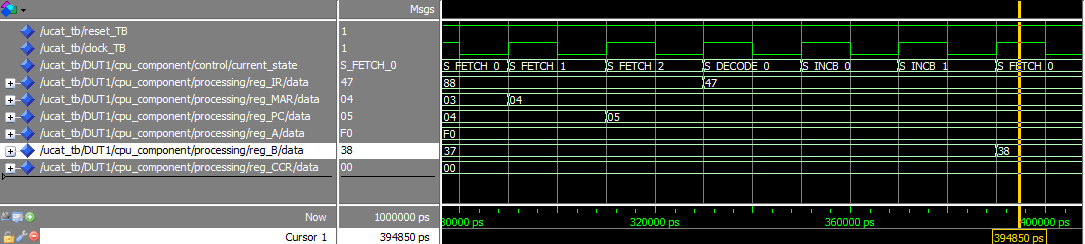


Figure : INCB Execution

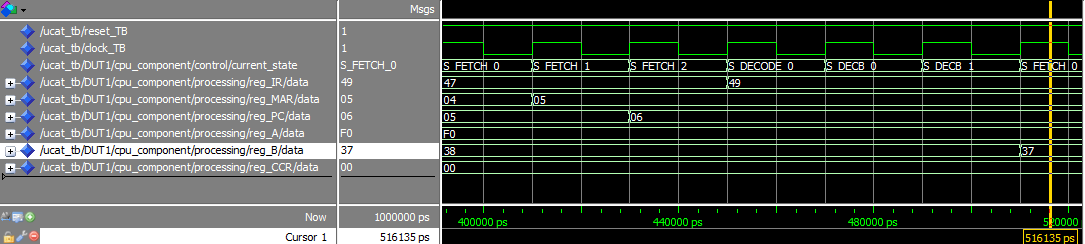


Figure : DECB Execution

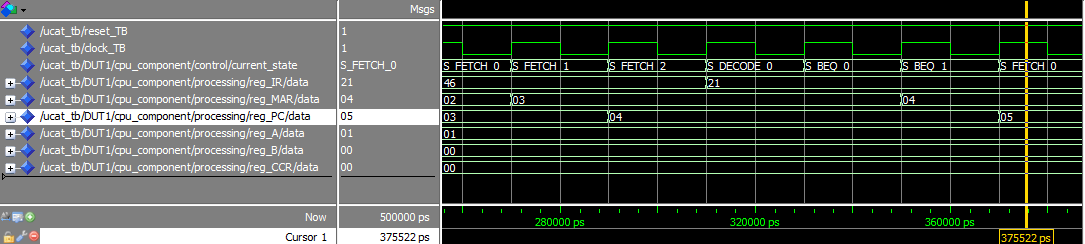


Figure : BEQ Execution 1

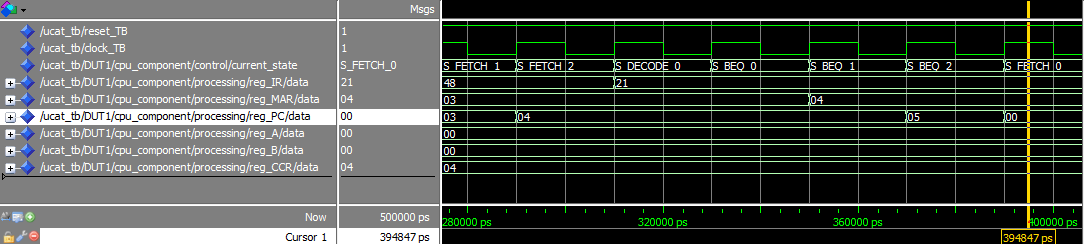


Figure : BEQ Execution 2