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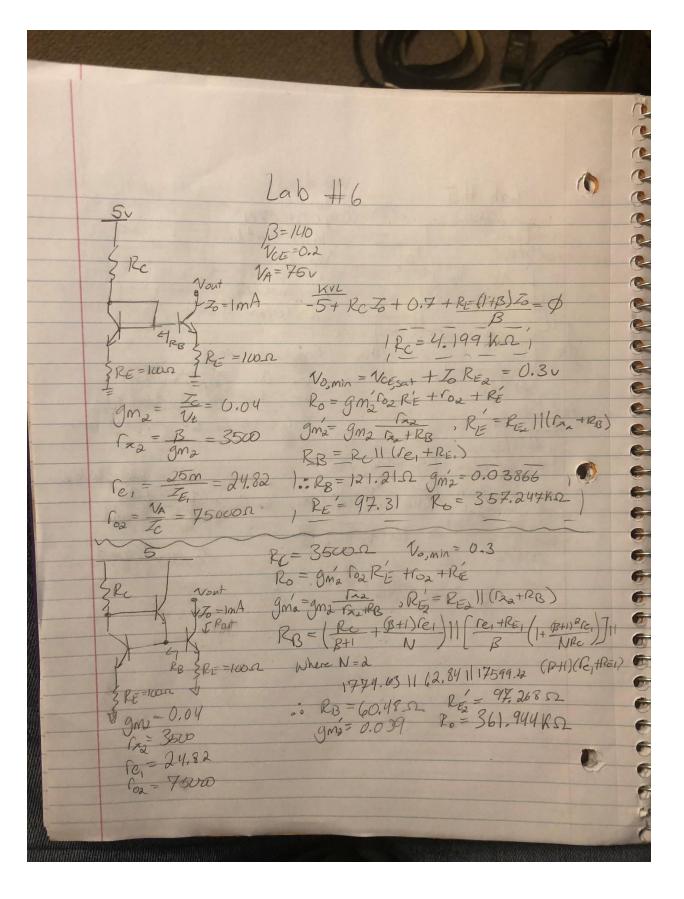
ECEN 326-501

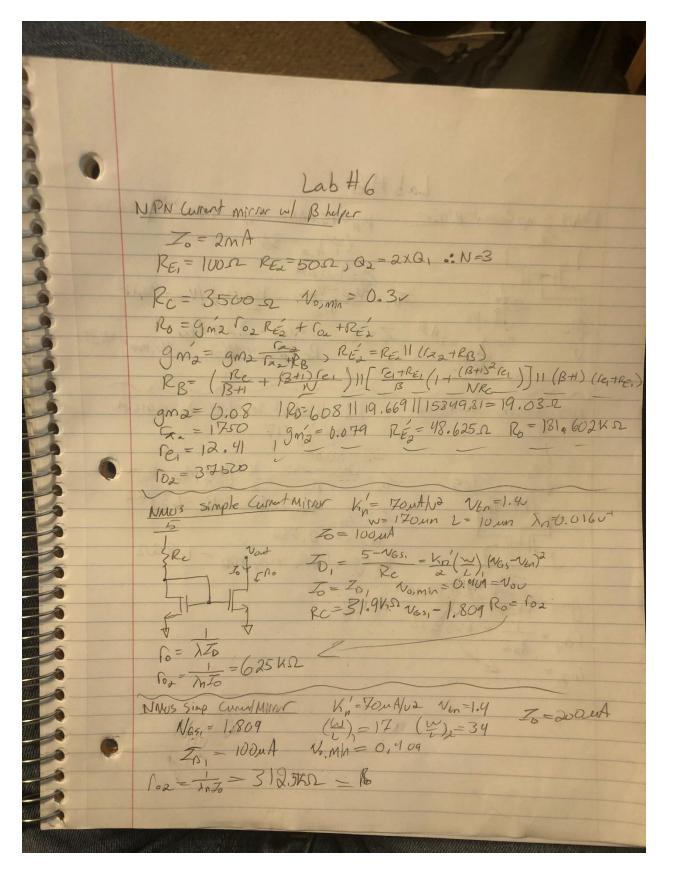
## **Lab 6: Design of Current Mirrors**

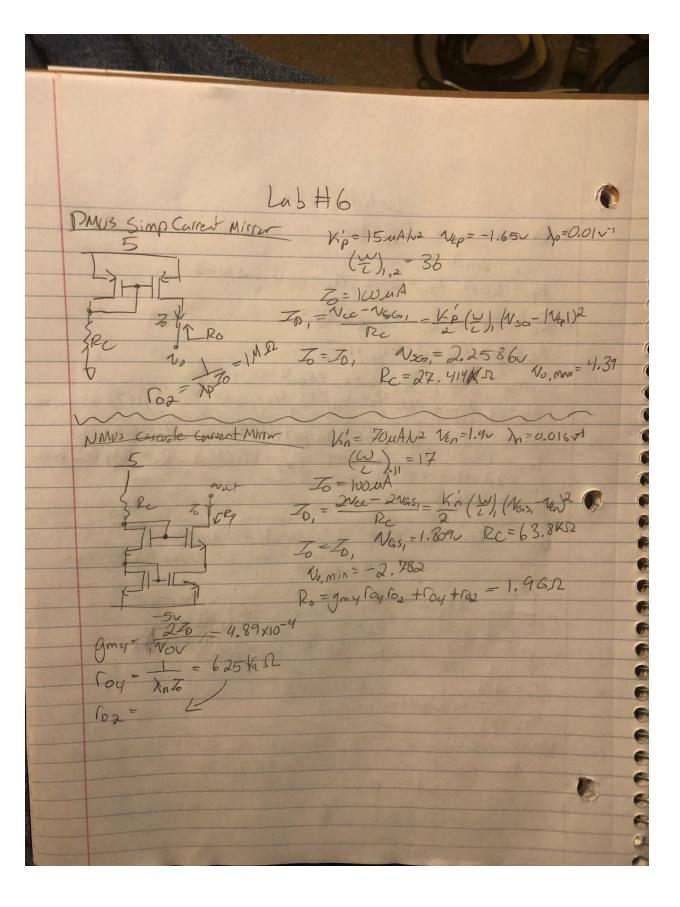
#### **Procedure:**

In this lab we constructed several current mirrors with varying designs. Each of these current mirrors have unique properties and to determine them we ran multiple calculations on each.

## **Calculations:**



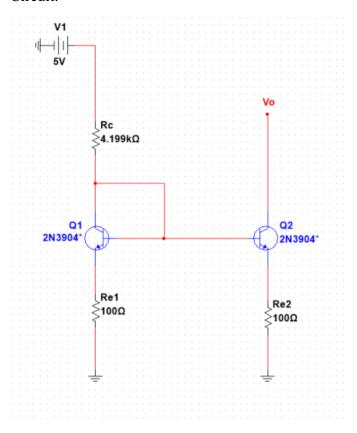




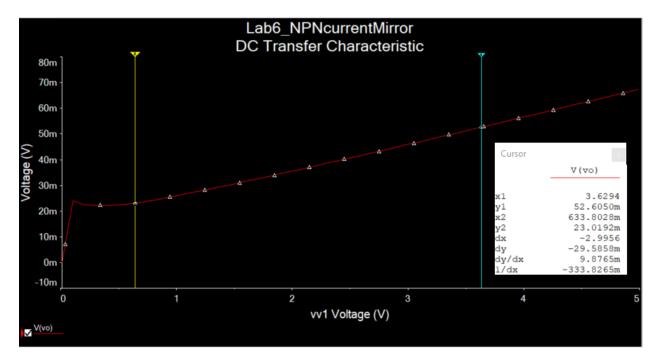
# Data:

Simple NPN Current Mirror

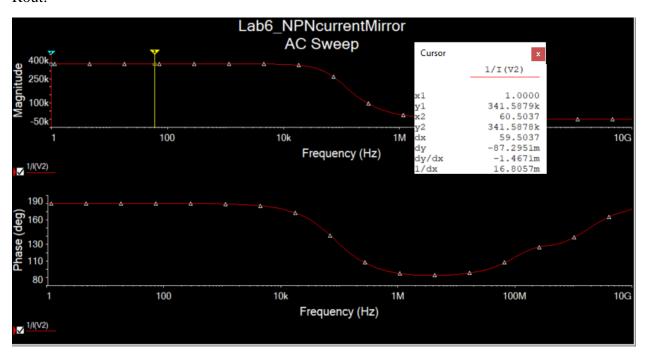
Circuit:



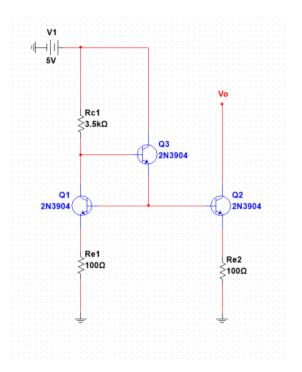
DcSweep:



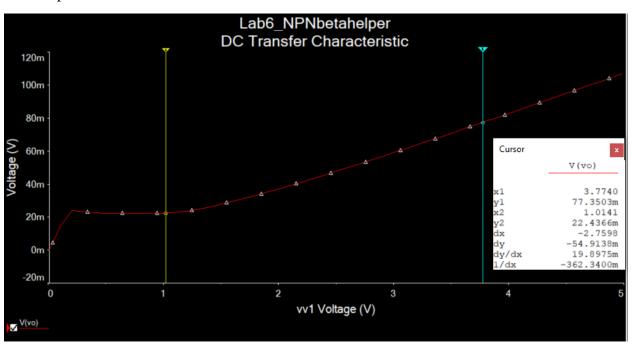
#### Rout:



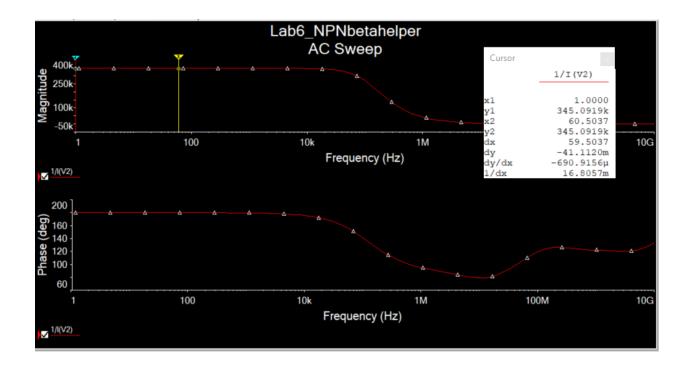
### NPN Beta Helper:



# DcSweep:

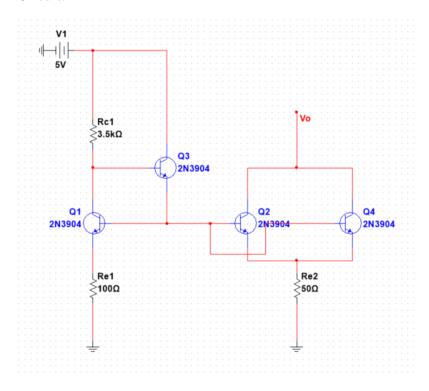


Ro:

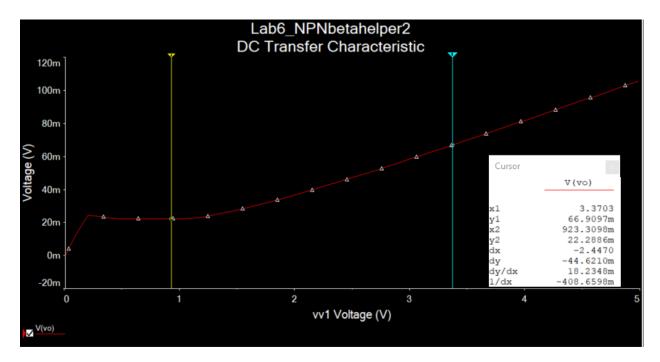


## NPN Beta Helper with second stage

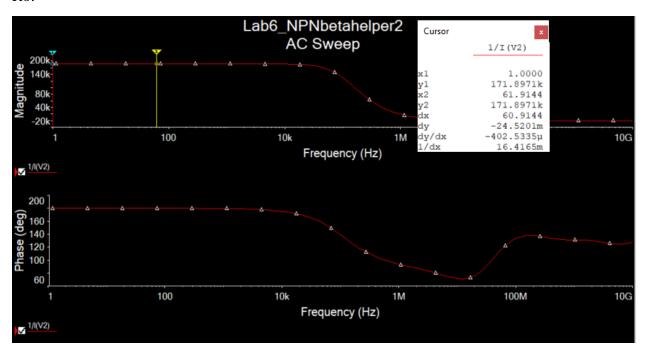
### Circuit:



DcSweep:

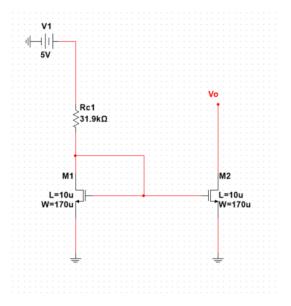


Ro:

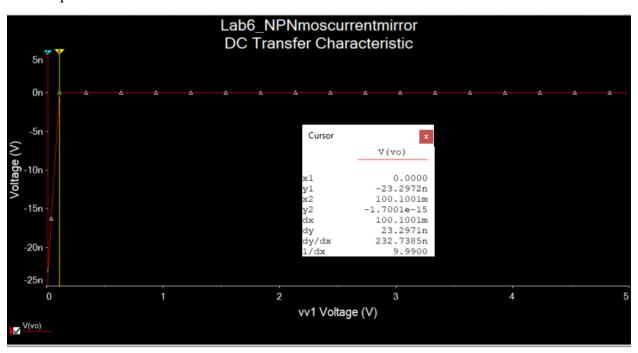


NMOS Simple Current Mirror:

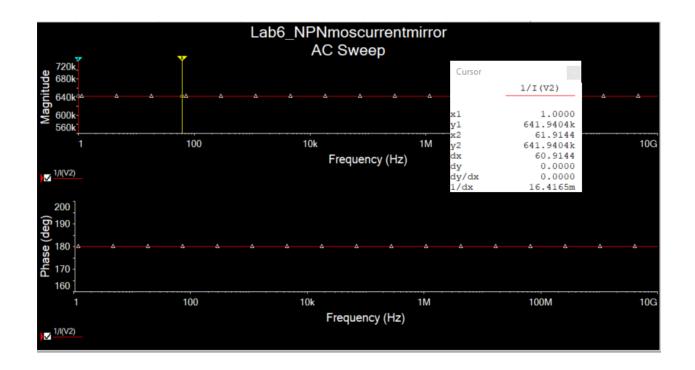
Circuit:



## DcSweep:

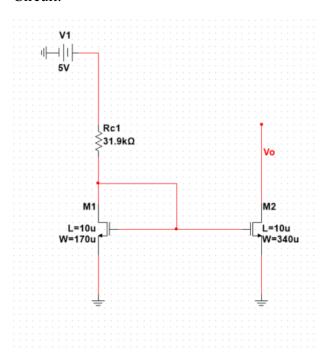


Ro:

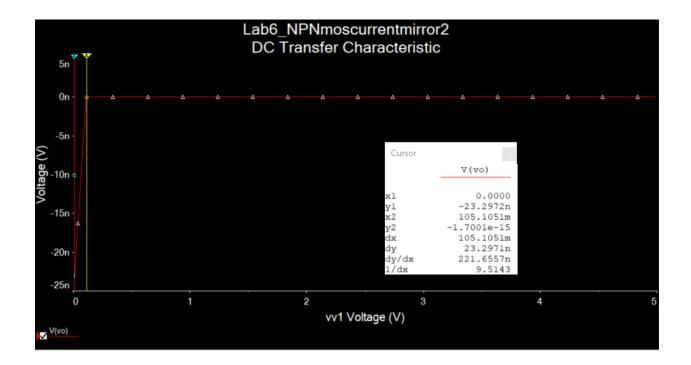


# NPN MOS Simple Current Mirror:

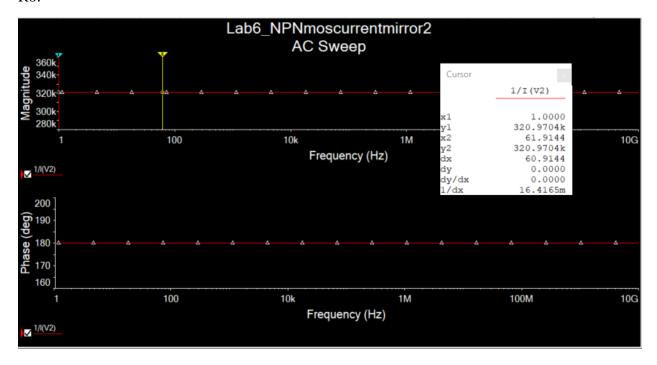
## Circuit:



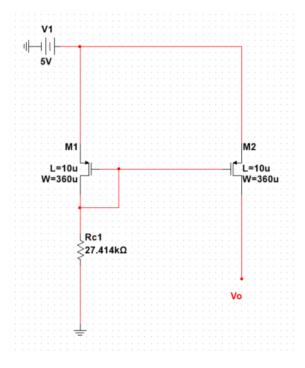
DcSweep:



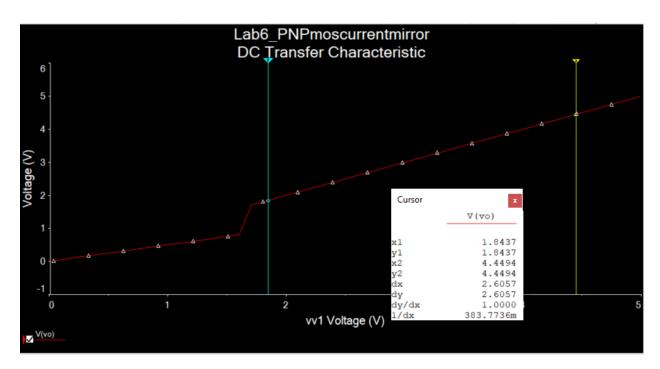
#### Ro:



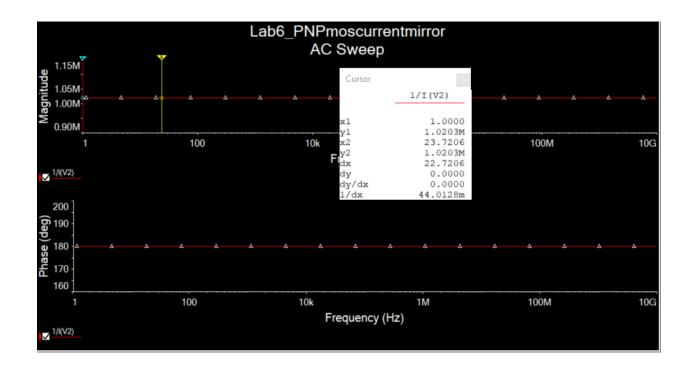
PNP MOS Current Mirror:



# DcSweep:

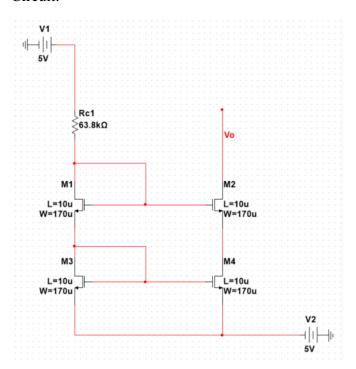


Ro:

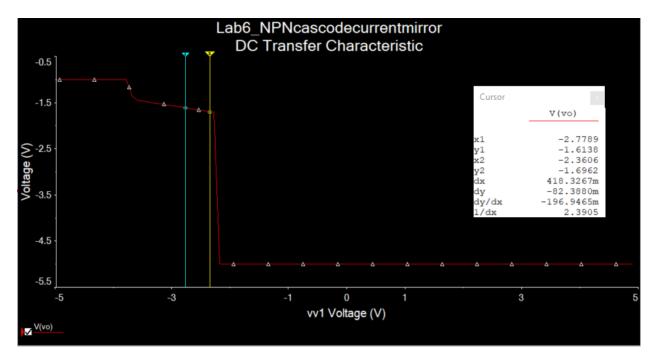


### NMOS Cascode Current Mirror

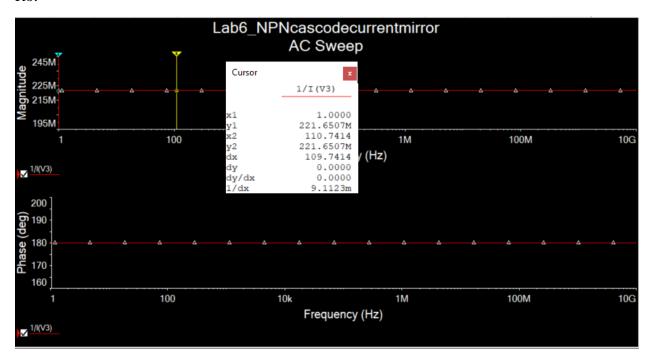
## Circuit:



DcSweep:



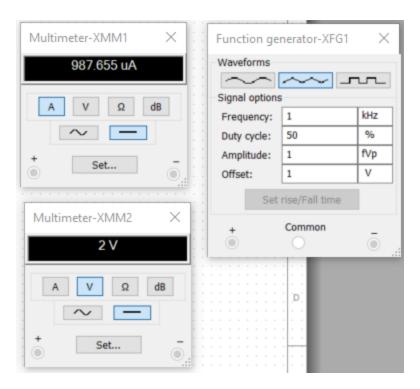
Ro:



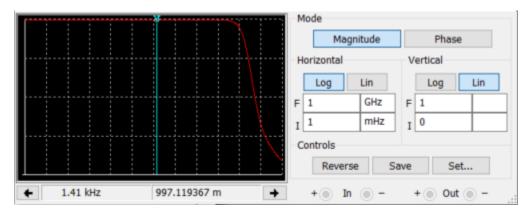
### **Measurements:**

NPN Simple Current Mirror:

Io:

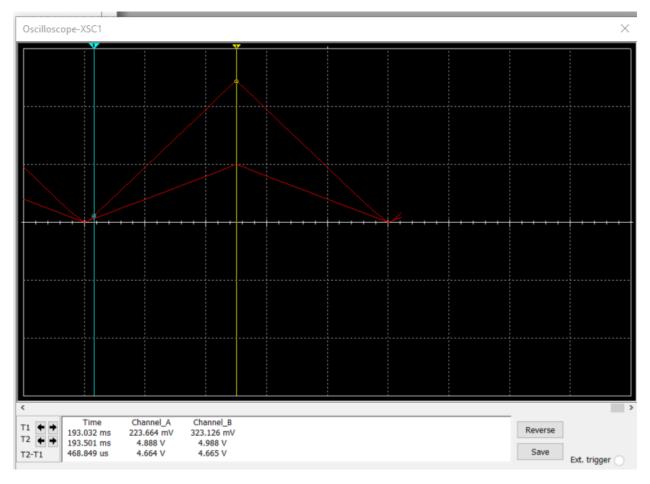


Ro:



== 332.3kohm

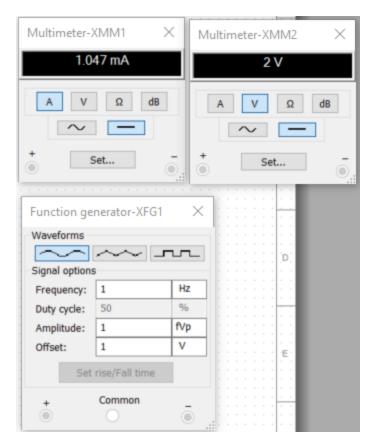
Operation Range



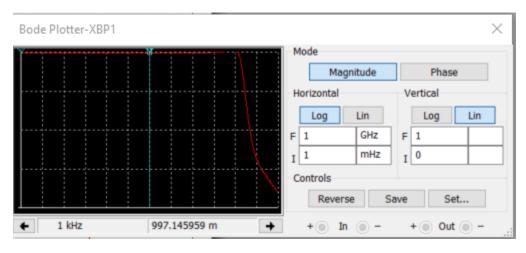
0.323v - 5v

NPN Beta helper single stage

Iout:

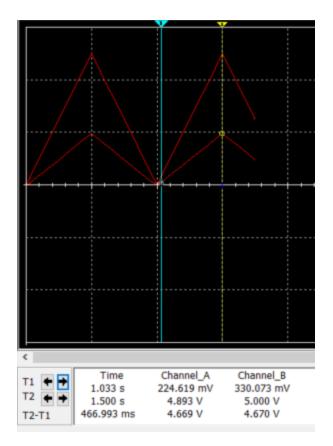


### Ro:



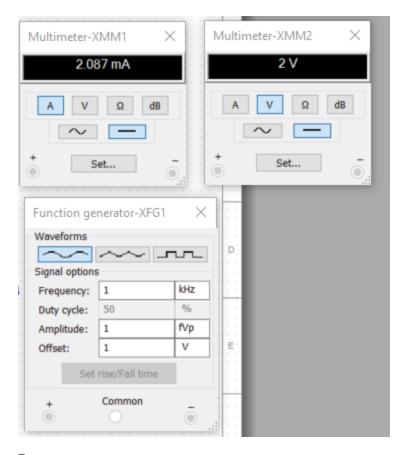
== 332.3kohm

## Operation Range:

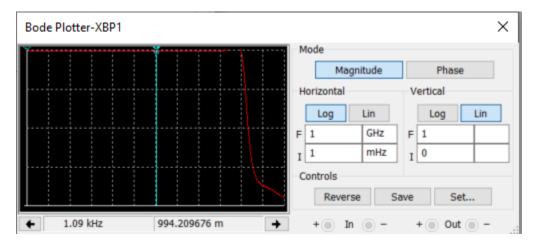


0.330v - 5v

NPN Beta Helper three stage Iout:

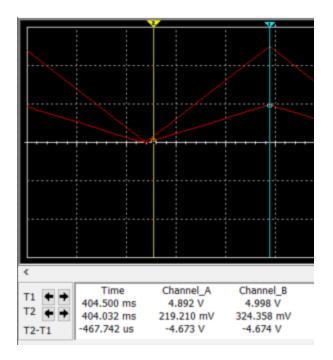


### Rout:



==165.66kohm

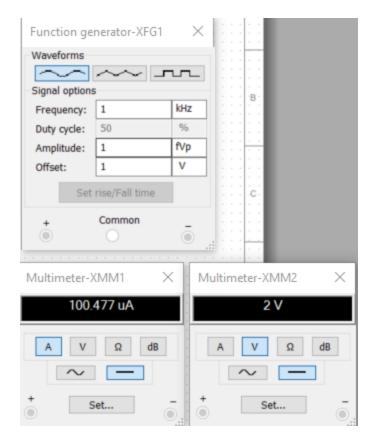
Operation Range



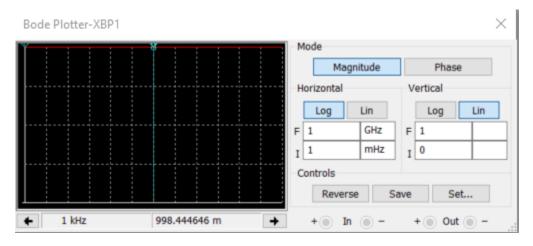
0.324v - 5v

NPN MOS Simple Current Mirror

Iout:

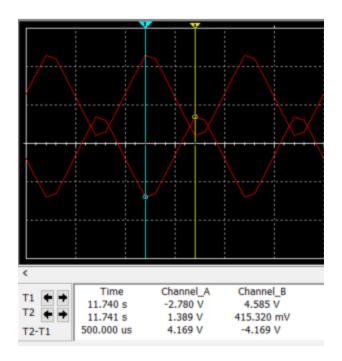


### Rout:

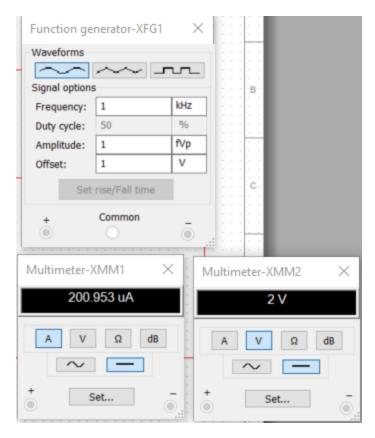


==641.67kohm

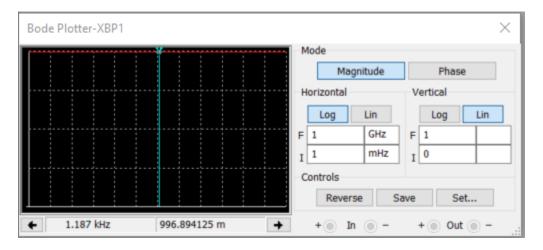
# Operation Region:



NMOS Simple Current Mirror w/ varying sized MOSFETs Iout:

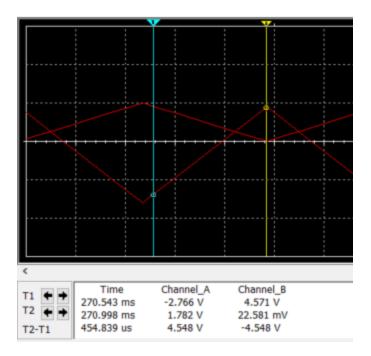


#### Rout:



==320.97kohm

Operation Region:

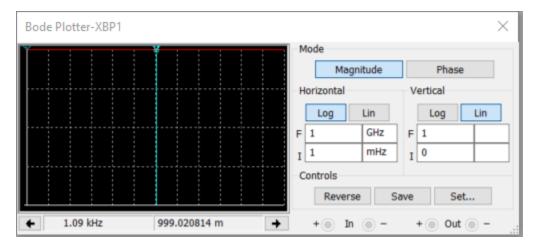


# PMOS Simple Current Mirror

### Iout:

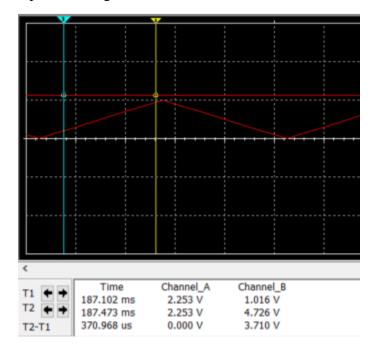


Rout:



== 1.02Mohm

# Operation Region:

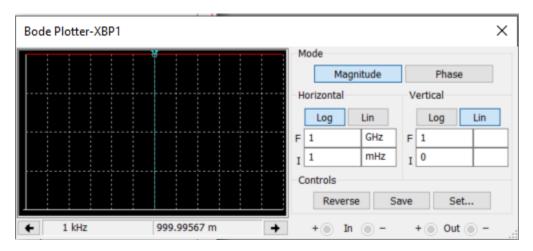


NMOS Cascode Current Mirror

Iout:

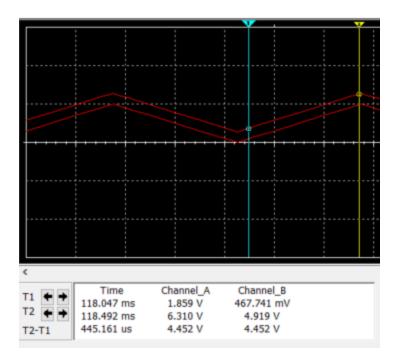


## Rout:



== 230Mohm

Active Region:



### **Results**

## NPN Current Mirror

	Calculated	Simulated	Measured
Io	1mA	9.8mA	0.987mA
Ro	357.24kΩ	345kΩ	$337 \mathrm{k}\Omega$
Active Region	0.3-5	-	0.323-4.988

## NPN Beta Helper

	Calculated	Simulated	Measured
Io	1mA	19.87mA	1.047mA
Ro	361.944kΩ	345kΩ	332kΩ
Active Region	0.3-5	-	0.33-5

## NPN Beta Helper with second cascade stage

	Calculated	Simulated	Measured
Io	2mA	19.24mA	2.087mA
Ro	181.602kΩ	171kΩ	165.66kΩ
Active Region	0.3-5	-	0.324-4.988

#### NMOS Simple Current Mirror

	Calculated	Simulated	Measured
Io	100uA	0.2uA	100.477uA
Ro	625kΩ	641kΩ	641kΩ
Active Region	1.4<1.809<5	-	1.4<1.908<4.998

### NMOS Simple Current Mirror Differing Sizes

	Calculated	Simulated	Measured
Io	200uA	0.221uA	200.953uA
Ro	312.5kΩ	320kΩ	$320.97 \mathrm{k}\Omega$
Active Region	1.4<1.809<5	-	1.4<2.015<4.98

### **PMOS Simple Current Mirror**

	Calculated	Simulated	Measured
Io	100uA	0.2uA	102.3uA
Ro	1MΩ	1.02ΜΩ	1.02ΜΩ
Active Region	1.65<2.258<4.39	-	1.65<2.456<4.39

#### NMOS Cascode Current Mirror

	Calculated	Simulated	Measured
Io	100uA	802uA	102uA
Ro	1.9 <b>G</b> Ω	221ΜΩ	230ΜΩ
Active Region	1.4<1.809<2.5	-	1.406<1.8756<2.489

Some differences in my simulation data comes from my failure to correctly measure the output current on the MOSFET designs. This is most likely a user error as the measured values is all correctly corresponding to the values calculated. Another source of change was from the last cascode mirror and the output resistances. The measured value was simple massive and while the simulated and measured value are large, not the same magnitude as the calculated value. This leads me to believe that the Multisim simulation is unable to register a value that large and caps out where my values are.