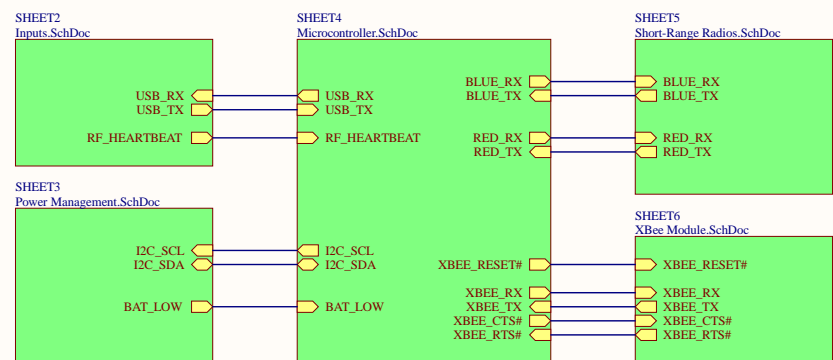


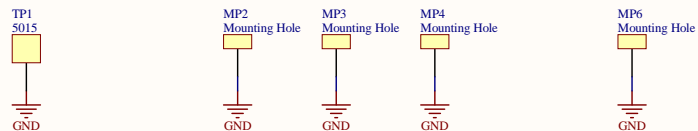
# Canberra Grammar School

## Orienteering Device



### Data Flow

If Slave: Short-Range Radios → Micro → Xbee  
If Master: USB ↔ Micro ↔ Xbee



	Title: System Overview	
	Project: CGS_ORI_SLV.PrjPeb	
	Client: Canberra Grammar School	
	Engineer: Scott Williams Sheet 1 of 6	
Revision: 0.1	5:18:25 PM 22/07/2020	

A

B

C

D

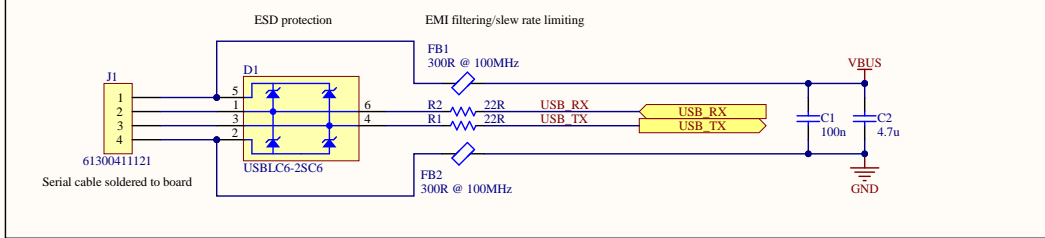
A

B

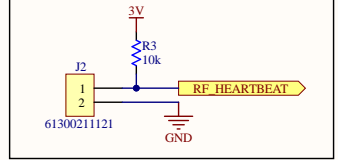
C

D

# Power & Serial Interface



# RF Heartbeat



	Title: Inputs	
	Project: CGS_ORI_SLV.PrjPcb	
	Client: Canberra Grammar School	
	Engineer: Scott Williams	Sheet 2 of 6
	Revision: 0.1	5:18:25 PM 22/07/2020

A

B

C

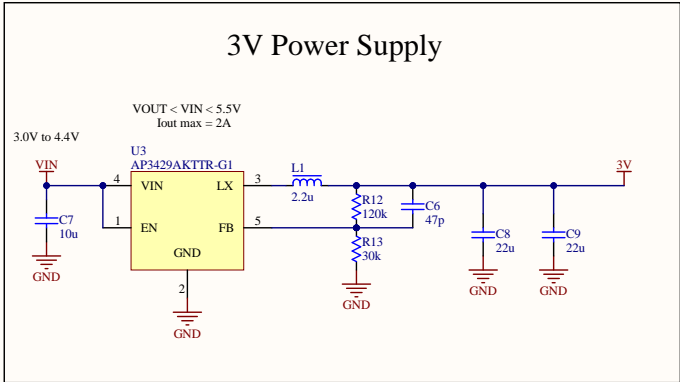
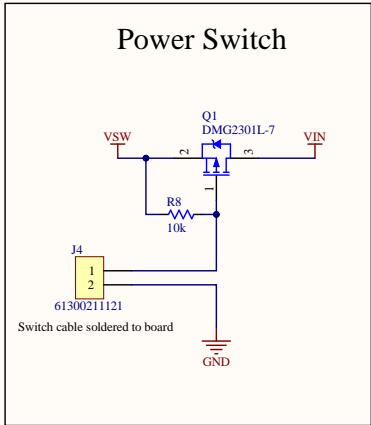
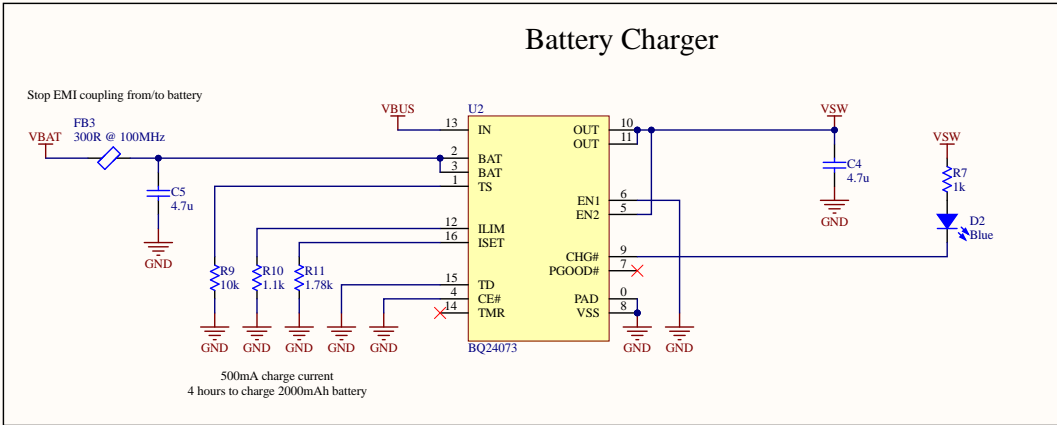
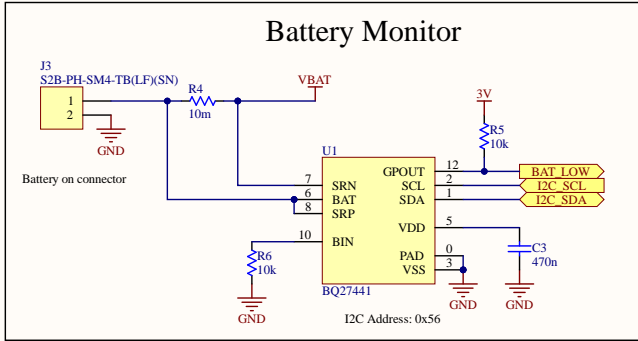
D

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