

Spatial

- Banking + Buffering
- Loop Unrolling

Nested loops with explicit memory hierarchy

Low-level IR

- VB Allocation
- VB Partitioning
- Control Allocation

Flat data flow graph + controller hierarchy

Mapping

- Back Tracking Place and Route
- Iterative Place and Route
- VC Allocation

Mapping of virtual blocks in applications onto physical blocks

Simulation

Fast cycle accurate control simulator
Integrated with DRAMSim