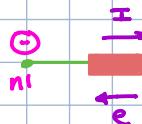


OHMS LAW = $V = I \cdot R$



Current is the flow of electrons.

For passive sign convention, current goes in the opposite direction of flow of electrons.

n1: electrons flow towards n1, therefore n1 is more negative.

n2: electrons flow away. Therefore more positive.

Getting the sign convention correct.

resistor stops the flow of current @ rate defined as I .
 $\rightarrow R$



$$\downarrow I \quad + V_A \quad - V_B \quad \rightarrow \text{Power} = |I \cdot V_B|$$

↳ load always dissipate power.

BE CAREFUL ABOUT the SIGN CONVENTION

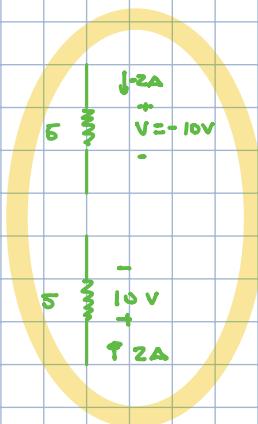
- ppl scores can go from 8/10 to 1/10 just because of sign convention.

$$V_A = -V_B \quad \Rightarrow \text{look at plus and minus terminals.}$$

$$V_B = -V_A$$

↳ Don't pull a Lamar Jackson II.
 Pull a Derrick Henry. !!

$$5\Omega \quad \downarrow -2A \quad \rightarrow V = I \cdot R \\ = (-2)(5) \\ = -10V$$



$$5 \quad \downarrow -2A \quad V = -10V$$

Note:

- $\downarrow I_A$ direction of current determine your $(+)$ terminal.
- V_B R V_A
- $+ \quad -$
- $V_A = I_A \cdot R$
- $V_B = I_B \cdot R$
- $\therefore V_A = -V_B$

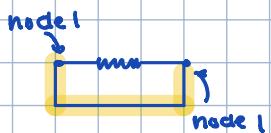
Node: Terminal ends of a load.

ex.

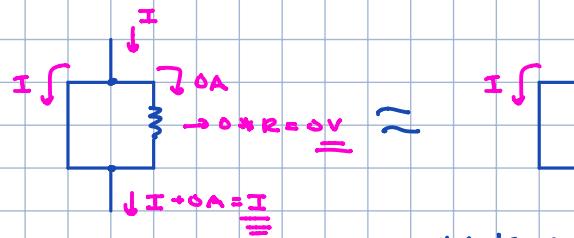


The R is the load

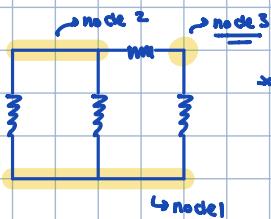
node 1 and node 2 are the ends.



Both are the same node because a wire will always have a potential of 0



* Acts as if a load does not exist.

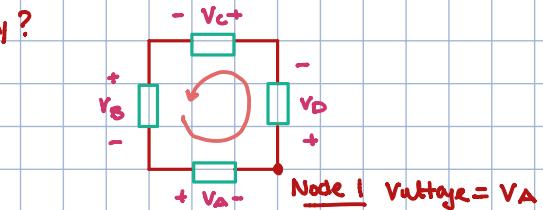


* Note: All Nodes have different voltage potential.

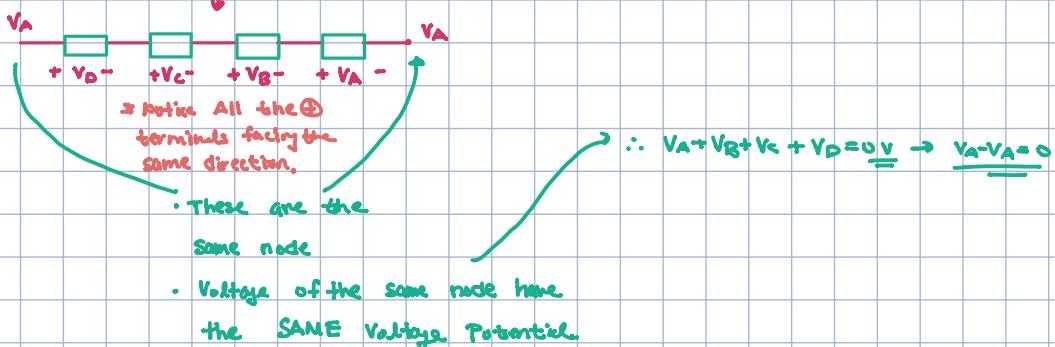
If 2 nodes have the same potential, then it is the same node.

Kirchoff's law: Voltage potential of loop equals to 0.

Why?



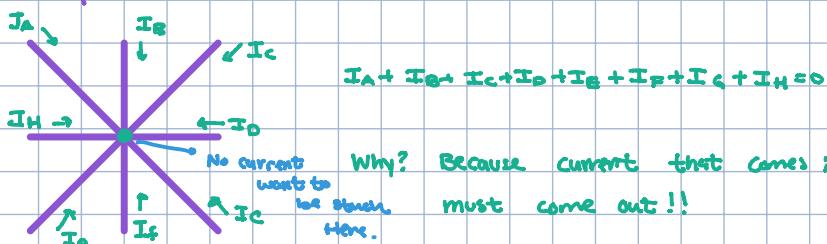
Node 1 Voltage = V_A



$$\therefore V_A + V_B + V_C + V_D = 0 \Rightarrow V_A - V_A = 0$$

- These are the same node
- Voltage of the same node have the SAME Voltage Potential.

Nodal Analysis.

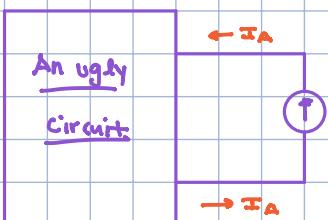


$$I_A + I_B + I_C + I_D + I_E + I_F + I_G + I_H = 0$$

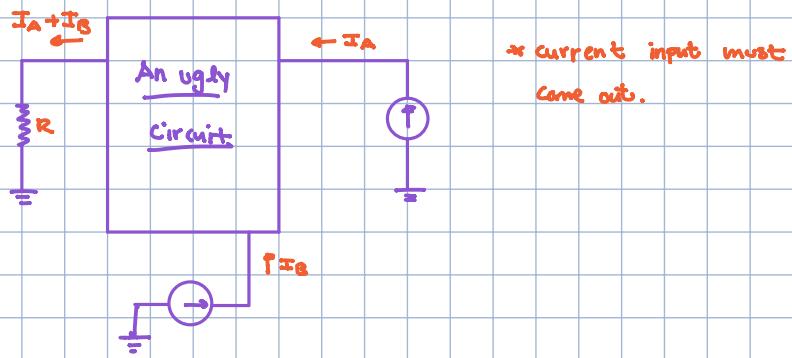
Why? Because current that comes in must come out!!

$$\therefore I_A, I_B, I_C, I_D, I_E, I_F, I_G, \text{ or } I_H$$

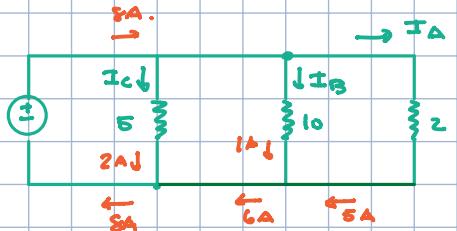
must be negative (At least 1).



* Notice the same apply to a circuit network.



How does this connect to model analysis?



$$I_A + I_B + I_C = 0$$

$$I_A = \frac{10V - 0}{2} = 5A$$

$$I_B = \frac{10 - 0}{10} = 1A$$

$$I_C = \frac{10}{5} = 2A$$

Note Current

$$I = \frac{V_A - V_B}{R}$$

THINK Va to Vb

Mesh Analysis: You perform NVL (let's go straight to an example).

Ex.

