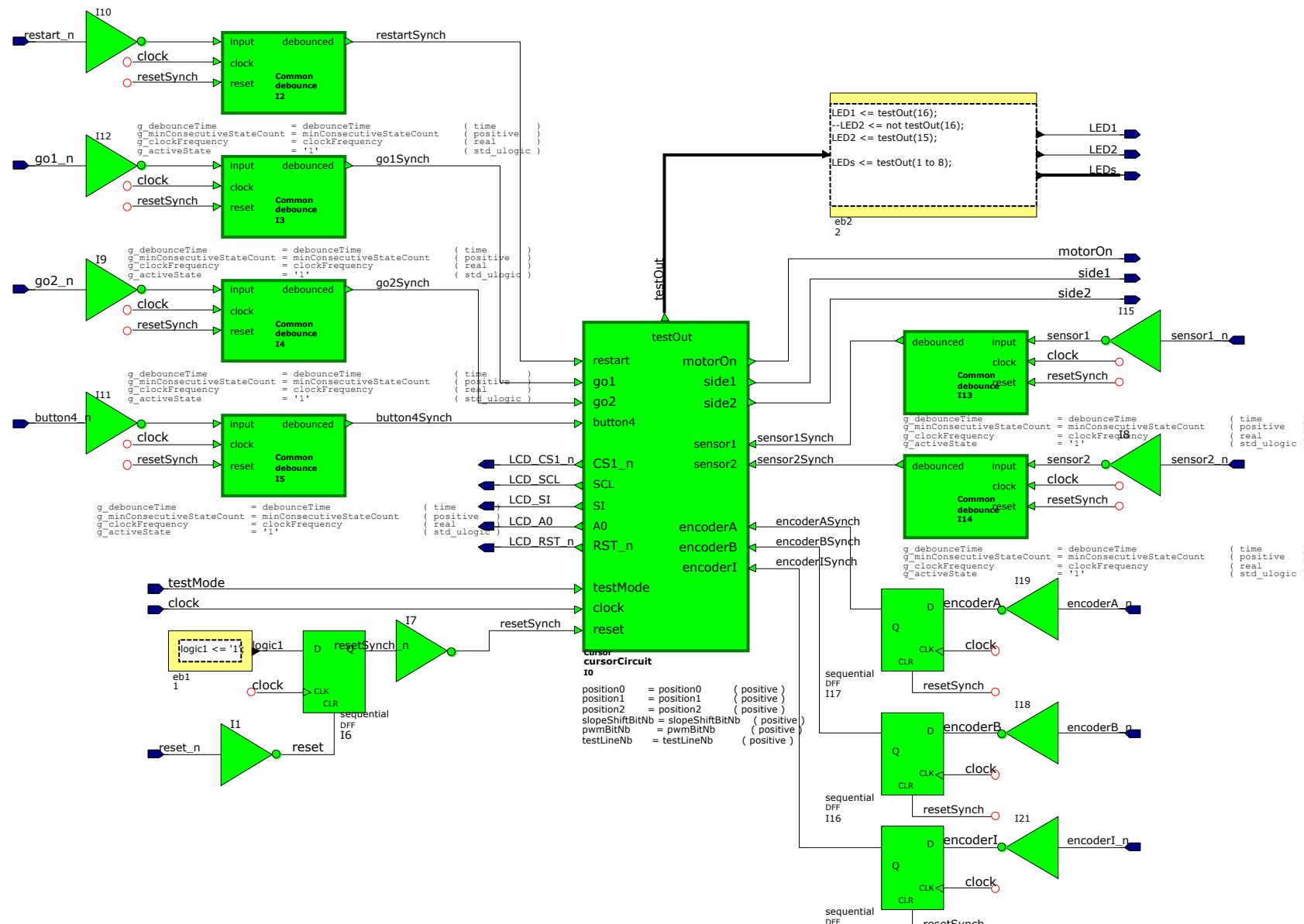


### Package List

```
LIBRARY ieee;
USE ieee.std_logic_1164.all;
USE ieee.numeric_std.all;
LIBRARY gates;
USE gates.gates.all;
LIBRARY Common;
USE Common.CommonLib.all;
```



HEI Sion		Project:	Project cursor
Title:	Full circuit logic		
Path:	Board/FPGA_cursor/struct		
Edited:	by Axam on 14 janv. 2025		

---

**Declarations**  
**Pre User:**

```
constant stepsPerTurn : positive := 500 * 4;
constant cmPerTurn : real:= 0.175;
constant position0 : positive := integer(3.5 * real(stepsPerTurn) / cmPerTurn);
constant position1 : positive := integer(8.0 * real(stepsPerTurn) / cmPerTurn);
constant position2 : positive := integer(12.0 * real(stepsPerTurn) / cmPerTurn);
constant debounceTime : time := 450 ns;
constant minConsecutiveStateCount : positive := 10;
constant clockFrequency : real := 66.0E6;
constant pwmBitNb : positive := 10;
constant slopeShiftBitNb : positive := requiredBitNb(integer(real(stepsPerTurn)/cmPerTurn+0.5)) - pwmBitNb - 1;
constant testLineNb : positive := 16;
```