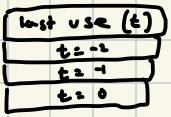
Homework 4

Matt Krueger

i	Accessed	Hit/Miss	Swapped Page
i=0	0	Hit	x
1	2	Hit	x
2	3	Miss	1
3	1	Miss	0
4	4	Miss	2
5	1	Hit	x
6	0	Miss	3
7	3	Miss	4

INIT



Least Recently Used (LRU)

!! Assumptions & process notice !!

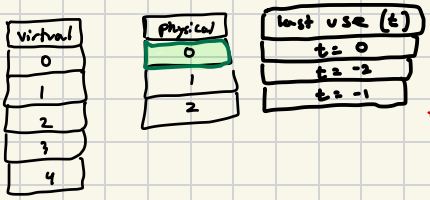
Starting Condition: "inserted in the order: 0, 1, 2" as 2 being most recently accessed.

This Also Assumes optimal LRU and not an LRU approximation

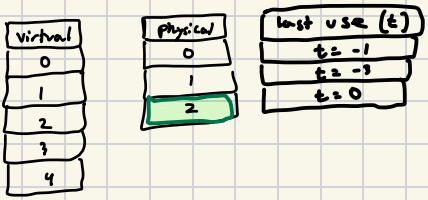
For this reason, I am tracking time since last access assuming each iteration is 1 time unit. LRU is implemented differently as stated in the book. But, for the purposes of this assignment, these assumptions should suffice.

Additionally my drawings are conceptual...

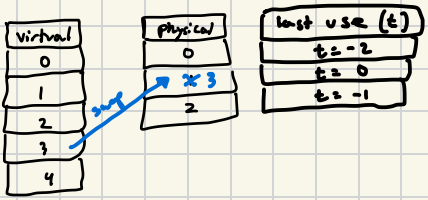
i = 0 : access 0 — Hit



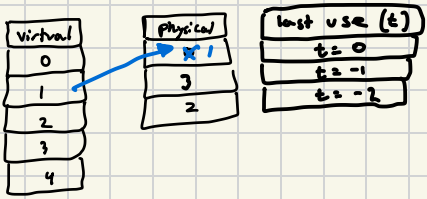
i = 1 : access 2 — Hit



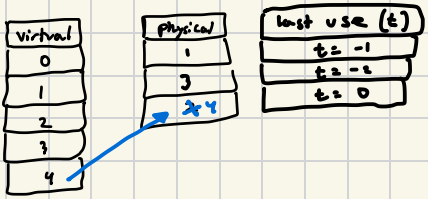
i = 2 : access 3 — Miss



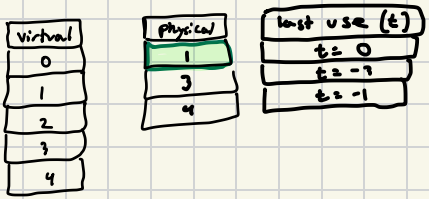
i = 3 : access 1 — Miss



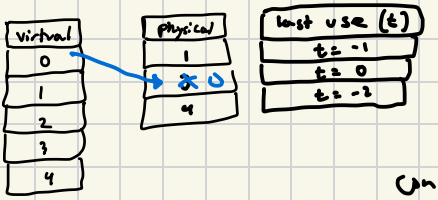
i = 4 : access 4 — Miss



i = 5 : access 1 — Hit

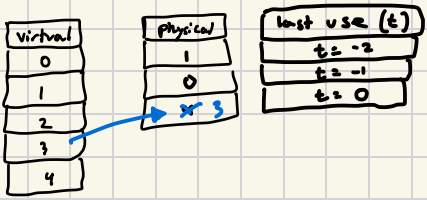
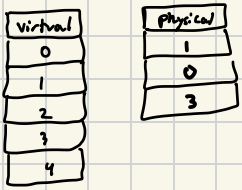


i = 6 : access 0 — Miss



Continue ...

t=7 : access Miss



Final Main Memory State