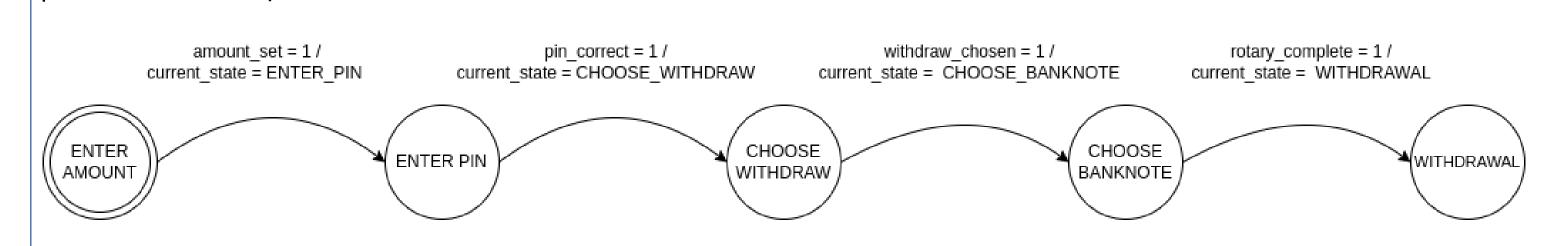
# EMP Final Assignment

## - Daniel - Mads - Valdemar

#### **System control FSM**

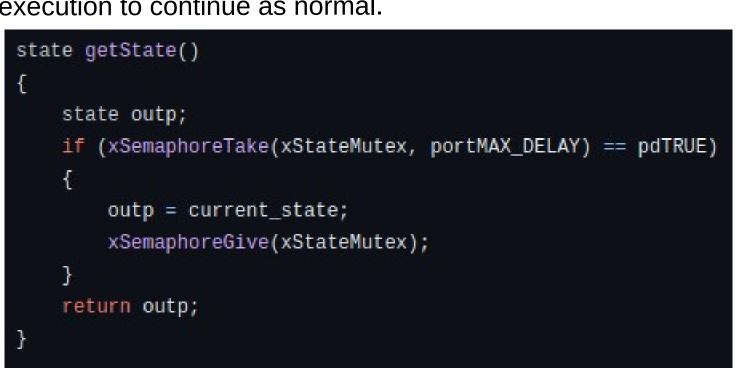
The following finite state machine (FSM) is implemented in the FSM task and serves as the "brain" of the ATM's control system, ensuring that user interactions follow a logical sequence. Each transition is driven by specific flags, set by the corresponding task for that state. This ensures secure and orderly processing of ATM transactions. The design creates a linear and predictable user experience.

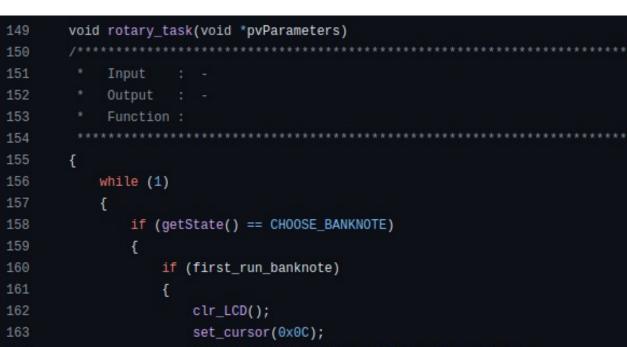


The transitions are driven by the flags "amount\_set, pin\_correct, withdraw\_chosen, and rotary\_complete". These are set within the task responsible for operations in the current state. For example, when the system is in the "ENTER AMOUNT" state, the task responsible for receiving the withdrawable amount from the user has control of the system until it sets the "amount\_set" flag. This indicates to the control FSM that the task is finished, which subsequently transitions the system to the "ENTER PIN"

#### Task control system

The current state from the system control is what decides if a task is allowed to run. Each time a task starts an execution cycle it checks if the current state matches the one where the task is supposed to run. This is implemented using a getState method as shown in the figure below. The getState method works by taking the mutex, protecting the current state and returning the state. If the state is not the one intended for that task, it breaks out of that execution cycle immediately. Otherwise, it allows the execution to continue as normal.





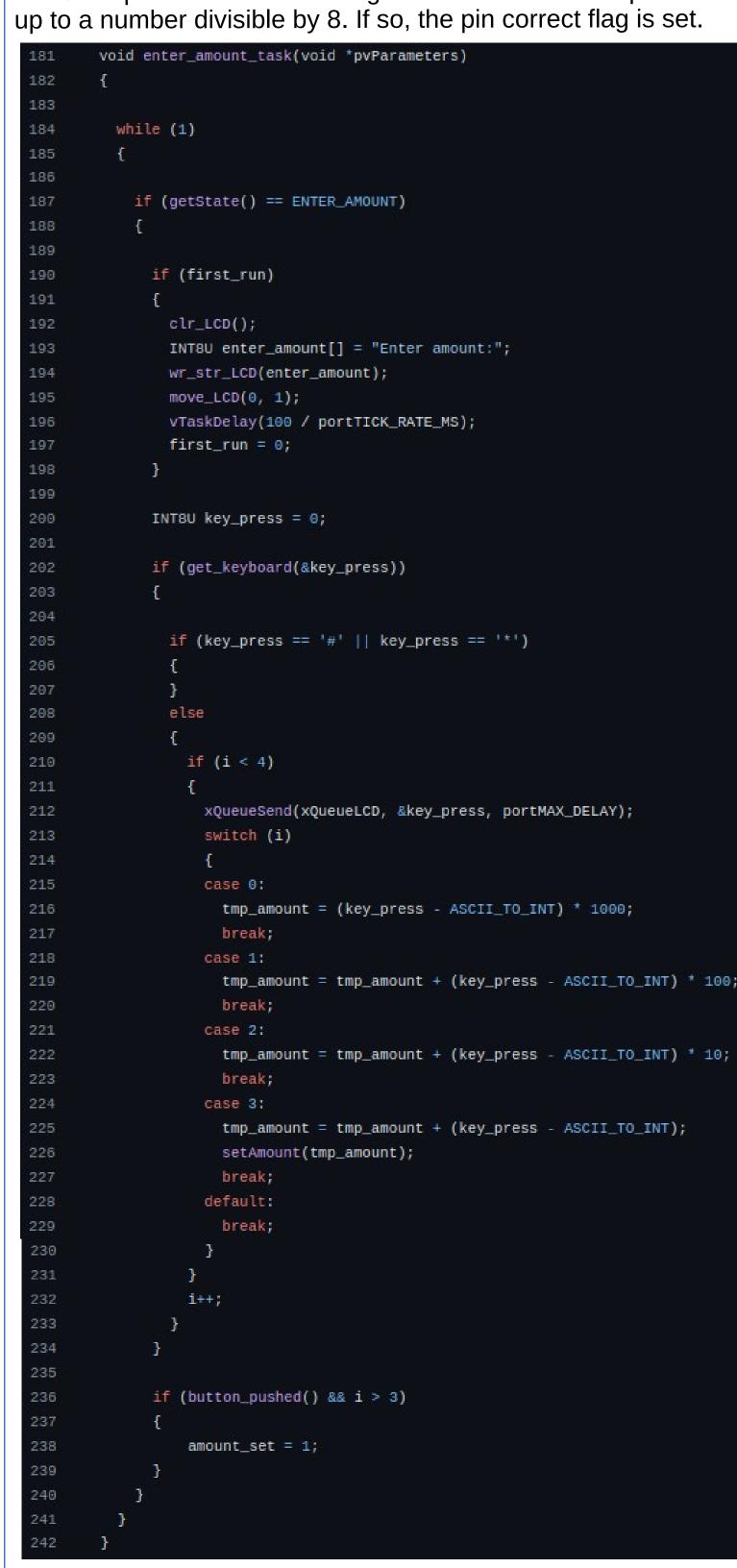
The figure to the right shows an if statement in the start of the task that makes sure the current state is correct before proceeding with execution.

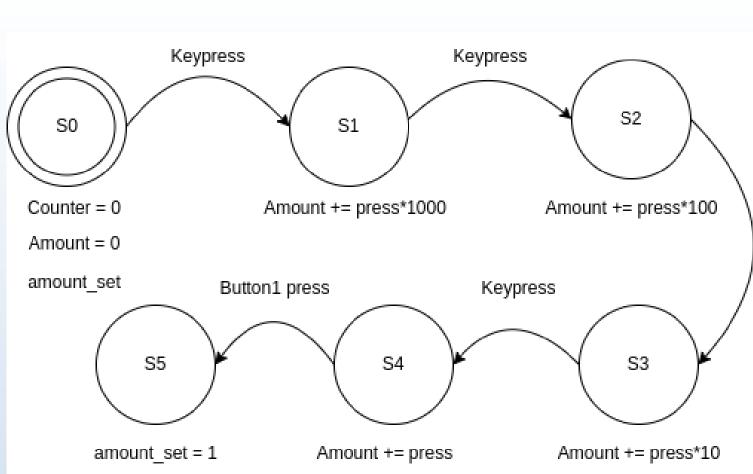
The same approach is used for all five tasks that correspond to a state.

#### **Enter amount & enter pin tasks**

The "enter amount task" and "enter pin" tasks work similar to each other. In each execution cycle they take an input from the keypad queue. It is made sure that it is an appropriate key before entering a switch case. The switch statement is dependent on what the current cycle number is. For all cases the key will be converted from an ASCII character to an integer. Depending on the cycle number, the integer will be multiplied by a thousand, a hundred, ten or one before being summed to the "amount\_value" or "pin value". After four runs, the value is stored. If it is the amount task that is running, it starts looking for a button press to set the transition flag.

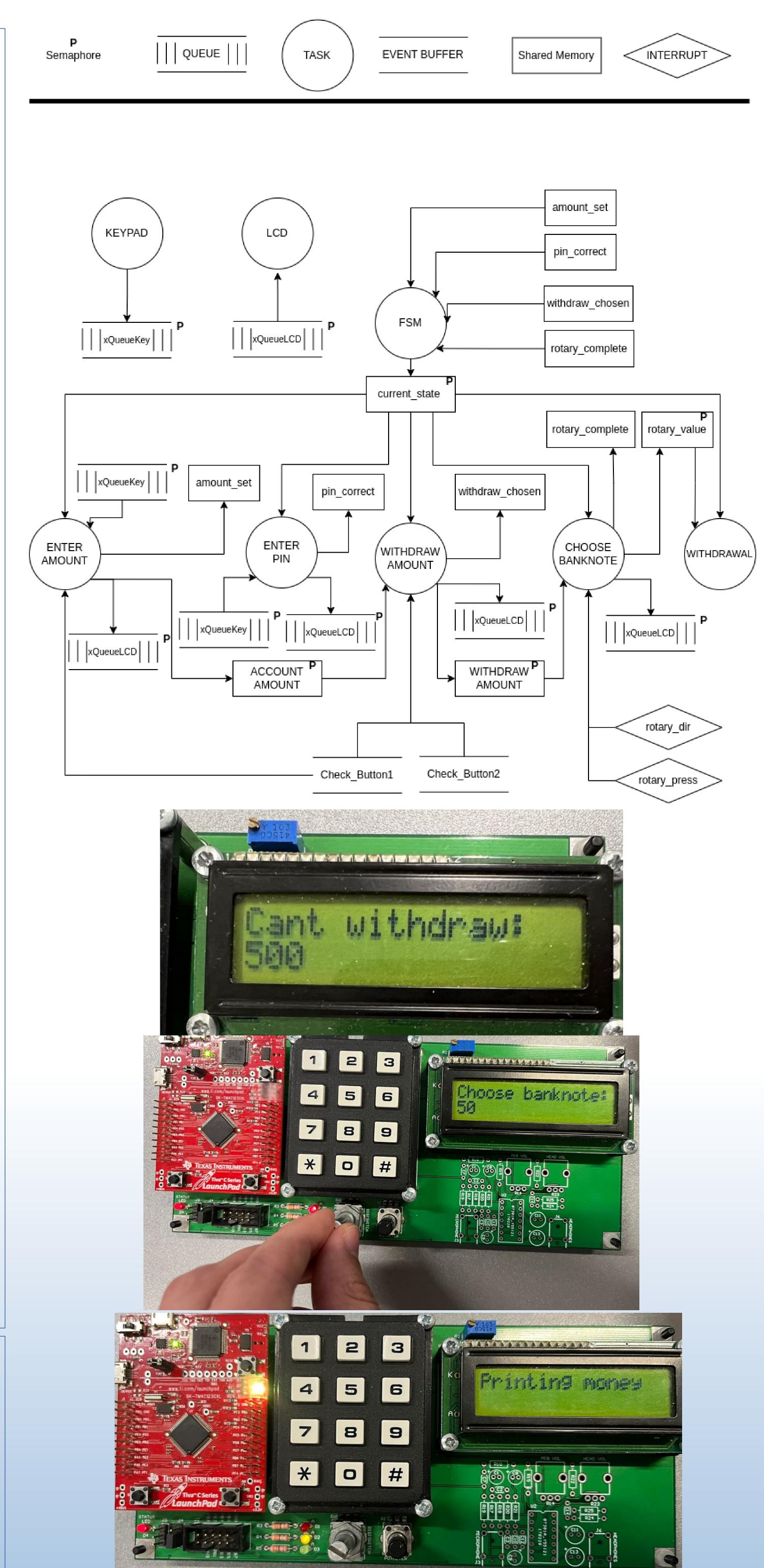
If it is the pin task that is running it checks if the four inputs add





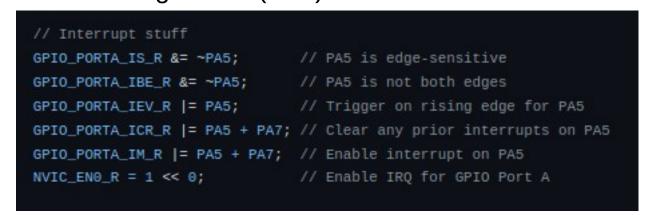
#### **Full demonstration video link:**

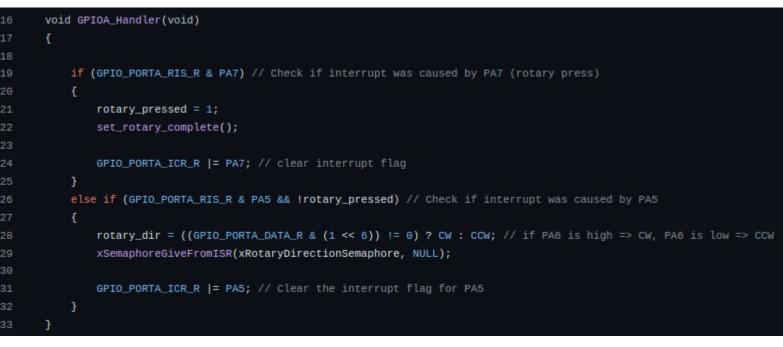




### **Rotary encoder**

The rotary encoder is implemented using interrupts. It is setup to send an interrupt on the rising edge of switch pin A (PA5) and on a press on the digiswitch (PA7).

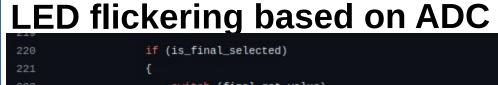




The "else if" on I. 127 is responsible for handling interrupts from the digiswitch turning. It works by checking the value of switch pin B to determine if the interrupt came from turning clockwise or counter clockwise. It then uses a semaphore to signal the task that is responsible for keeping track.



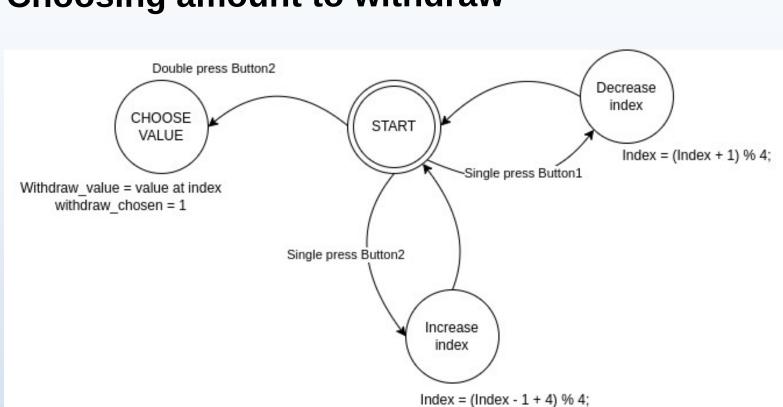
This task takes the semaphore signal and updates the rotary value based on the direction. It cycles through the values 0, 1, and 2 depending on the direction set by the interrupt handler. This rotary value is protected by a mutex and is used as an index in an array consisting of the different banknote options.



```
switch (final_rot_value)
   GPIO_PORTF_DATA_R ^= 0x02;
   GPIO_PORTF_DATA_R ^= 0x04;
INT16U delay_ms = MAX_DELAY_MS - ((adc_value * (MAX_DELAY_MS - MIN_DELAY_MS)) / MAX_ADC_VALUE
vTaskDelay(delay_ms / portTICK_RATE_MS);
```

delay\_ms determines the frequency for which the LEDs flicker. The ADC gives a value from 0-4095. The max. delay is 1000 ms resulting in 1 Hz. The min. delay is 100 ms resulting in 10 Hz. However, in practice this seems to be incorrect. It is suspected that various tasks running in the background may increase the delay in an unpredictable way.





Choosing the amount to withdraw is done using the two buttons. Button 1 cycles left and button 2 cycles right. They update a value that is used to index into an array of the different withdraw amounts allowed. A double press on button 2 confirms the chosen amount.