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THE UNIVERSITY OF QUEENSLAND
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FPGA Web Interface and Ethernet Network Controller using the RISC-V Processor

Project Proposal

Matthew Gilpin

45801600

The University of Queensland, 2023

REIT4841 - School of ITEE

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- Dedications (if applicable)
- Table of Contents
- List of Figures and Tables
- List of Abbreviations used in the thesis
- Main text of the thesis
- Bibliography or List of References
- Appendices

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Dedication (if applicable)

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List of Abbreviations and Symbols

Abbreviations

AC	Alternating Current
AFM	Atomic Force Microscopy/Microscope
<i>etc.</i>	<i>etc.</i>

Symbols

$\hat{\rho}$	Density operator
<i>etc.</i>	<i>etc.</i>

Chapter 1

Introduction

1.1 Your thesis topic

Introduce your topic.

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1. [1] **Your Name**, Co-author 1, and Final Author, Title of your paper, *Journal* Issue, Number, Year

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Chapter 2

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Chapter 3

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Appendix A

Appendix

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A.1 Name of Appendix-1

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