

GSE_SEL2	A1	GSE_IO		GSE_IO	B1	GSE_SEL3
GSE_SEL0	A2	GSE_IO		GSE_IO	B2	GSE_SEL1
GSE_RESET	A3	GSE_IO		GSE_IO	B3	GSE_DISARM
GSE_PGED	A4	GSE_PROG		GSE_PROG	B4	GSE_PGEC
GSE_TLM	A5	GSE_UART		GSE_UART	B5	GSE_CMD
PL2_TLM	A6	PL2_UART		EPS_IO	B6	EPS_RESET
GSE_SDA	A7	EPS_I2C		EPS_I2C	B7	GSE_SCL
SYS_REG_3V3	A8	SYS		GND	B8	GND
RSW_INHIBIT	A9	INHIBIT		INHIBIT	B9	BAT_INHIBIT
PWR_SAFE	A10	FSW		GND	B10	GND
PWR_SAFE	A11	FSW		GND	B11	GND
PWR_UNSF	A12	FSW		GND	B12	GND
PWR_UNSF	A13	FSW		GND	B13	GND
BAT_SAFE	A14	FSW		GND	B14	GND
BAT_SAFE	A15	FSW		GND	B15	GND
BAT_UNSF	A16	FSW		GND	B16	GND
BAT_UNSF	A17	FSW		GND	B17	GND