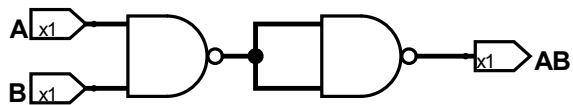


Organización del Computador I

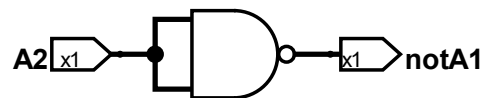
Guía práctica 2

Ejercicio 4

AND usando dos NAND

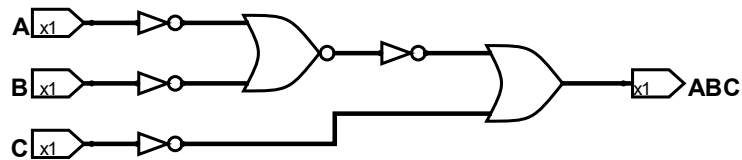


not A usando NAND



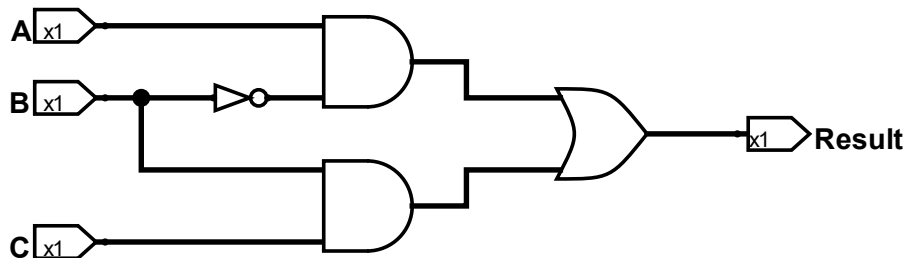
Ejercicio 5

A AND B AND C usando NOR y NOT



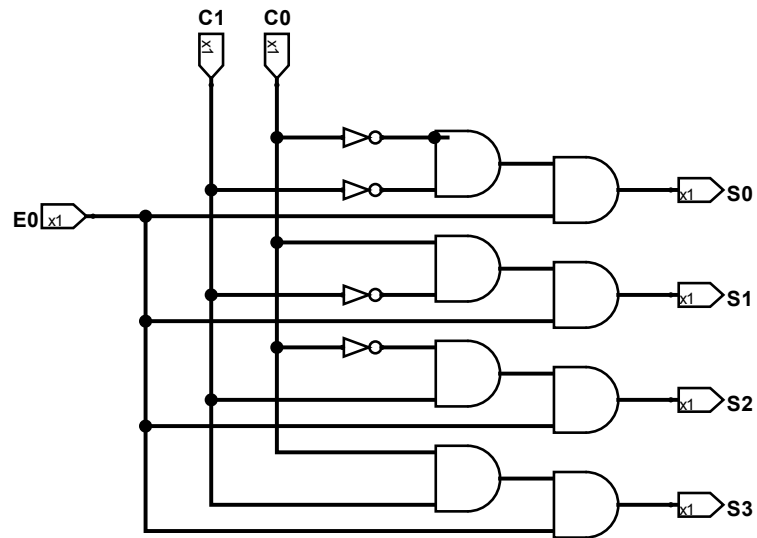
Ejercicio 6

$BC + \neg BA$



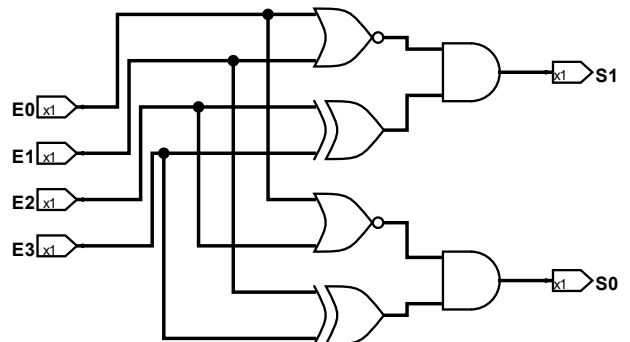
Ejercicio 7

Demultiplexer



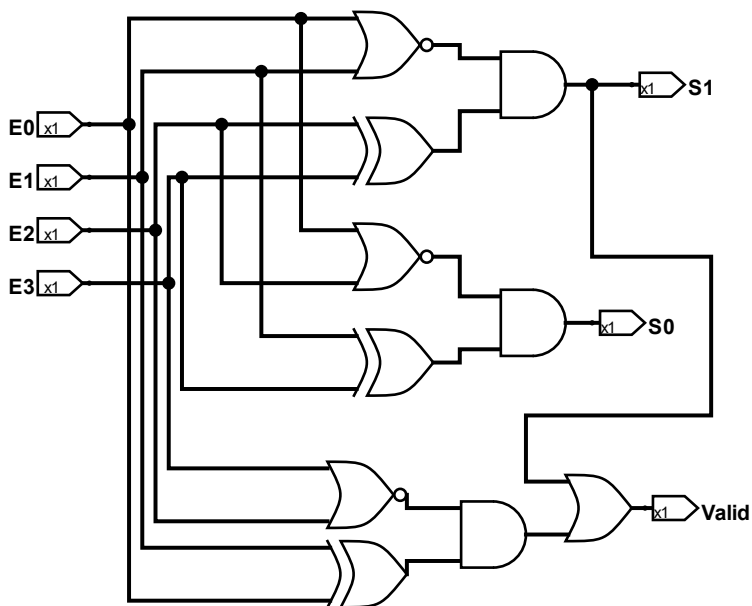
Ejercicio 8 – A y B

Decoder

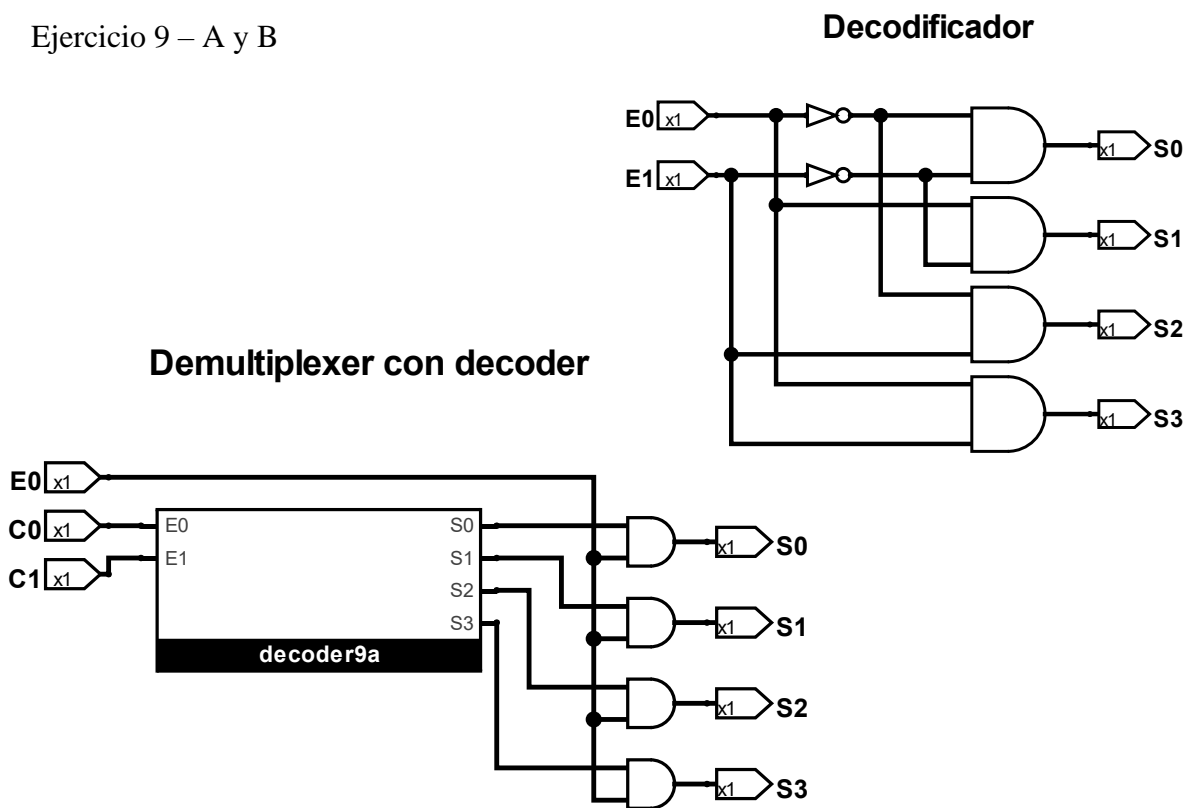


Decoder

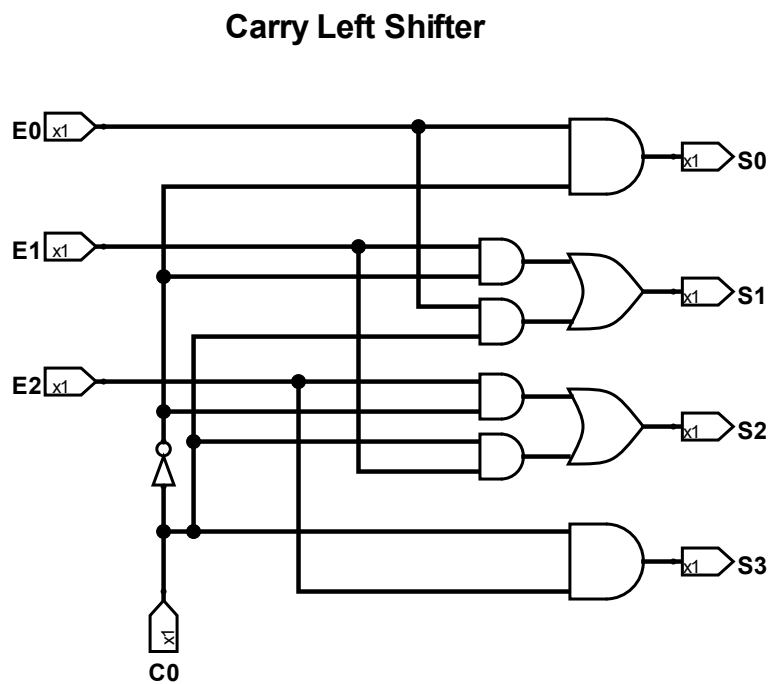
Valid = 1 si es una entrada valida, 0 si no



Ejercicio 9 – A y B

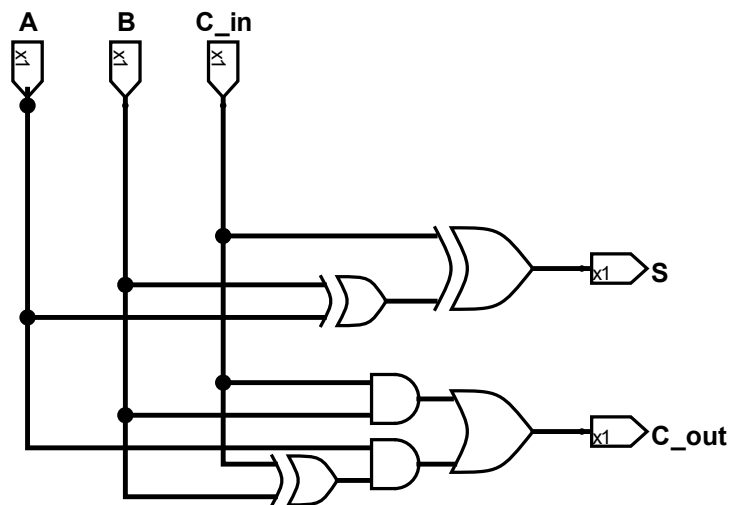


Ejercicio 10



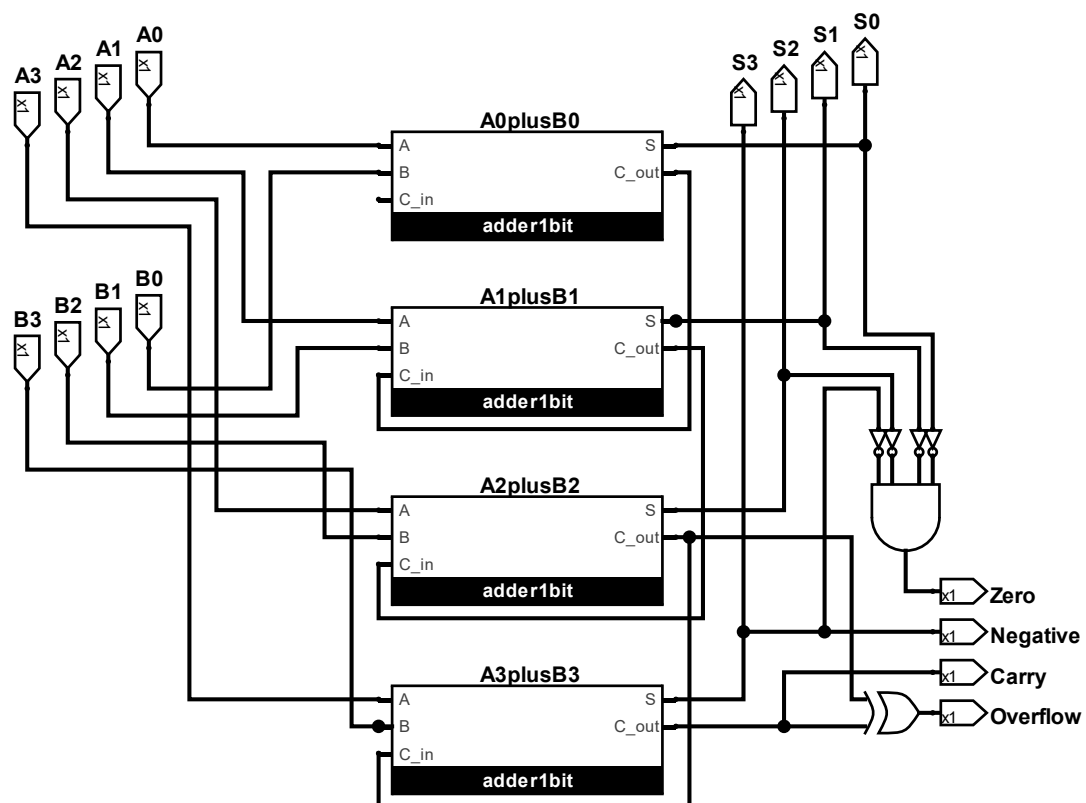
Ejercicio 11

Full Adder de 1 bit



Ejercicio 12 Full Adder de 4 bits

Full Adder de 4 bits



Ejercicio 13

Inverso Aditivo en complemento a 2

