

School of Computer Science and Engineering Pusan National University

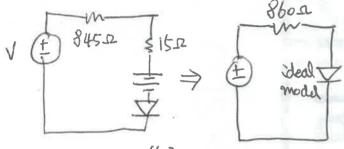
スレフスストとらとフトラーラとりによん

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a) XL = j2/1/L = j2-3-5k.4m = j1202 4. a) BJT: Bithary Junction c) Z = R+ (XL+Xc) = 100 + j20 2 d) Y=G+(B+B)

 $=\frac{1}{100}+j\left(-\frac{1}{170}+\frac{1}{100}\right)$ = 100 +) 600 7

?. If we apply a piecewise wise linear model diode to the circuit



a) ID = 413 = 5 mA 1026

b) Vo = 0.7 + 15.5 x 103 = 0.795V

3. Integrator (时知之从对对社 心思知社

$$\alpha) \quad \hat{k}_i + \hat{k}_f = 0 \quad \therefore \frac{v_i}{R} + C\frac{dv_i(t)}{dt} = 0$$

: V.4) = - Re (W(+) of = - 1 (v: 4) dt 102

b) 1.41= \-\frac{1}{4}t, 0= t < 8

-16-4(+-8)+(+-8), 9=+=10 + [4] other style was of the other of the style of Victori

Transistor by

MOSFET: Metal Oxide Semiconductor Field Effect Transistors

b) BJT controls the current through it by input ament. FET controls the Current through it by the input voltage

5.0) Vp4 =0 ⇒ Io= 4mA Ip =0 => Vps = 8V Ip [ma]/

b) Vas=VDD RI+R2 = 8. 3K+5K = 3 [V]

: Q-point 2 (2mA, 4V)