

SOFTWARE ENGINEER

Experience

Software Engineer

June 2013 to Current Hcl Technologies Ltd. 1/4 Schenectady, NY

- Programming language used is C, scripting experience with python Focused on Layer 2 data forwarding in one of Cisco's flagship products, the Nexus 7000(N7k).
- Expertise in mac table management on the Supervisor & Linecard Experience working with 3 generations of N7k supervisors & 8 generation of Line cards Features supported include Vlan, Vdc, vPC, conversational learning, fabricpath, vPC+, Fabric extender(FEX), port security, HSRP, OTV, L2VPN, Vxlan, SVI, FCOE over FEX, FEX Active-Active, phy vPC & phy vPC+ Improved component design to improve system performance Successfully led the efforts to bring up L2 unicast packet flow in the next generation N7k Linecard using Cosim which required collaborating with hardware, driver and a number of software component teams Filed 32 bugs in the process of verifying L2 unicast packet flow Debugged 59 bugs and an even larger number of other issues over the last 1 year Experienced in debugging issues on live customer setup Collaborated with multiple teams to improve overall system performance and convergence in scale setup.
- Experienced in debugging L2 forwarding issues on complex topologies Designed Auto-correcting Mac table in a 1 day Cisco Hackathon event.

Software Engineering Intern

June 2012 to August 2012 Intel Corp. 1/4 San Diego, CA

- Verifying the L2 unicast packet flow in one of Cisco's flagship products, the Nexus 7000.
- Documented the flow mechanism required for running simulations to help future new hires.
- Automated test cases for future reusability.

Senior Design Engineer

July 2006 to July 2011 Johnson & Johnson 1/4 Spring House

- Contributed as an RTL verification engineer for 4.5 years and an RTL Design Engineer for 0.5 years.
- As a team, engineered the 3G baseband of a wireless modem for a customer, Image processor and Graphics Processor going into the OMAP platform used by many of the current smartphones in the market.
- Authored Verification Plans for multiple design modules with clear objectives and pass/fail criteria.
- Responsible for successful debug at a crucial phase of my first project, which built on the trust which TI had with its customer Accomplished technology transfer in TI, Tokyo for 3 important modules in a short span of time.
- Pioneered a new RTL verification methodology and demonstrated its potential.
- Subsequently, this was used across different projects.
- Completed change request verification of 4 different modules simultaneously to meet the overall design schedule Collaborated with TI sites across India, US, France and Japan to solve critical bugs.
- Authored flow methodology documents for seamless reuse by the team.

Hardware Engineering Intern

February 2006 to May 2006 Bio-Techne 1/4 Detroit

- Designed & verified a Discrete Cosine Transform module.
- Corrected mistakes in the algorithm given in a white paper that I had used.

Education

Masters : Telecommunications, May 2013 University of Maryland College Park, Clarke School of Engineering & Smith School of Business College 1/4 City, State Telecommunications

Bachelor of Engineering : Electronics & Communications Engineering, June 2006 GPA: First Class Honors Electronics & Communications Engineering First Class Honors

Telecommunications & Business courses GPA: GPA: 3.7/4.0 GPA: 3.7/4.0 GMAT 720/800; GRE(Math) 790/800 Entrepreneurship Experience: Working with Dingman Center for Entrepreneurship to make "Coffee Cup 2.0" a reality. Authored a business plan to re-launch a popular breakfast cereal with a unique segmentation & targeting. Authored Scholarly Paper on "Open Flow-Based Software Defined Networks" CEO, Team Baldwin - Capsim Business Simulation Business courses taken - Marketing, Organization Behavior & Strategic Management, Project Management, Basic Principles of Microeconomics, Decision Making using Risk Solver Platform, Gamification (Coursera Technical courses taken - Networks & Protocols II - TCP/IP, OSPF, EIGRP, RIP, RIPv2, Network Programming, Introduction to Cellular Communication Networks, Advanced Topics in wireless communications: CDMA 2000, WCDMA, EVDO, HSDPA, HSUPA, 4G Visvesvaraya Technological University, R.V College of Engineering 1/4 City, India

Interests

Represent Stanford Ballroom Dance team in various competitions in the Bay area. *Represented University of Maryland in Ballroom dance competitions in UMD, UPenn, MIT, Columbia University & Ohio *Have a keen interest in photography, especially of dancers in motion.

Additional Information

- COMMUNITY ACTIVITIES & PERSONAL INTERESTS *Represent Stanford Ballroom Dance team in various competitions in the Bay area. *Represented University of Maryland in Ballroom dance competitions in UMD, UPenn, MIT, Columbia University & Ohio *Have a keen interest in photography, especially of dancers in motion.

Skills

3G, Basic, business plan, C, Cisco, hardware, debugging, Decision Making, EIGRP, Engineer, Features, Graphics, Image, Mac, market, Marketing, Math, 2000, modem, Network, Networks, next, OSPF, Programming, Project Management, Protocols II, python, reality, RIP, scripting, Simulation, Strategic Management, Supervisor, TCP/IP, Telecommunications, unique