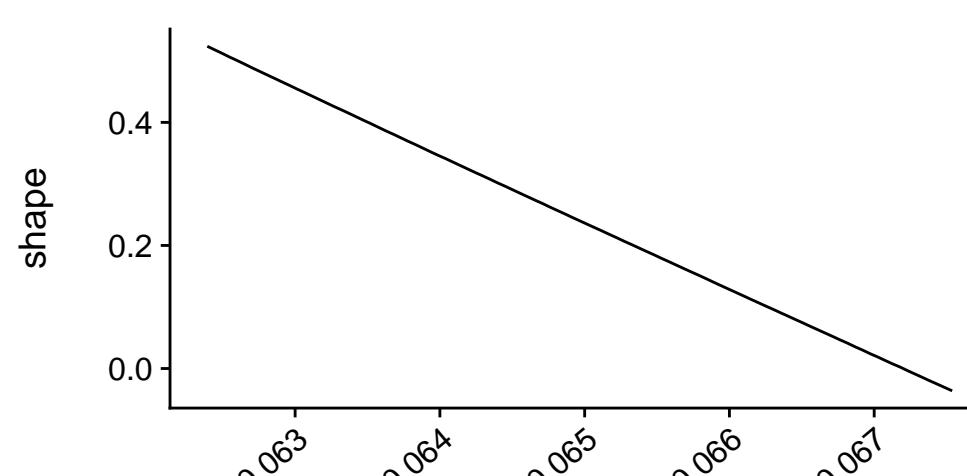
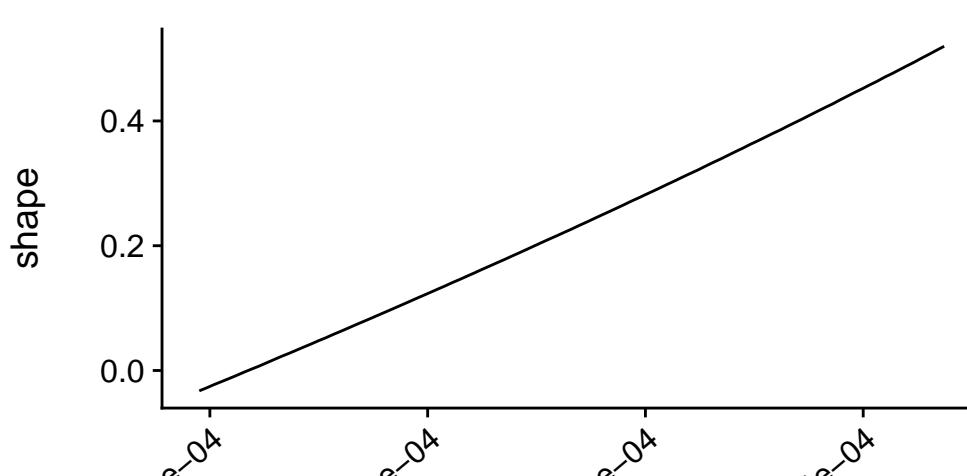
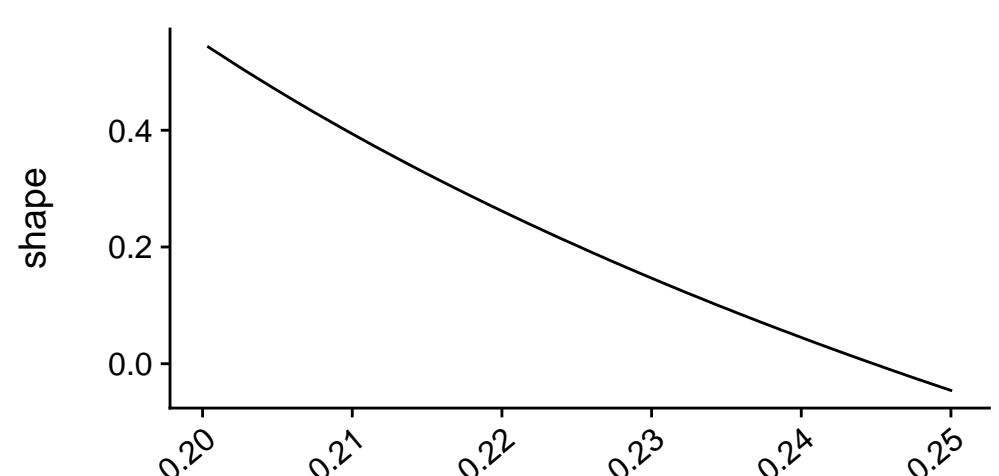
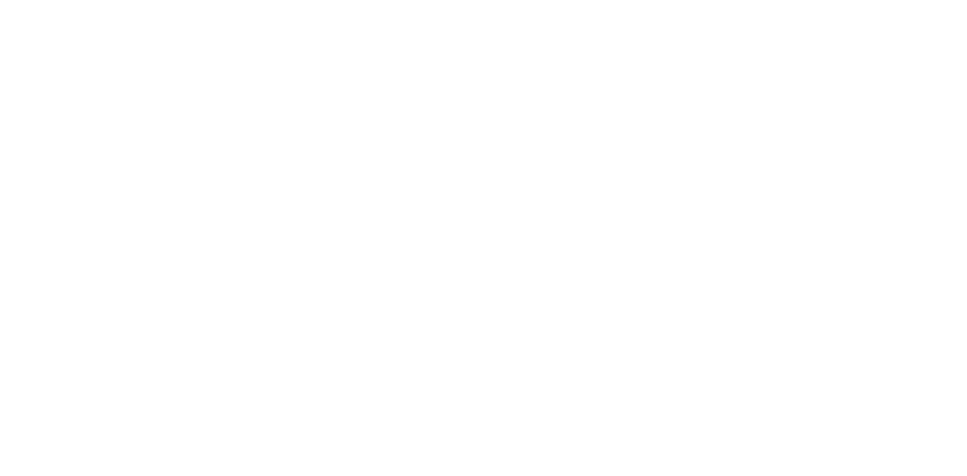
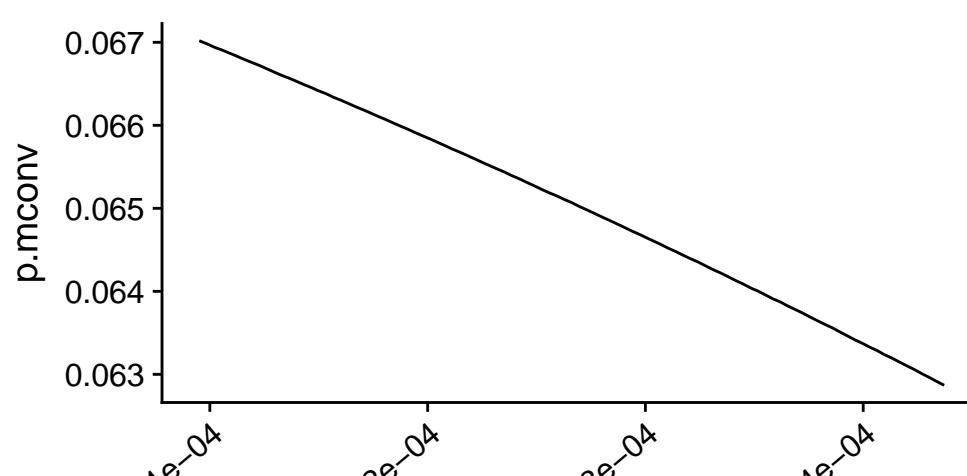
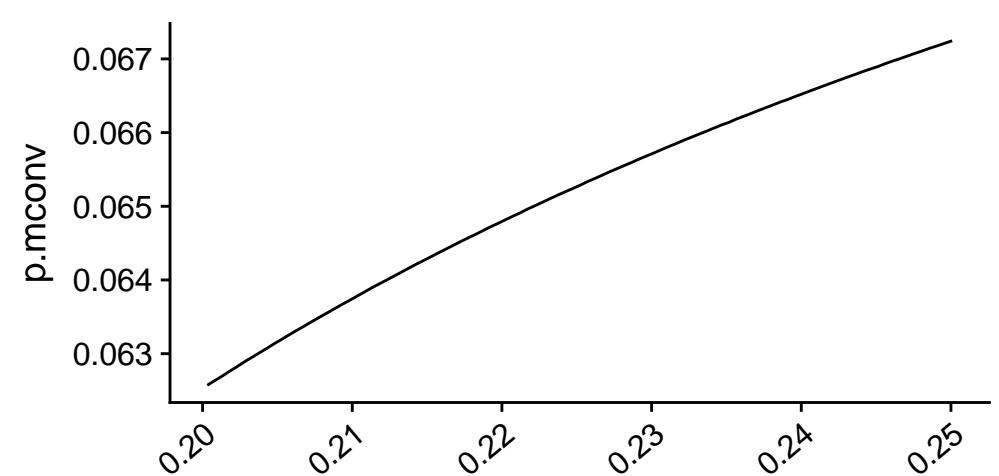
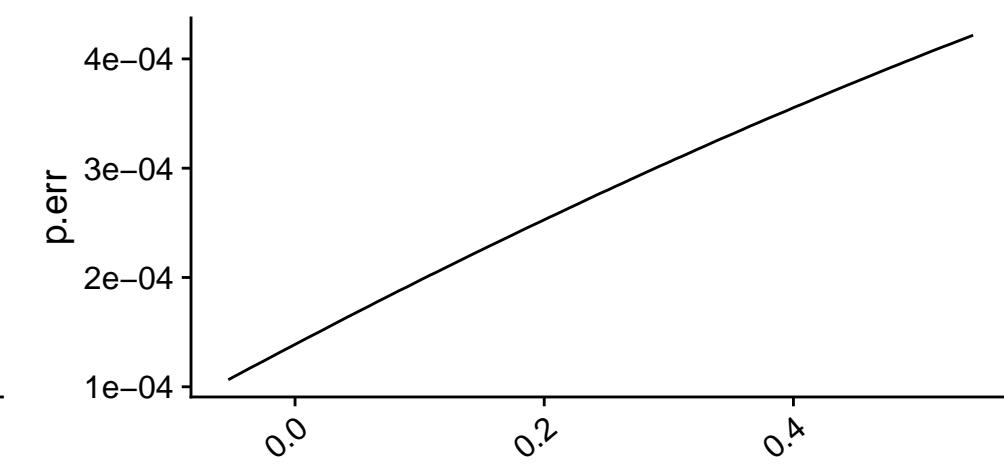
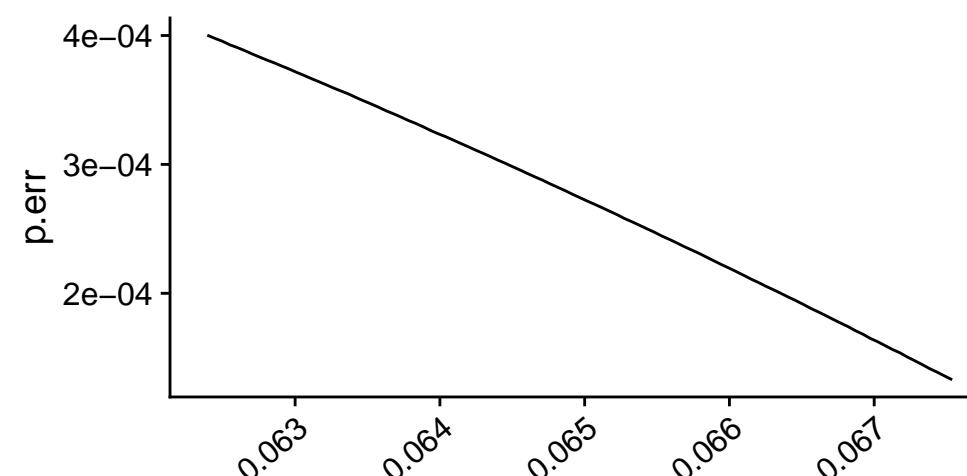
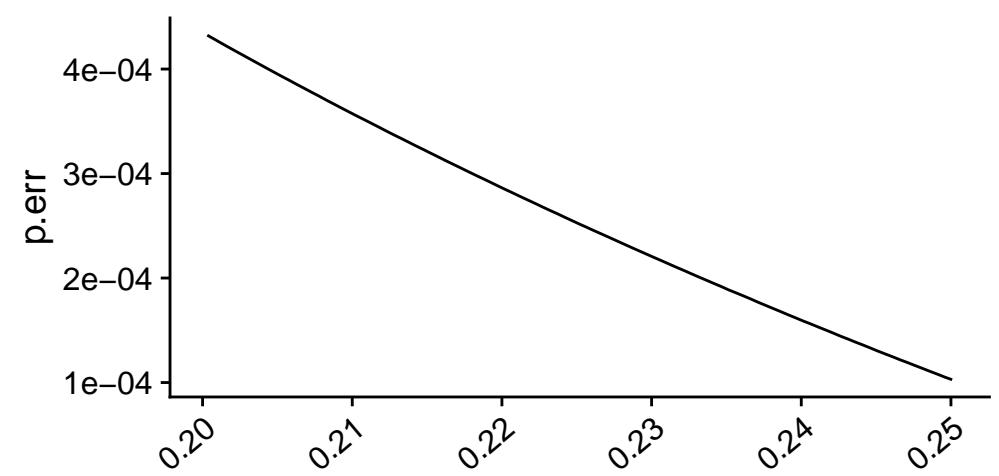
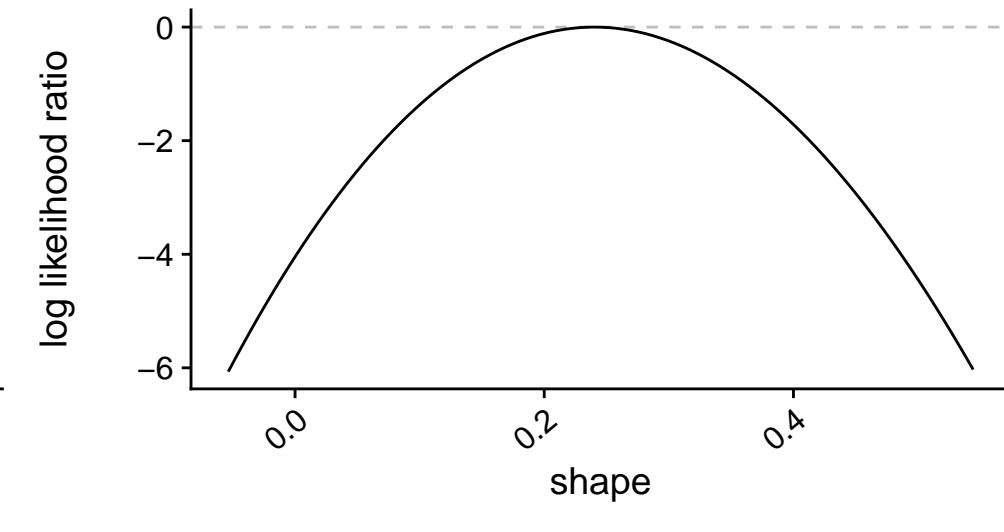
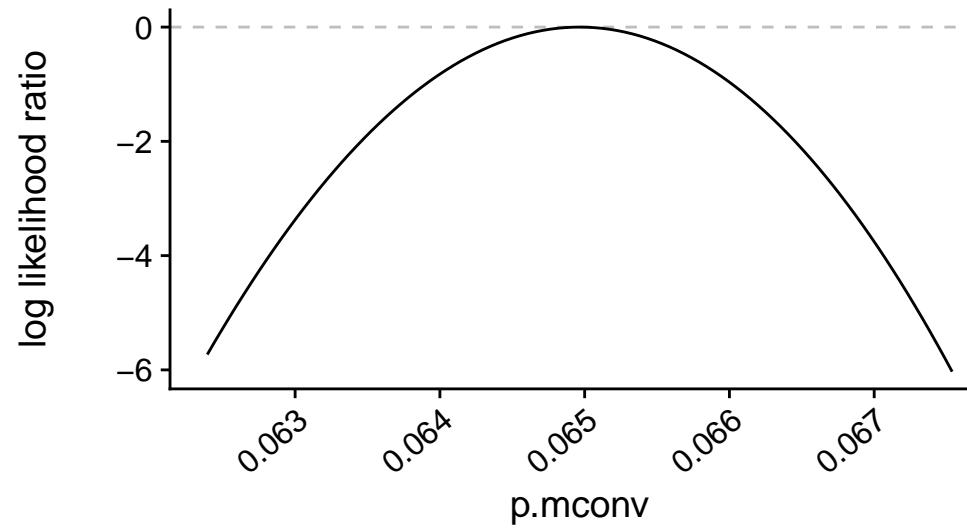
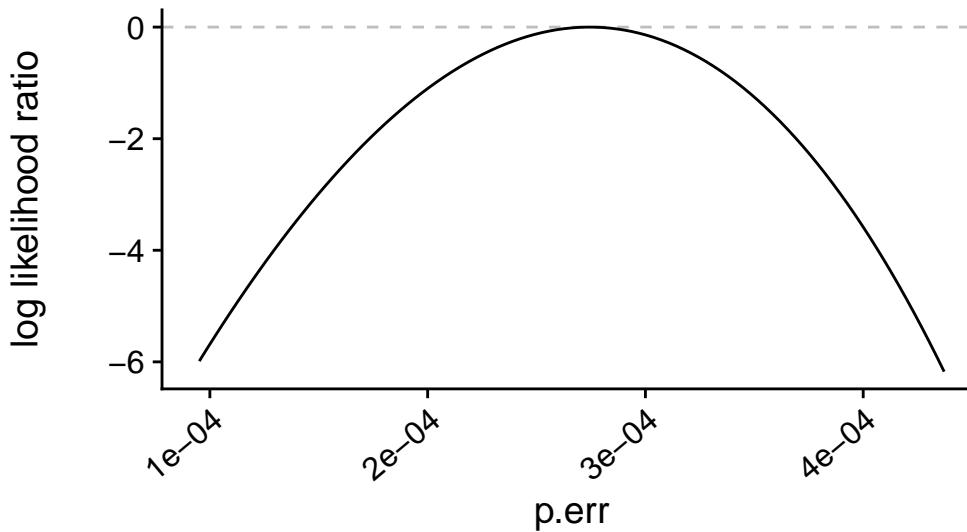
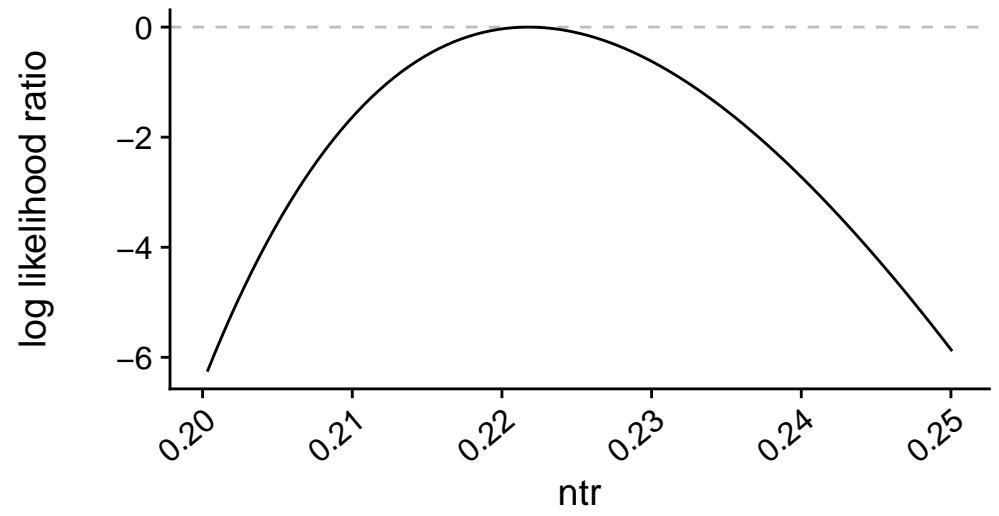
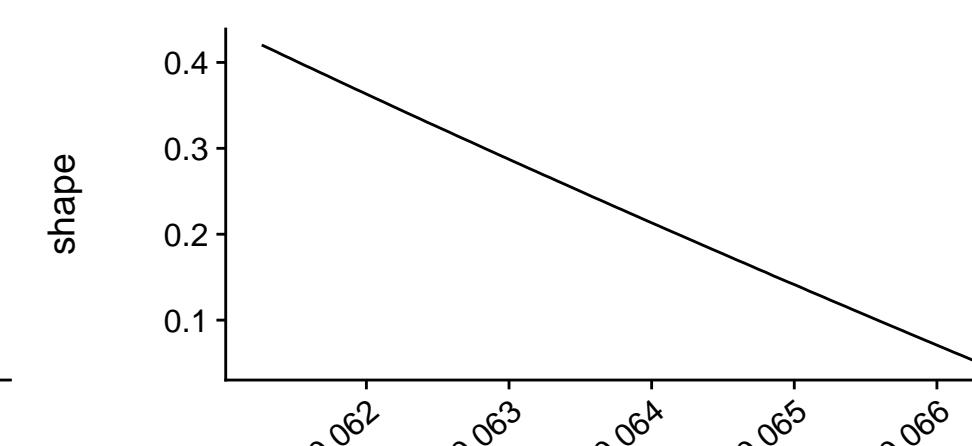
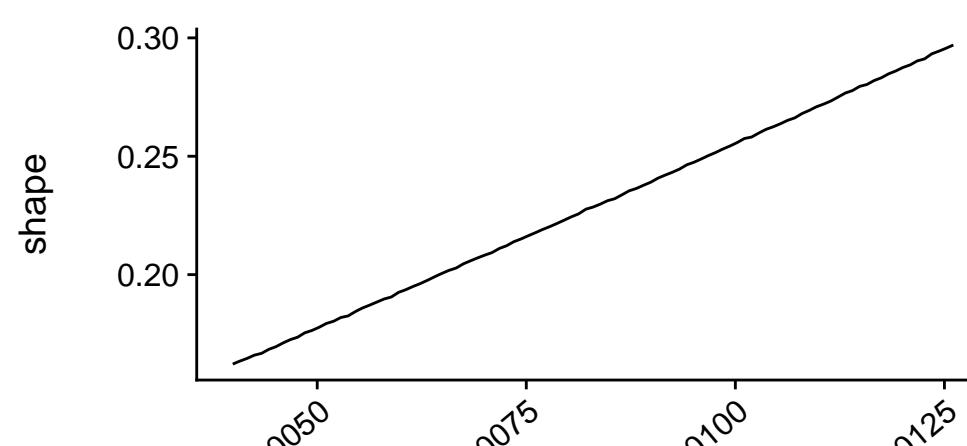
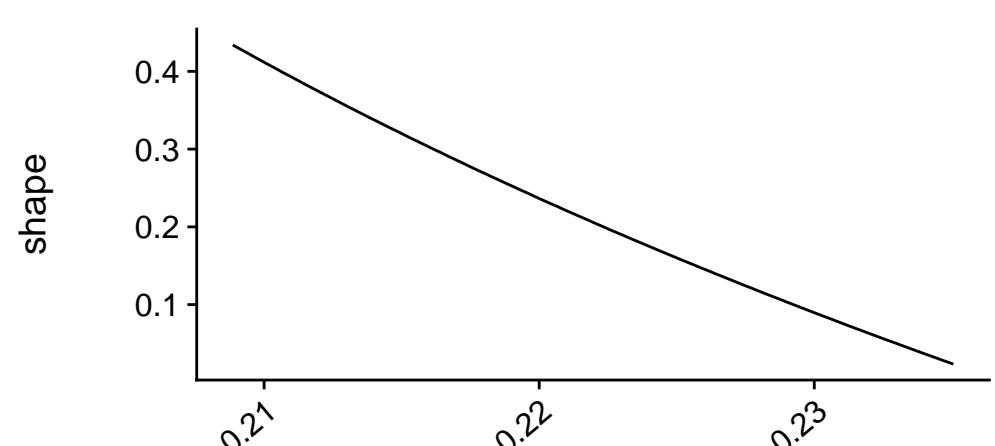
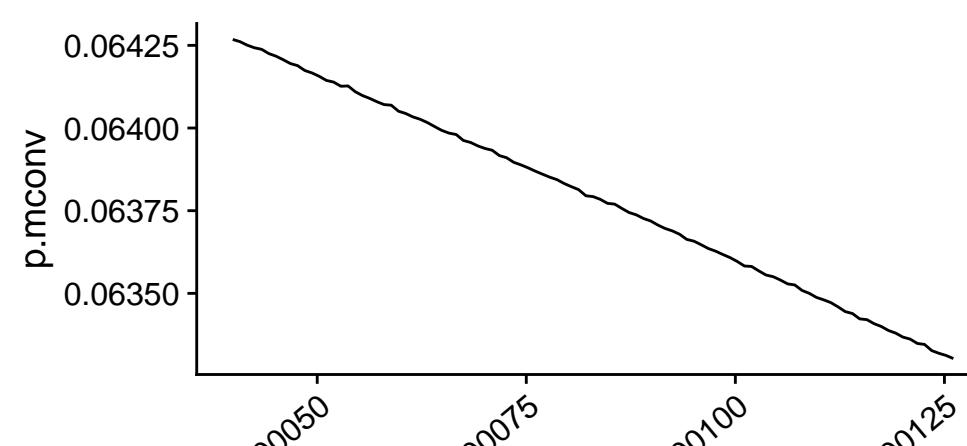
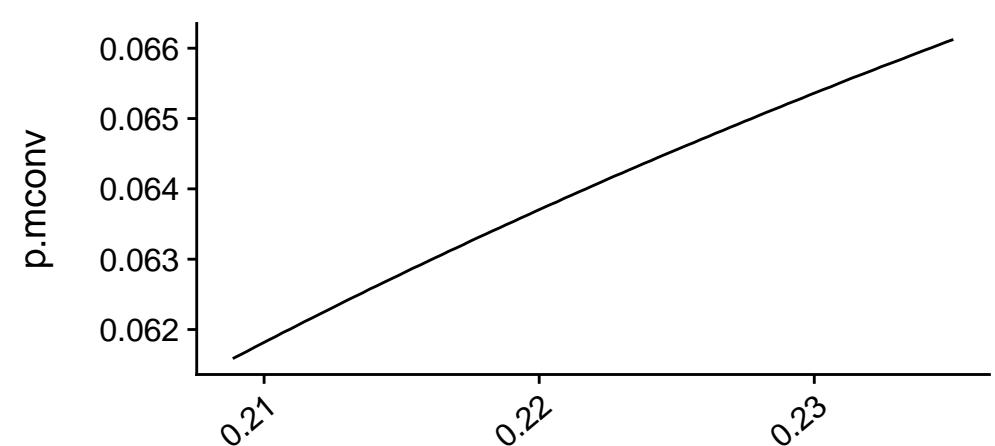
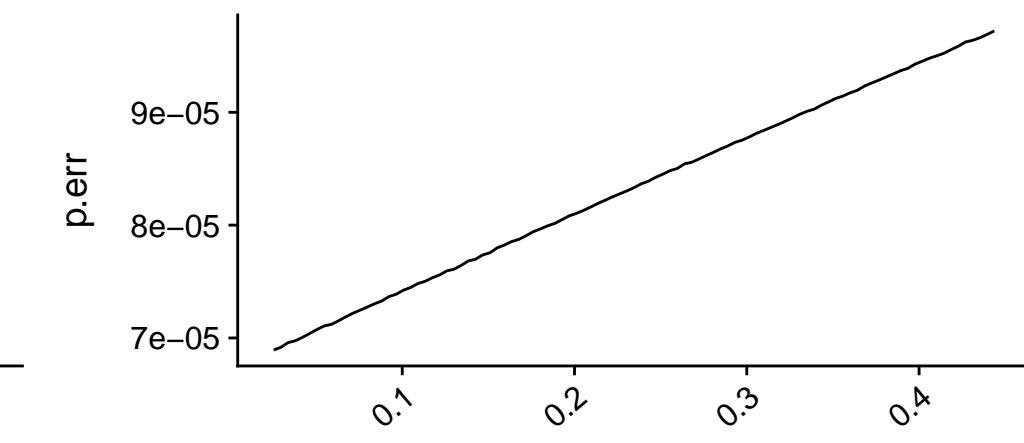
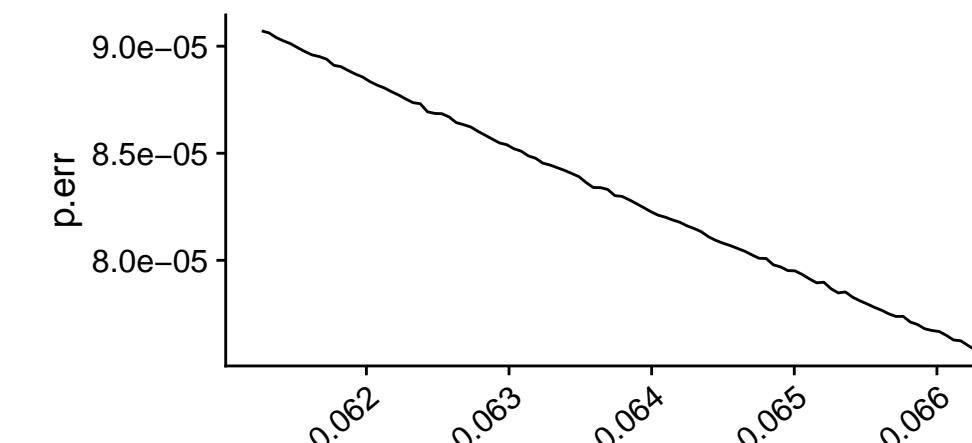
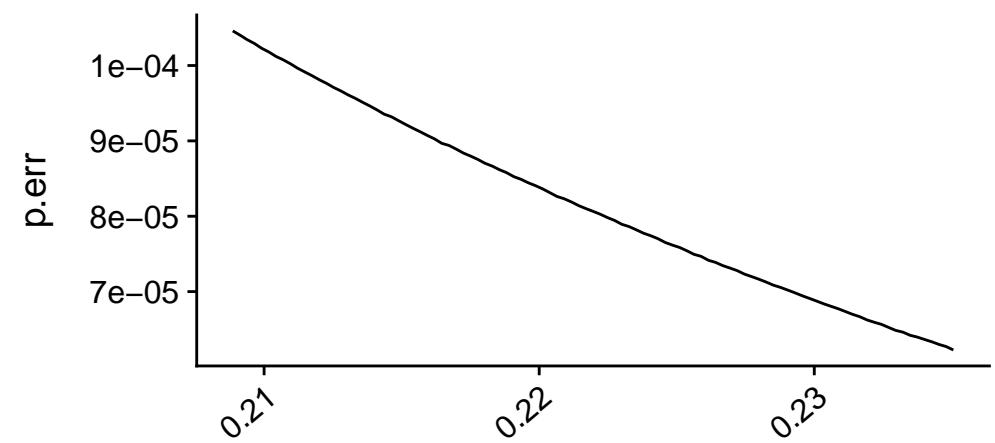
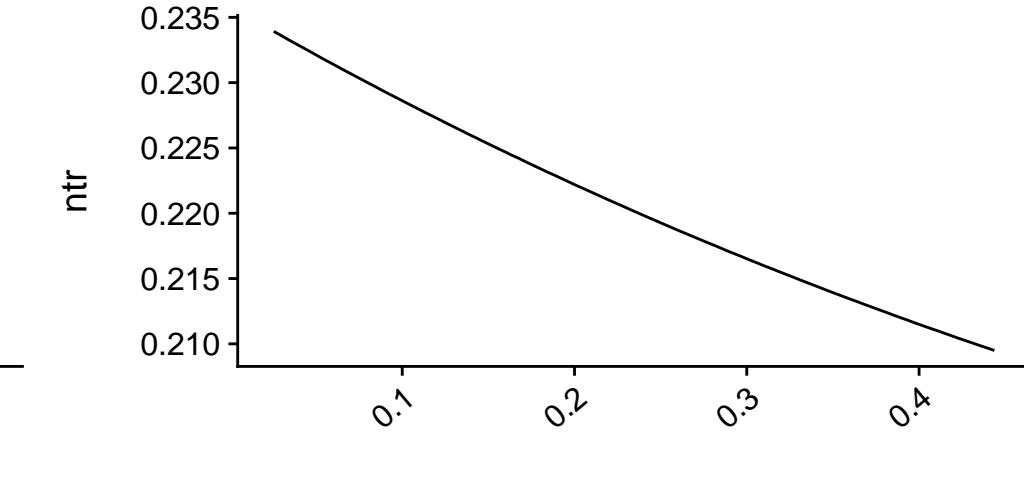
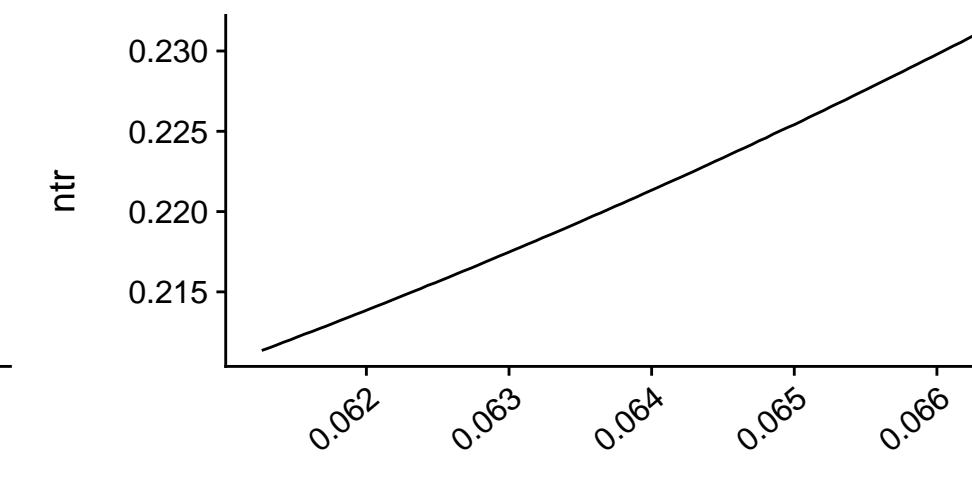
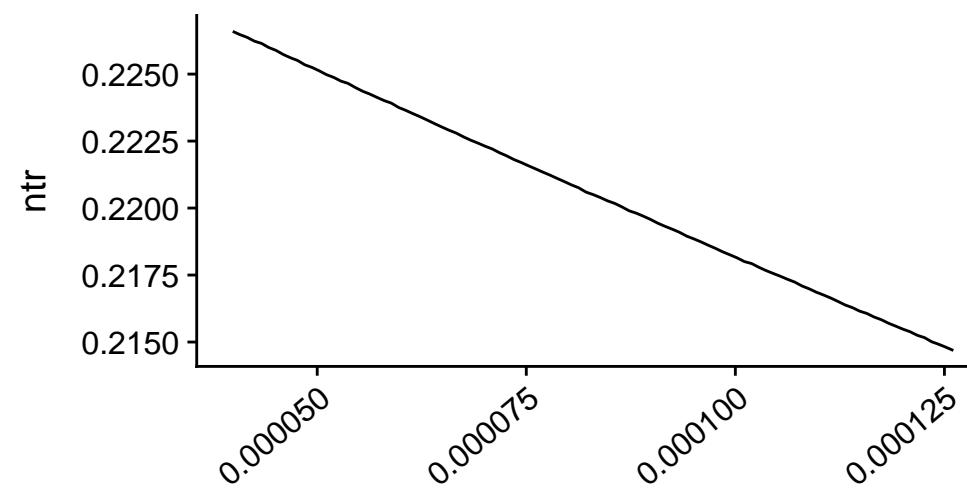
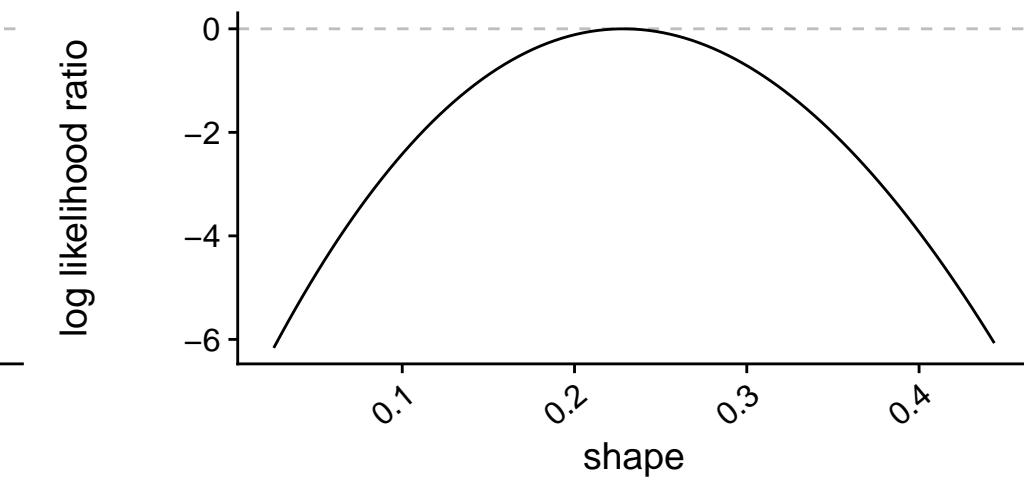
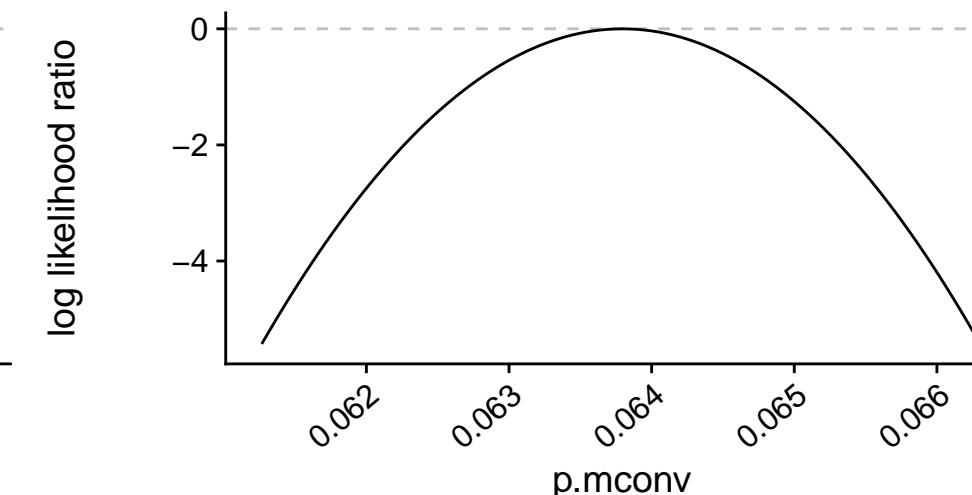
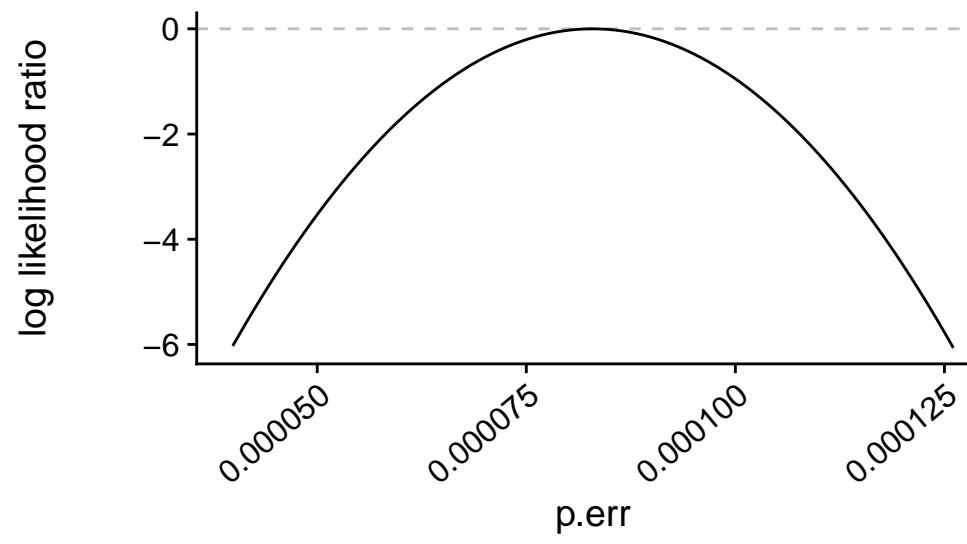
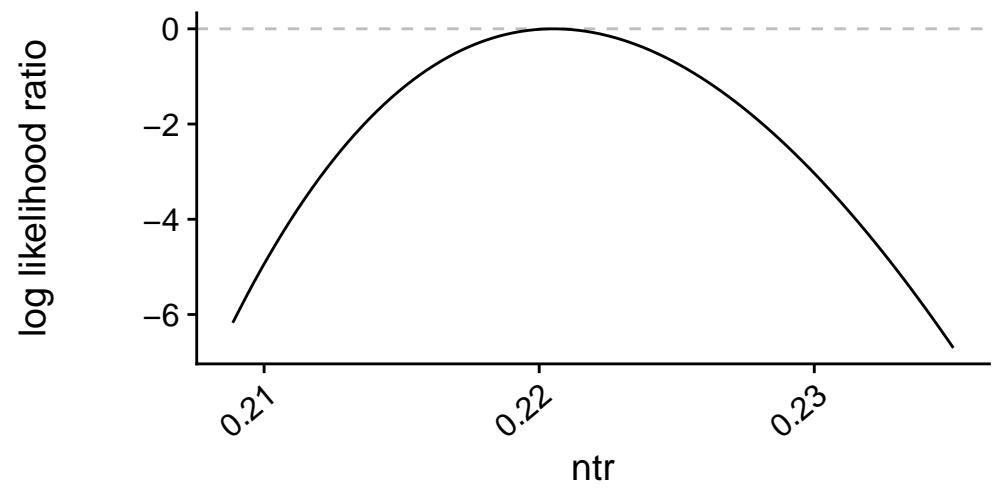


MCMV.2h.4sU (Single read) – 4sU



MCMV.2h.4sU (Double read) – 4sU



MCMV.2h.4sU (3x read) – 4sU

