
BITCA3111

COMPUTER ARCHITECTURE

MODULE DETAILS

Course Location : Swaziland
Examiner (s) : Mr. Ndumiso E. Khumalo
Contact details (email) : ndumiso.e.khumalo@gmail.com
Commence Date : Week 7
Submission Date : No submission required

1. Define ALU? Explain the arithmetic and logical operations carried out by the ALU.
2. Explain the four registers involved in the fetch cycle micro-operations?
3. With the aid of a well-labelled diagram, explain
 - a. Micro-operations
4. Explain the fetch cycle from the point of view of its effect on the processor registers?
5. Specify the series of micro-operations that will perform the following operations
 - a. $AC = AC + TR$
 - b. $DR = DR + AC$

..... THE END