

# **Authors: Max Finch, Tiger Slowinski**

## **Team Members: Max Finch, Tiger Slowinski**

### **ECE:3360 Embedded Systems**

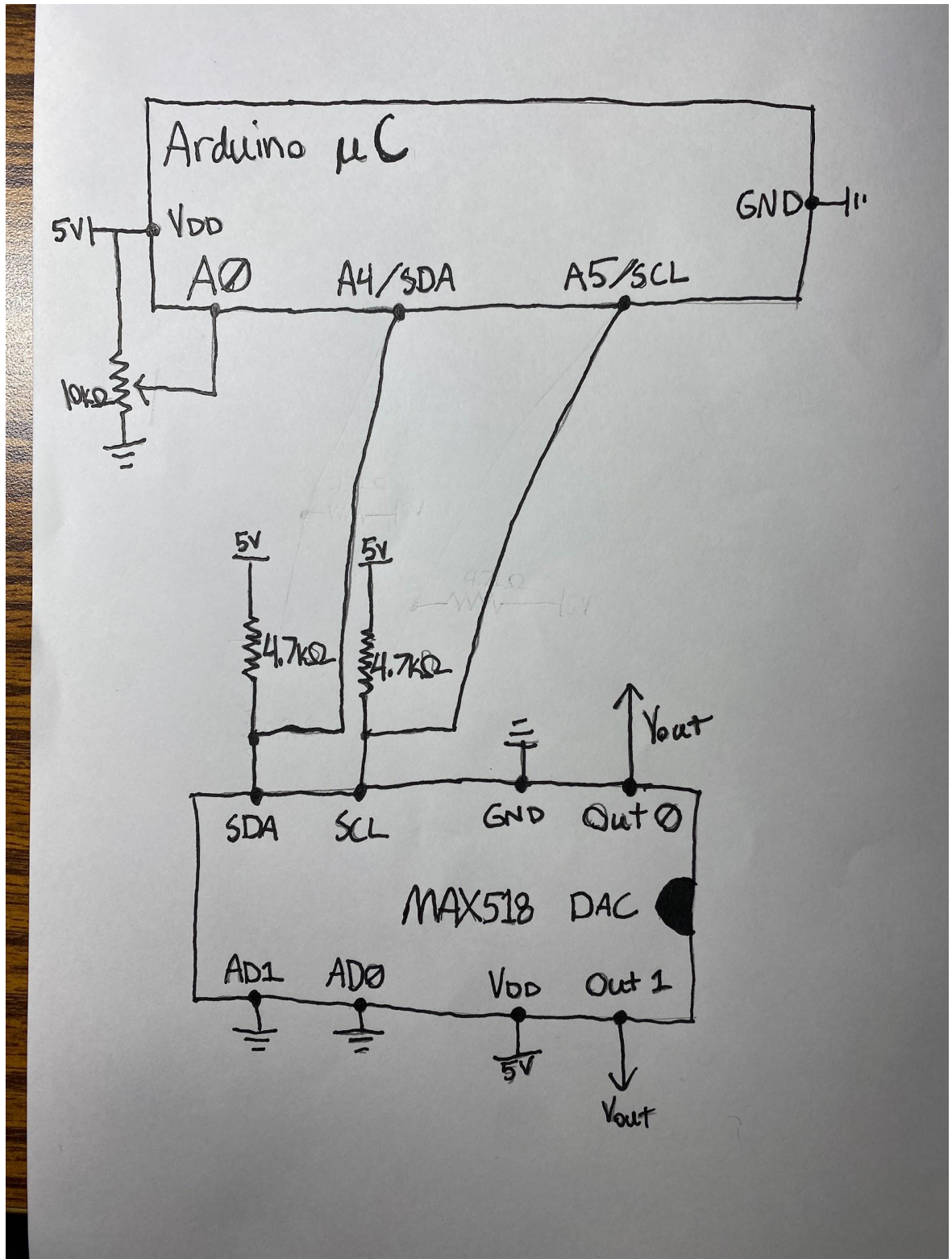
### **Post-Lab Report 5**

#### **1. Introduction**

The objective is to create a system capable of measuring and generating analog signals that can be controlled remotely. This will be achieved by utilizing the internal A/D converter of the ATmega328P controller and an external two-channel D/A converter known as MAX518, which has an I2C interface. The system will be equipped with an RS232 interface (9600 8N1) to connect to a laptop or computer. Users can trigger analog voltage measurements and set the output voltage sequence for both DAC channels by sending commands through the RS232 interface.

Command	Function	Arguments
<b>G</b>	Get single voltage measurement from ADC	no arguments The ATmega328P ADC must be used in 10-bit mode!
<b>S,c,v</b>	Set DAC output voltage	c ... DAC channel number (integer, $c \in \{0,1\}$ ) v ... output voltage (float, format: "n.nn" V, needs to be converted into a decimal number which is sent to the DAC such that the quantization error is minimal)
<b>W,c,f,r</b>	Generate a sine wave on DAC output	c ... DAC channel number (integer, $c \in \{0,1\}$ ) f ... frequency of waveform (integer, $f \in \{10,20\}$ Hz), the signal frequency must be within +/- 5% r ... number of consecutive waveform cycles to generate (integer, $1 \leq r \leq 100$ )  A lookup table for generating the sin wave can be found on ICON (file: "sin_table.csv")

## 2. Schematic



### 3. Discussion

#### Hardware Explanation

##### ADC

Analog digital conversion (ADC) is to take in a variable analog voltage, and then creating a digital representation of that voltage digitally using a set number of bits, or the bit resolution. In the case of the ATmega328P, the 6 analog pins have a resolution of 10 bits (0 to 1023). By setting our reference voltage (VREF) to our VCC of 5V, any voltage between 0V and 5V can be expressed as an integer value between 0 and 1023. By multiplying the digitally converted voltage value by  $5/1024$ , we can scale the value back down to a value between 0 and 5 (for purposes such as viewing the voltage through the serial monitor) that appears to be analog, but the 1024 points of precision constraint remains.

The ADMUX register is used to configure ADC on the ATmega328P. Bits REFSx are used to set VREF, bit ADLAR can change the bit resolution between 8 and 10 bits, and finally MUXx bits select which analog pin is to be used for conversion. ADCSRA and ADCSRB are control and status registers used for ADC, however only ADCSRA is applicable in the case of this lab. This register contains ADEN (initialized to 1 to enable conversion), ADSC (start conversion bit, which is to be manually set to high when a conversion is to be done, and is set automatically back to low when conversion is complete), ADIF (enables interrupts to be called upon completion of an ADC, not applicable in our case and is set to low), and finally 3 ADPSx bits. These bits determine a prescaler value for the ADC conversion, as the ADC uses the same internal clock as the microcontroller but requires a prescaler as the internal clock speed of 16MHz is too fast for the ADC. A prescaler of 128 is used in our case.

As the ATmega328P has 8 bit registers, but the ADC allows for 10 bit resolution, two registers ADCH and ADCL are used in tandem to store the final ADC value post conversion.

##### DAC

Our digital analog converter (DAC) device for this lab is the Maxim MAX518, an 8 pin converter that communicates with the microcontroller using two wire I2C protocol. The DAC reads in an 8 bit value (note the decreased resolution from the microcontroller's

internal ADC), and can output a corresponding analog voltage out of its two analog out pins. VDD and GND pins on the DAC determine the extremes of the possible voltages out of the OUT pins on the DAC. Although the DAC was used alone for this lab, the pins A1 and A0 can be set high and low to create 4 distinct DAC addresses, making I2C communication with multiple DACs possible. Similarly to the scaling performed for the ADC, the user's desired analog voltage (in this case between 0 and 5), must be scaled to fall within the 8 bit resolution range of 0 to 255, which can be done by multiplying the desired analog voltage by 255/5 and then casting that value as an integer. This final value is sent to the DAC over I2C.

## Software Explanation

### I2C

We utilize the I2C protocol via a Two-Wire Interface (TWI) to communicate with the I2C interface on the MAX518 DAC used in this lab. Let's go through the functions used in our code:

#### **[1] i2c\_init();**

This function handles initializing the TWI clock by configuring the TWSR and TWBR registers of the ATmega328P. Only needs to be called at the start of the program.

#### **[2] i2c\_start();**

This function issues a start condition (see figure 1) and sends address and transfer direction. We passed in the address of the MAX518 DAC, which based on our configuration was "0x58". Setting pins A1 and A0 of the MAX518 to ground as one possible address. Since there are 4 different bit combinations of A0 and A1 we can have a max of 4 MAX518's if we wanted to on the same bus (see figure 2). It will return 0 if the i2c device is accessible and 1 if it has failed to access the device.



To see examples of how I2C is used in process in our code, please see lines (158-161) and (136-141).

## USART

We used the microcontroller to send and receive data from the serial monitor interface via the Arduino IDE. In this lab, the bits we sent are represented via RS-232 voltage levels. The functions we used for USART come from the ATmega328P datasheet.

USART stands for Universal Synchronous/Asynchronous Receiver Transmitter.

### **[1] USART\_Init(unsigned int ubrr);**

This function sets the baud rate (bits per second), enables the receiver and transmitter and sets the frame format. The “ubrr” variable is calculated outside the function as  $FOSC/16/BAUD-1$ , where FOSC is the microcontroller's clock speed and BAUD is the desired baud rate for data transmission.

### **[1] USART\_Recieve(void);**

This function waits for bit RXC0 to be set which signals that it has received data. It will return the UDR0 register when received.

### **[1] USART\_Recieveunsigned char data);**

Similar deal, except we wait for the transmit buffer to be empty ( $UDRE0 = 1$ ). When it is emptied we can put “data” into UDR0.

## Decimal to String, String to Decimal

Multiple standard C libraries were used to perform conversions between C-style string character arrays and numbers of integer, float, and decimal type. *atoi()* can be passed elements of a character array containing ascii characters for integers, and produces an integer with each place value of the integer corresponding to its respective ascii character in the original character array.

*dtostrf()* was likewise used to convert floating point values to a C-style string, with the function taking in the desired character array to be modified, total desired characters/digits (right justified), and decimal points of precision as parameters.

## 4. Conclusion

This lab provided useful insight into the broadly applicable techniques of analog to digital conversion and digital to analog conversion, along with a primer to using the equally useful I2C communication protocol which can be used to communicate with devices with a low number of pins and wires. Additionally, peripherals such as serial monitoring for both transmitting information and viewing data was used.

## 5. Appendix A: Source Code



```

1  /*
2  * LAB5.c
3  * C language file for lab 5 in ECE:3360
4  * Created: 4/10/2023 12:52:52 PM
5  * Authors : Max Finch, Tiger Slowinski
6  */
7
8  #include <avr/io.h>
9  #include <stdio.h>
10 #include <stdlib.h>
11 #include <string.h>
12 #include <stdbool.h>
13 #include "twimaster.c"
14 #include <util/delay.h>
15
16 #define FOSC 16000000 // Clock Speed
17 #define BAUD 9600
18 #define MYUBRR FOSC/16/BAUD-1
19 #define DAC 0x58
20
21
22 void USART_Init( unsigned int ubrr )
23 {
24     /*Set baud rate */
25     UBRR0H = (unsigned char)(ubrr>>8);
26     UBRR0L = (unsigned char)ubrr;
27     /*Enable receiver and transmitter */
28     UCSRB = (1<<RXEN)|(1<<TXEN);
29     /* Set frame format: 8data, 2stop bit */
30     UCSRC = (1<<USBS)|(3<<UCSZ0);
31 }
32 void USART_Transmit( unsigned char data )
33 {
34     /* Wait for empty transmit buffer */
35     while ( !( UCSRA & (1<<UDRE)) )
36     ;
37     /* Put data into buffer, sends the data */
38     UDR = data;
39 }
40 unsigned char USART_Receive( void )
41 {
42     /* Wait for data to be received */
43     while ( !(UCSRA & (1<<RXC)) );
44     /* Get and return received data from buffer */
45     //Dtostrf
46     return UDR;
47 }

```



```

49 void ADC_Init(void)
50 {
51     ADMUX = (1<<REFS0); //set to VCC and to A0
52     ADCSRA = (1<<ADEN) | (1<<ADPS2) | (1<<ADPS1) | (1<<ADPS0);
53 }
54 }
55 void get_ADC(void)
56 {
57     ADCSRA = ADCSRA | (1<<ADSC);
58
59     while (!(ADCSRA & (1 << ADIF)));
60     ADCSRA |= 1 << ADIF;
61
62     float ADC_val = (ADC*5.0/1024.0);
63     char buffer[7];
64     dtostrf(ADC_val,4,2,buffer);
65     USART_Transmit('v');
66     USART_Transmit('=');
67     for(int i = 0; i < sizeof(buffer); i++){
68         USART_Transmit(buffer[i]);
69     }
70     USART_Transmit(' ');
71     USART_Transmit('V');
72     USART_Transmit(' ');
73 }
74 unsigned char data[11];
75 int sinn[] = {128,141,153,165,177,188,199,209,219,227,234,241,246,250,254,255,
76             255,255,254,250,246,241,234,227,219,209,199,188,177,165,153,141,128,115,103,
77             91,79,68,57,47,37,29,22,15,10,6,2,1,0,1,2,6,10,15,22,29,37,47,57,68,79,91,103,115};
78
79 void W_command()
80 {
81     int out = data[2]-'0';
82     int x = 0;
83     int r;
84     if(data[8] == ' ') //single digit
85     {
86         r = data[7]-'0';
87         x =1;
88
89     }else if(data[9] == ' ') //two digits
90     {
91         int i = data[7]-'0';
92         i = i*10;
93         int j = data[8]-'0';
94         r = i+j;
95         x = 2;
96
97     }else //else three digits
98     {
99         int i = (data[7]-'0')*100;
100        int j = (data[8]-'0')*10;

```

```

101     int k = data[9] - '0';
102     r = i+j+k;
103     x = 3;
104 }
105
106 char first_msg [] = {'G','e','n','e','r','a','t','i','n','g',' '};
107 for(int i = 0; i < sizeof(first_msg); i++){
108     USART_Transmit(first_msg[i]);
109 }
110 //printing r
111 char r_string [x];
112 itoa(r, r_string, 10);
113 for(int i = 0; i < sizeof(r_string); i++){
114     USART_Transmit(r_string[i]);
115 }
116 char second_msg[] = {' ','s','i','n','e',' ','w','a','v','e',' ','c','y','c',
117 'l','e','s',' ','w','i','t','h',' ','f','='};
118 for(int i = 0; i < sizeof(second_msg); i++){
119     USART_Transmit(second_msg[i]);
120 }
121 //printing frequency
122 USART_Transmit(data[4]);
123 USART_Transmit(data[5]);
124 USART_Transmit(' ');
125 char msgggg[] = {'H','z',' ','o','n',' ','D','A','C',' ','c','h','a','n','n','e','l',' '};
126 for(int i = 0; i < sizeof(msgggg); i++){
127     USART_Transmit(msgggg[i]);
128 }
129 //print channel
130 USART_Transmit(data[2]);
131
132 for(int i=0; i<r; i++)
133 {
134     for(int j=0; j<(sizeof(sinn)/2); j++)
135     {
136         i2c_start(DAC);
137         i2c_write(out);
138         i2c_write(sinn[j]);
139         if(data[4] == '1'){_delay_us(1250);}
140         if(data[4] == '2'){_delay_us(500);}
141         i2c_stop();
142     }
143 }
144
145 }
146
147 void S_command()
148 {
149
150     int out = data[2]-'0';
151
152     char fltstr[5];
153     strncpy(fltstr, data + 4, 4);
154     fltstr[4] = 0;
155     float final_val = atof(fltstr);
156     int final_int = final_val*51;

```

```

157
158     i2c_start(DAC);
159     i2c_write(out);    //0 or 1
160     i2c_write(final_int);
161     i2c_stop();
162     char msg [] = {'D','A','C',' ','c','h','a','n','n','e','l',' '};
163     for(int i = 0; i < sizeof(msg); i++){
164         USART_Transmit(msg[i]);
165     }
166     USART_Transmit(data[2]);
167     char msggg [] = {' ','s','e','t',' ','t','o',' '};
168     for(int i = 0; i < sizeof(msggg); i++){
169         USART_Transmit(msggg[i]);
170     }
171     USART_Transmit(data[4]);
172     USART_Transmit(data[5]);
173     USART_Transmit(data[6]);
174     USART_Transmit(data[7]);
175     USART_Transmit(' ');
176     USART_Transmit('V');
177     USART_Transmit(' ');
178     USART_Transmit('(');
179     char msgggg [3];
180     itoa(final_int, msgggg, 10);
181     for(int i = 0; i < sizeof(msgggg); i++){
182         USART_Transmit(msgggg[i]);
183     }
184     USART_Transmit('d');
185     USART_Transmit(')');
186     USART_Transmit(' ');
187 }
188
189
190 int main(void)
191 {
192
193     USART_Init(MYUBRR);
194     ADC_Init();
195     i2c_init();
196
197     while(1)
198     {
199
200         /* Wait for data to be received */
201         int i = 0;
202         int len = 0;
203         unsigned char c = USART_Receive();
204
205         if ( c == 'G')
206         {
207             get_ADC();
208         }
209         //goto G function
210         else if(c == 'S')
211         {
212             len = 8;
213             data[0] = c;

```

```

214         i=1;
215         while(i != len)
216         {
217             data[i] = USART_Receive();
218             i++;
219         }
220         S_command();
221     }else if(c == 'W')
222     {
223         len = 50;
224         data[0] = c;
225         i=1;
226         bool done = false;
227         while(i != len)
228         {
229             data[i] = USART_Receive();
230             if(data[i] == ' ')
231             {
232                 i = len;
233             }else{
234                 i++;
235             }
236         }
237         W_command();
238     }
239 }
240
241
242 }

```

## 6. Appendix B: References

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Lab Lecture Slides provided by Professor Beichel