ICLAB Schedule

2018-Spring

Date	Content	Lecturer
2/28	228 Memorial Day (Holiday)	
3/7	Lecture 0 - Introduction + Workstation Command	李有權
3/14	Lecture 1 – Verilog & Combinational Circuits Design	袁牧華
3/21	Lecture 2 - Sequential Circuits Design & FSM	黃柏諭
3/28	Lecture 3 - Testbench programming and Pattern	楊甯凱
4/4	The Woman & Children's Day	
4/11	Lecture 4 - Advanced Sequential Circuit Design & Designware **Midterm Project - Release**	杜彥旻
4/18	Lecture 5 - Coding Style/Macro Behavior/memory	楊甯凱
4/25	Lecture 6 - Synthesis/Design Compiler	蔡喬安
4/28	Midterm Exam	
5/2	Lecture 7 - Static Timing Analysis	蔡喬安
5/9	Lecture 8 - Low power	黃柏諭
5/16	Lecture 9 – SystemVerilog & Advanced Testbench **Midterm Project - Demo**	姜惟
5/23	Lecture 10 – Coverage & SystemVerilog Assertion **Final Project - Release**	姜惟
5/30	Lecture 11 - APR-I	袁牧華
6/6	Lecture 12 - APR-II: Power	杜彥旻
6/13	Final Exam Preparation (No Class)	
6/20	Final Exam	

6/27	**Final Project – Demo (6/29 2nd Demo)**	

Exercise	TA
Midterm Project + Online Exam + Final Project	李有權
Lab01 + Lab11	袁牧華
Lab02 + Lab08	黃柏諭
Lab03 + Lab05	楊甯凱
Lab04 + Lab12	杜彥旻
Lab06 + Lab07	蔡喬安
Lab09 + Lab10	姜惟