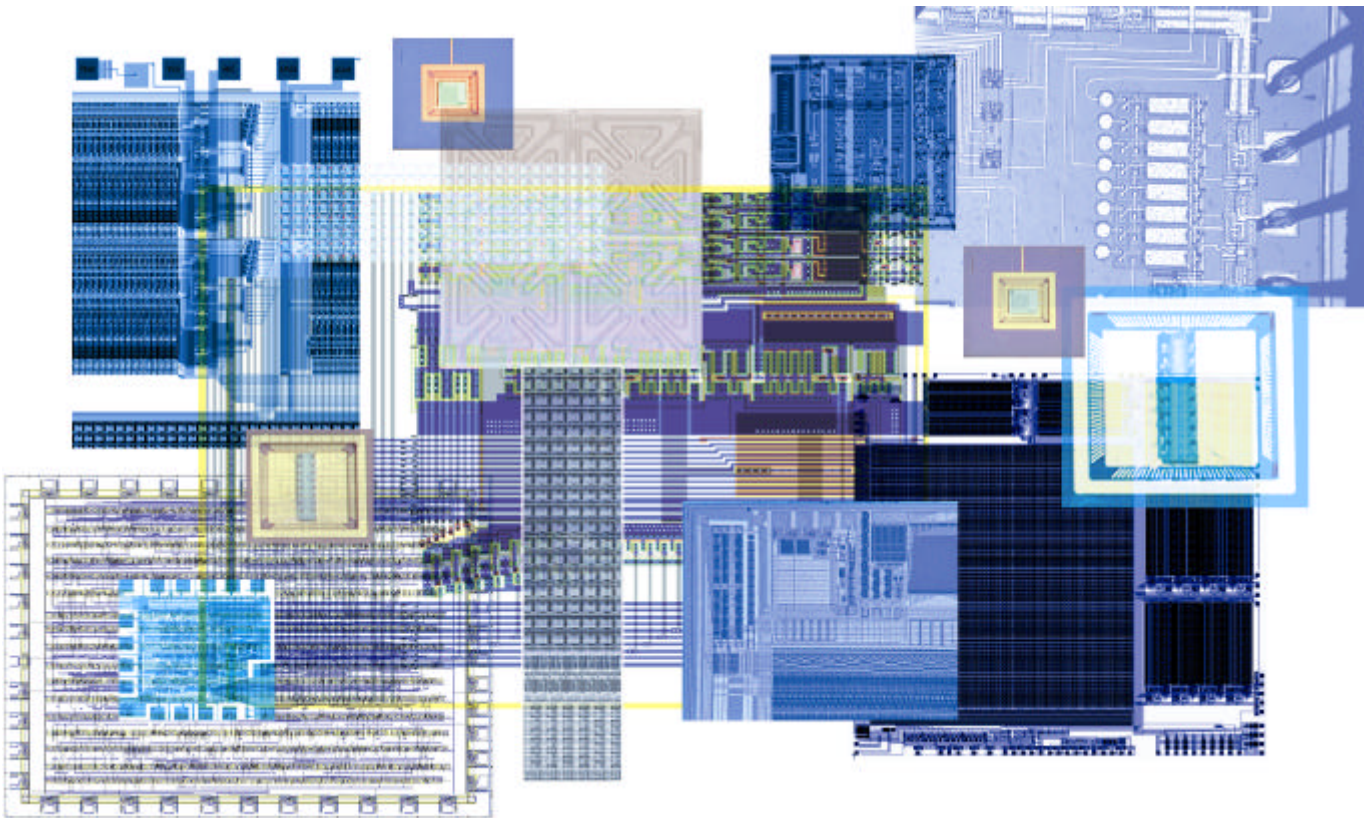


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The MOSIS Service
Hi-ESD Pad Library
AMI C5N (0.5 μ) Process
November 1998



*Tanner Consulting & Engineering Services
develops and delivers advanced ASIC and VLSI solutions for customers.
We accomplish this through training programs, consulting services,
field engineering and contracted design/development efforts.*

Description: Bi-Directional Pad with Buffer

Library: MOSIS AMI 050P

Primitive Set: Tanner SCMOS.Cells

Tanner.TIB.Samples

Schematic: S-Edit

File: TannerLb\scmos\mAMI05P.sdb

Module: PADBIDIR

Mask layout: L-Edit

File: TannerLb\scmos\mAMI05P.tdb

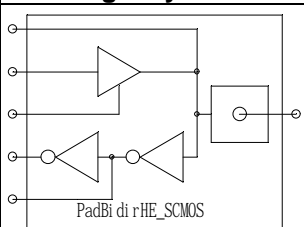
Cell: PADBIDIR

Mapping Macros: GateSim:

TannerLb\nettran\scmos\scms2sim.mac

L-Edit/SPR:

TannerLb\nettran\scmos\scms2tpr.mac

Logic Symbol	Truth Table	Capacitance																
	<table><tr><th>PAD</th><th>OE</th><th>DI</th><th>DO</th></tr><tr><td>X</td><td>1</td><td>X</td><td>In</td></tr><tr><td>1</td><td>0</td><td>1</td><td>X</td></tr><tr><td>0</td><td>0</td><td>0</td><td>X</td></tr></table>	PAD	OE	DI	DO	X	1	X	In	1	0	1	X	0	0	0	X	N/A
PAD	OE	DI	DO															
X	1	X	In															
1	0	1	X															
0	0	0	X															

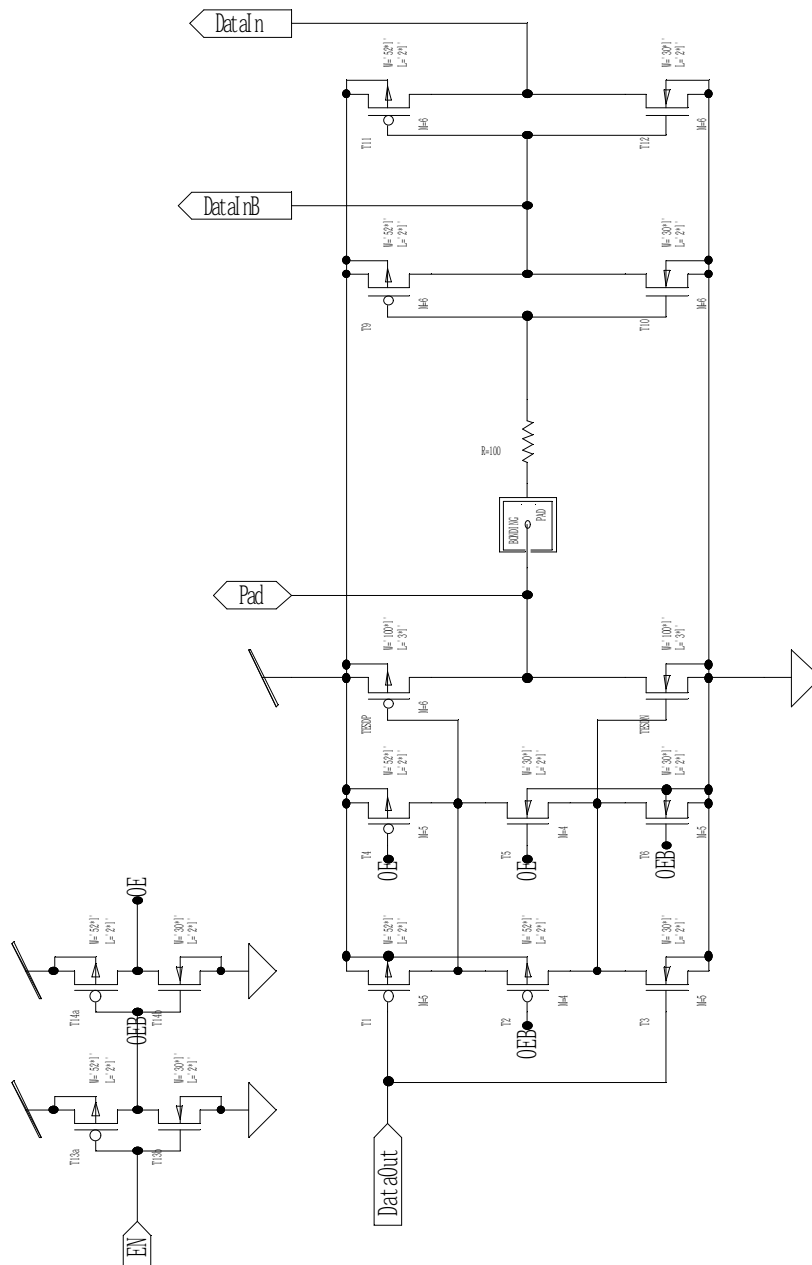
Height	Width	Area	Equivalent Gate	Drive
300 μ	90 μ	27000 μ^2	N/A	N/A

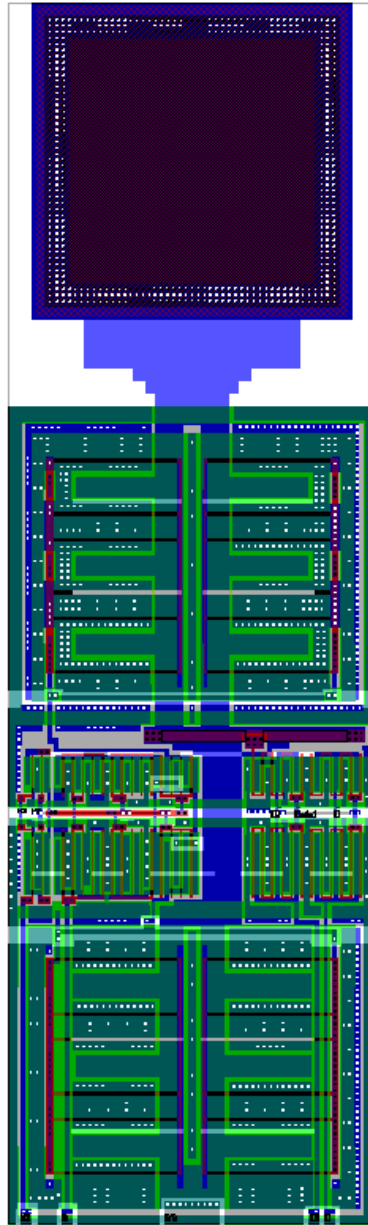
Logic Equation

See truth table

Delay Characteristics:

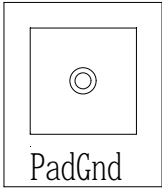
N/A





Description: Ground Pad

Library: MOSIS AMI 050P Primitive Set: Tanner SCMOS.Cells
Tanner.TIB.Samples
Schematic: S-Edit File: TannerLb\scmos\mAMI05P.sdb
Module: PADGND
Mask layout: L-Edit File: TannerLb\scmos\mAMI05P.tdb
Cell: PADGND
Mapping Macros: GateSim: TannerLb\nettran\scmos\scms2sim.mac
L-Edit/SPR: TannerLb\nettran\scmos\scms2tpr.mac

Logic Symbol	Truth Table	Capacitance
 PadGnd	N/A	N/A

Height	Width	Area	Equivalent Gate	Drive
300 μ	90 μ	27000 μ^2	N/A	N/A

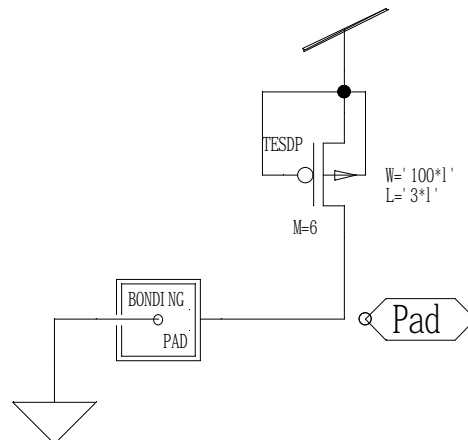
Logic Equation

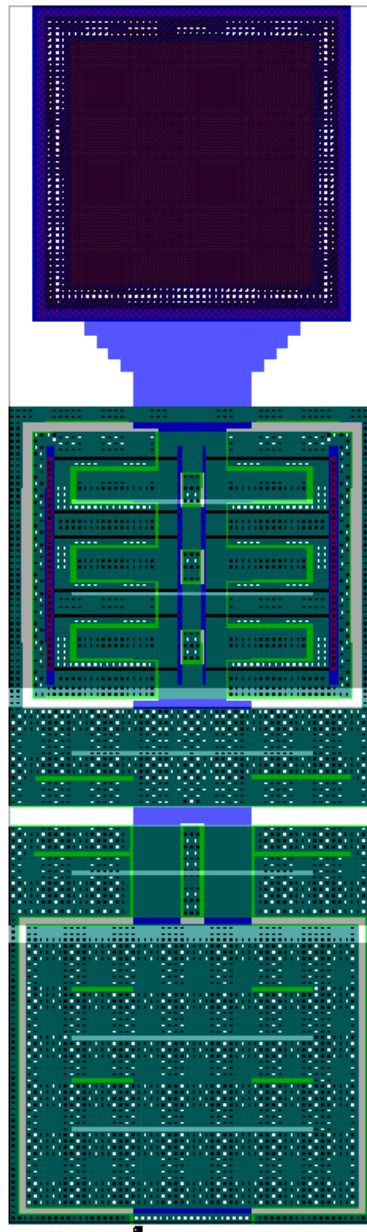
N/A

Delay Characteristics:

N/A

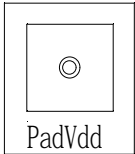
PadGnd





Description: Power Pad

Library: MOSIS AMI 050P Primitive Set: Tanner SCMOS.Cells
Tanner.TIB.Samples
Schematic: S-Edit File: TannerLb\scmos\mAMIP05.sdb
Module: PADVDD
Mask layout: L-Edit File: TannerLb\scmos\mAMIP05.tdb
Cell: PADVDD
Mapping Macros: GateSim: TannerLb\nettran\scmos\scms2sim.mac
L-Edit/SPR: TannerLb\nettran\scmos\scms2tpr.mac

Logic Symbol	Truth Table	Capacitance
	N/A	N/A

Height	Width	Area	Equivalent Gate	Drive
300 μ	90 μ	27000 μ^2	N/A	N/A

Logic Equation

N/A

Delay Characteristics:

N/A

PadVdd

