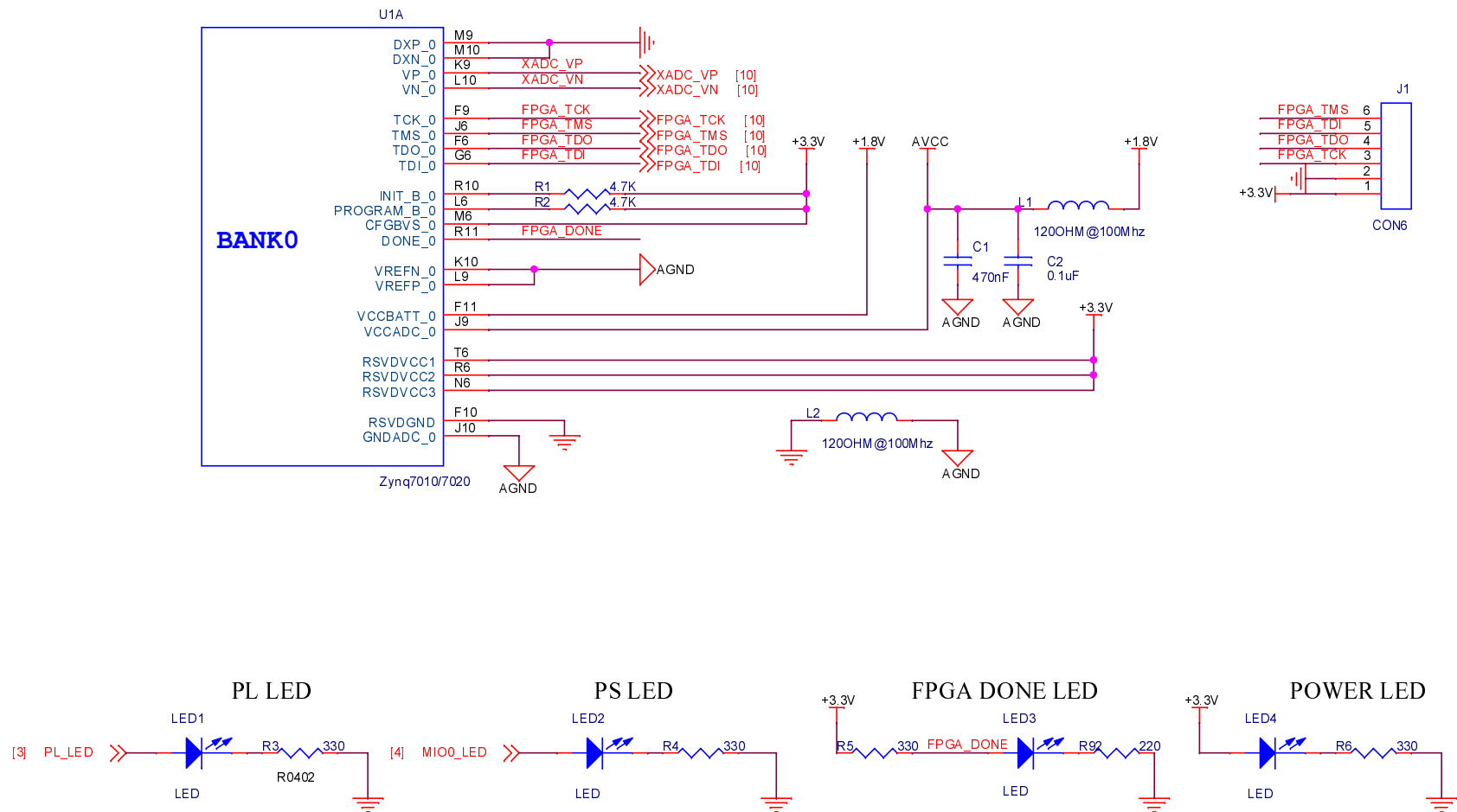
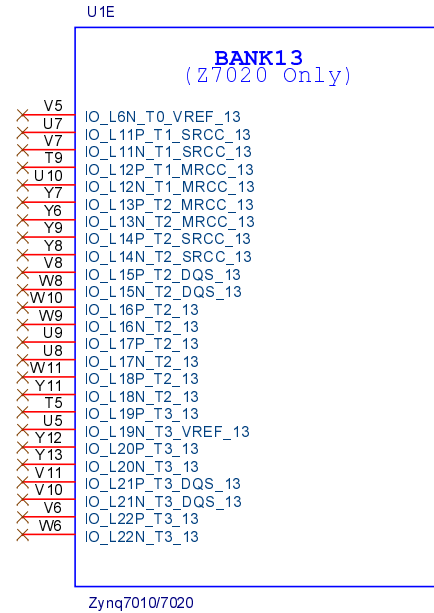
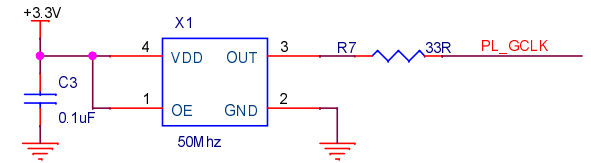
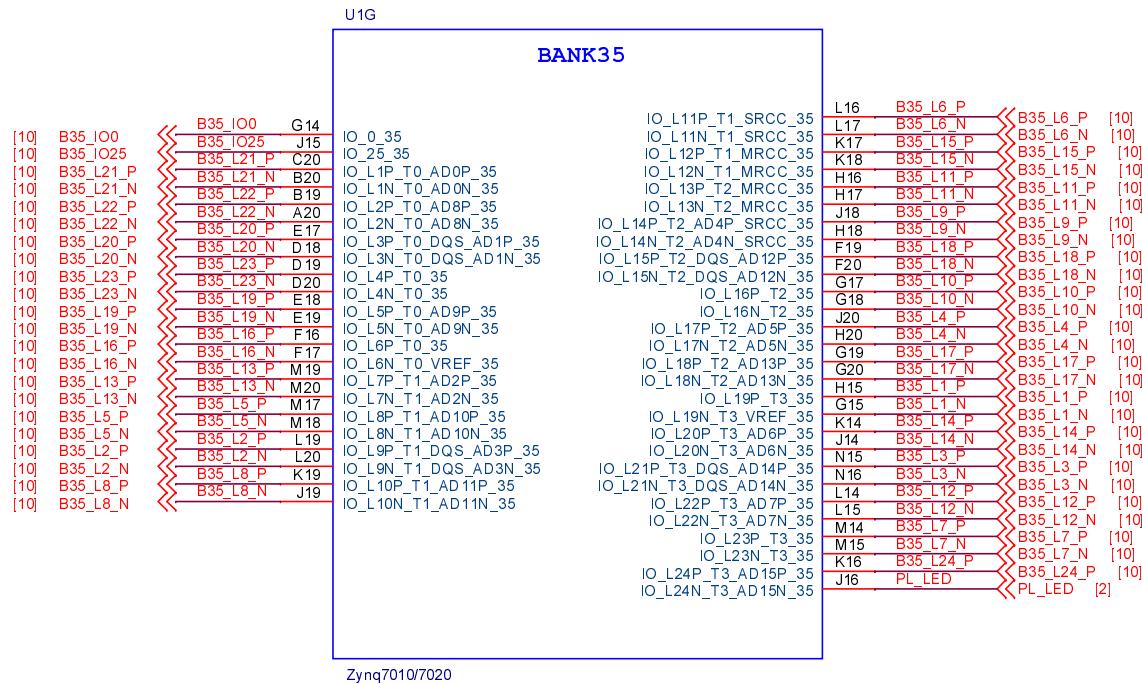
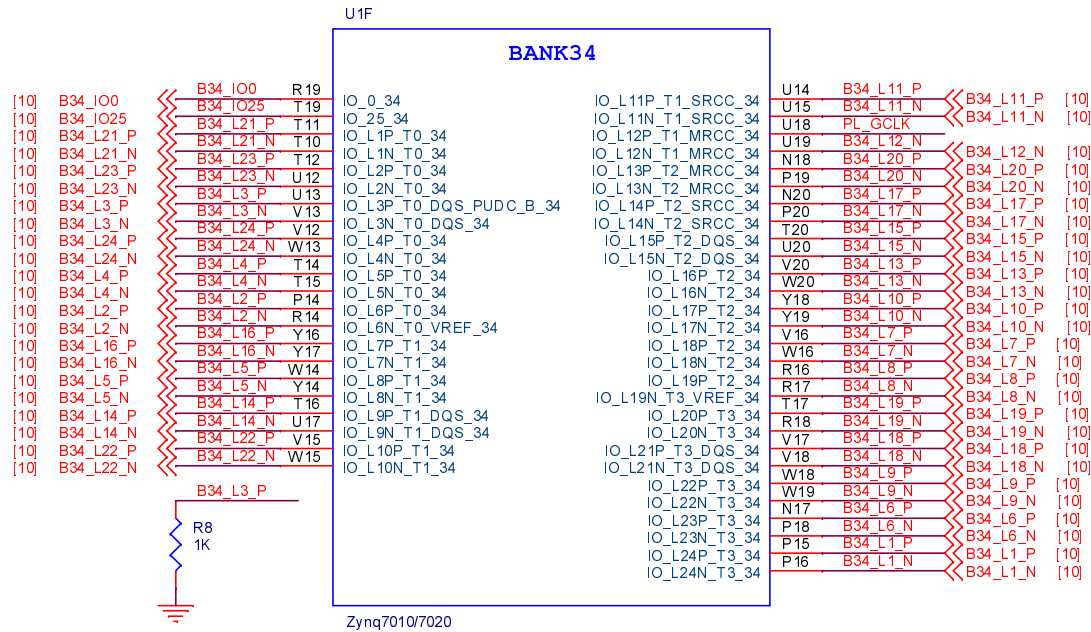


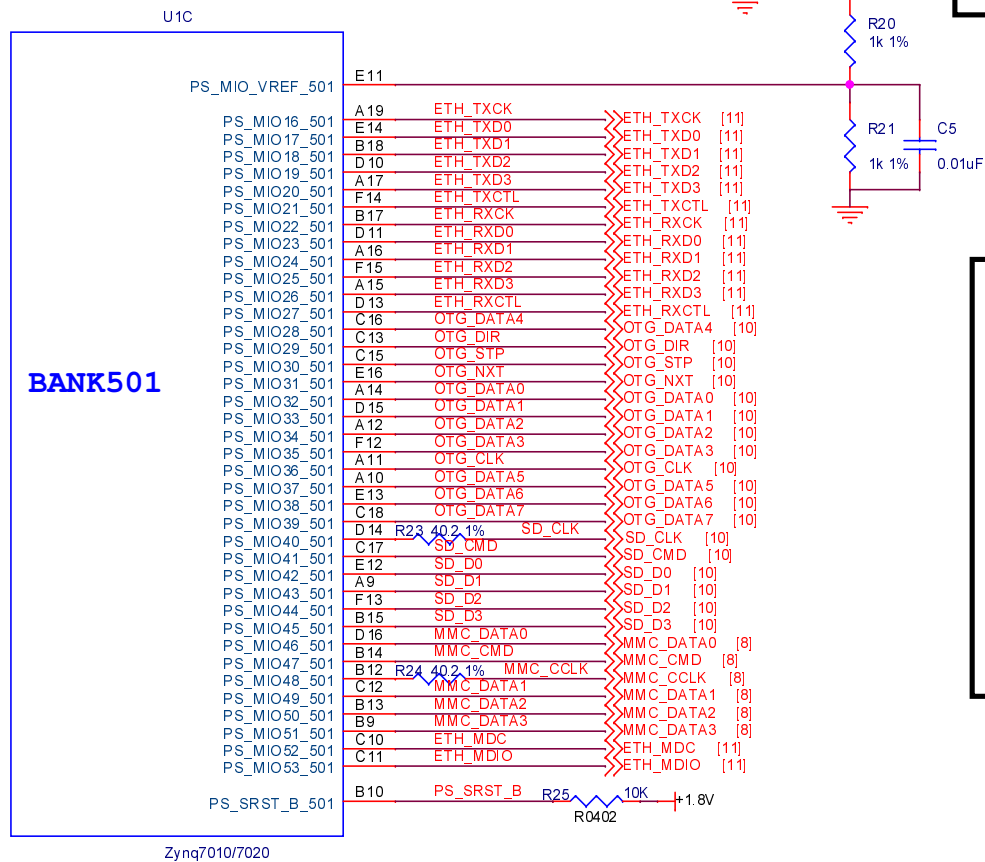
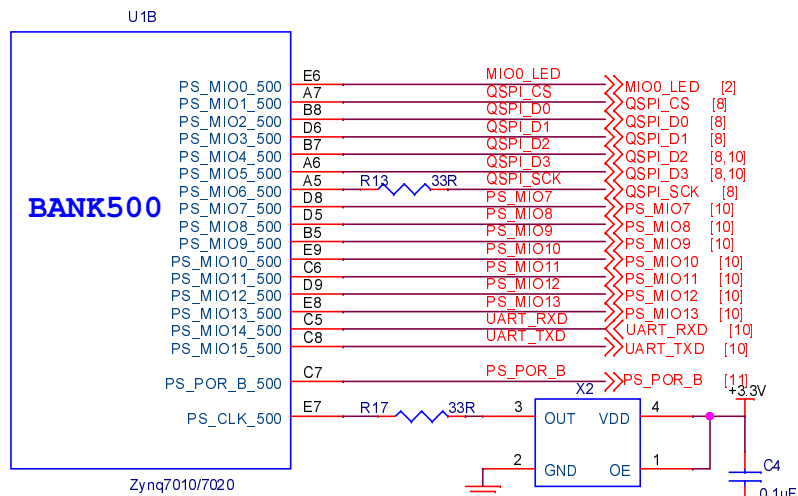
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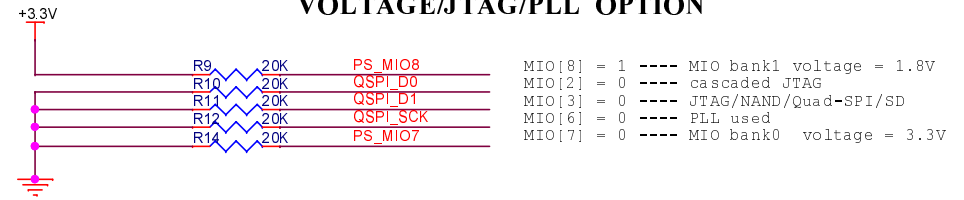
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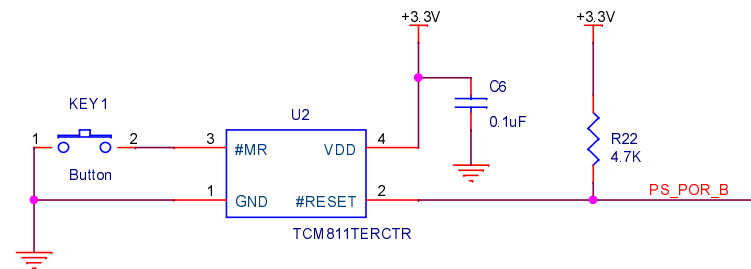


VOLTAGE/JTAG/PLL OPTION

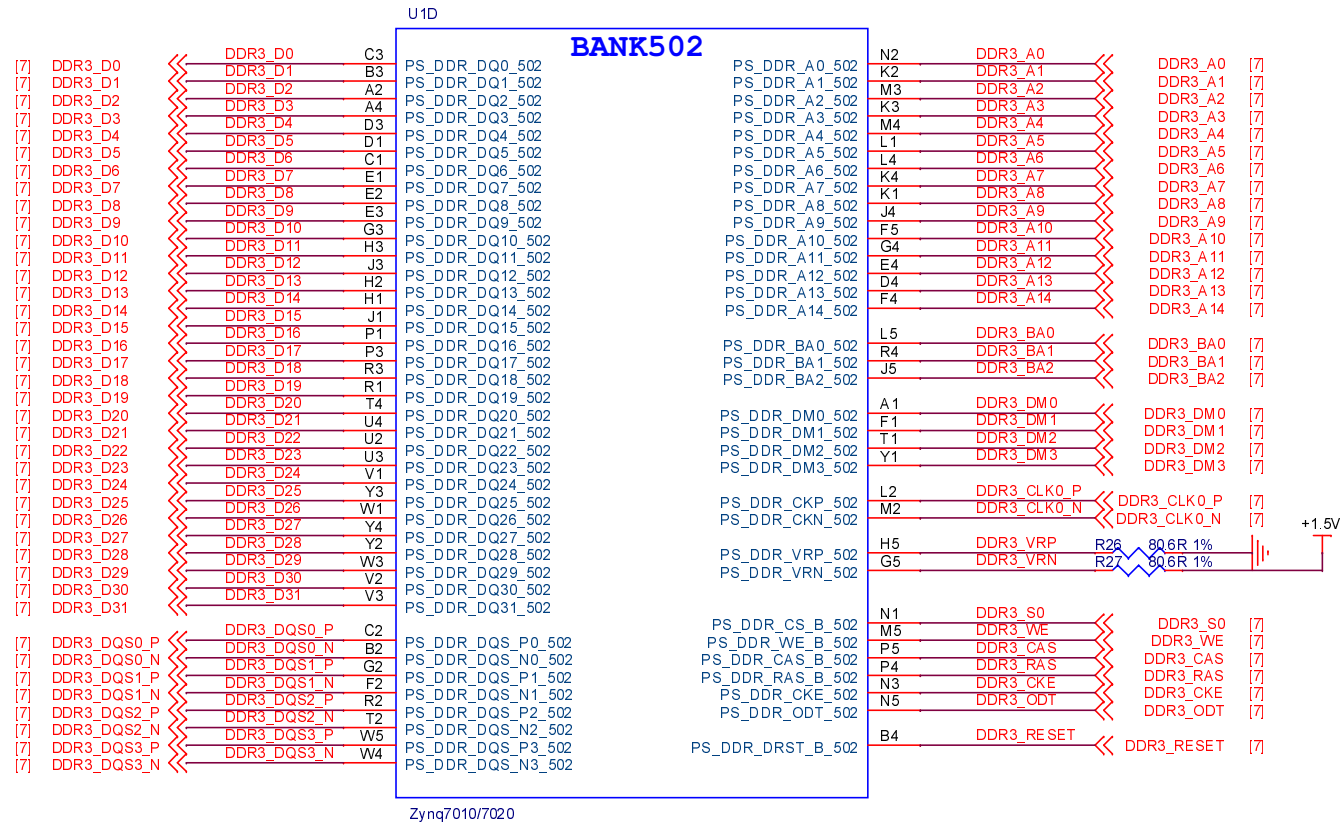


启动的时候MIO7和MIO8作为输入来配置ZYNQ
 复位完成后MIO7和MIO8只能作为输出，不能作为输入
 MIO8接1表示bank500电压设置为3.3v
 MIO7接0表示bank501电压设置为1.8v

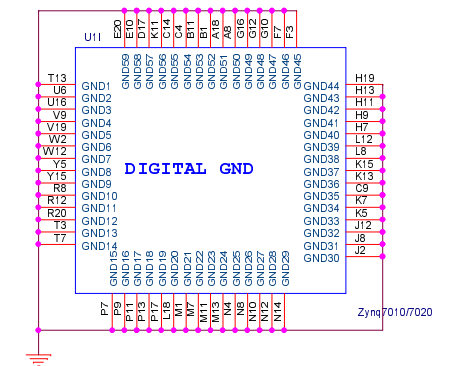
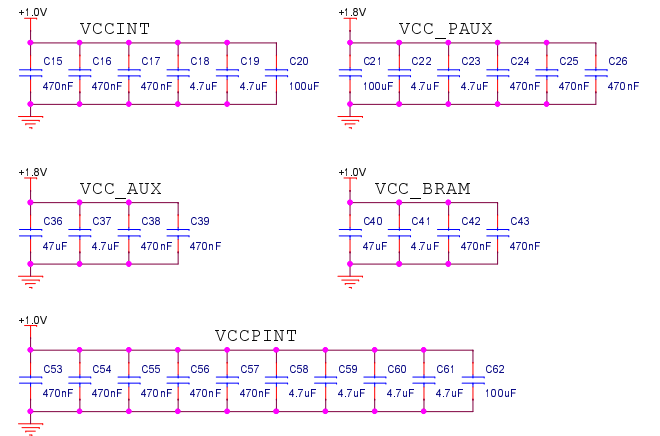
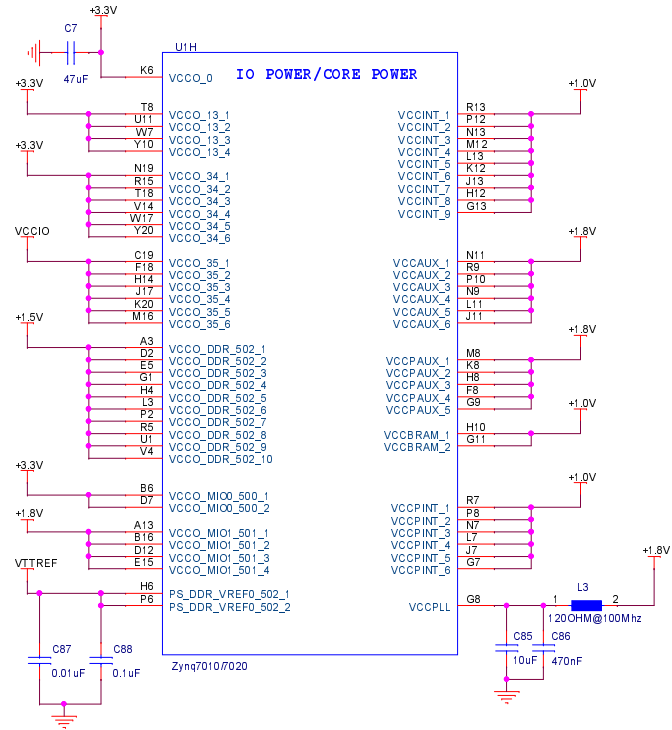
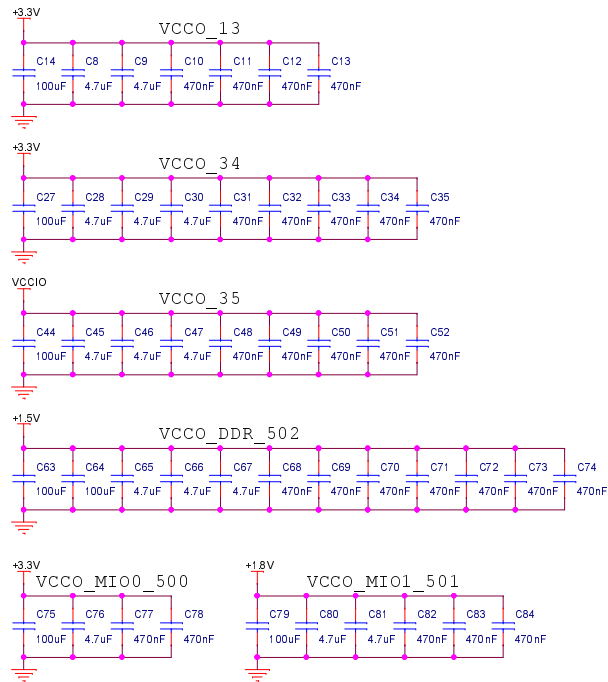
POWER ON RESET



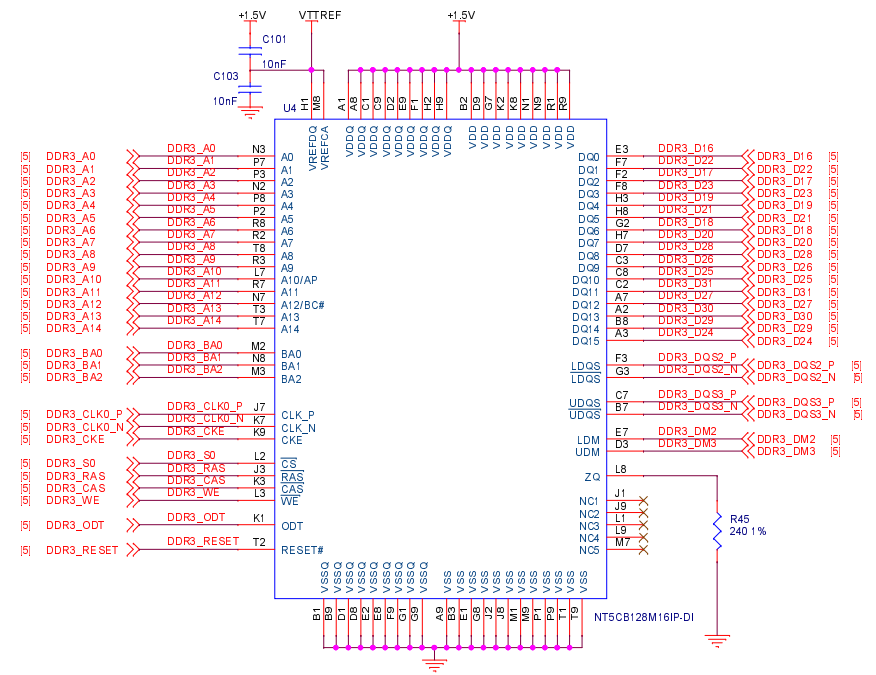
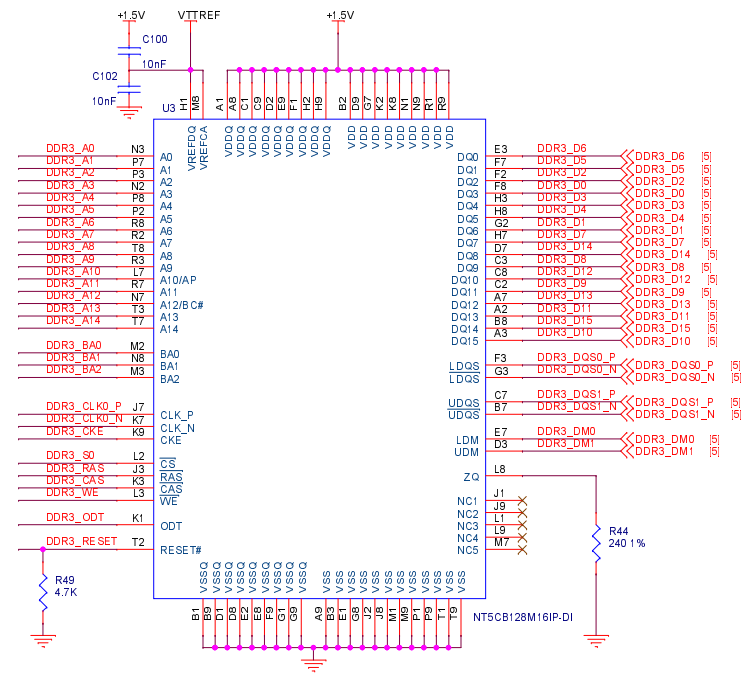
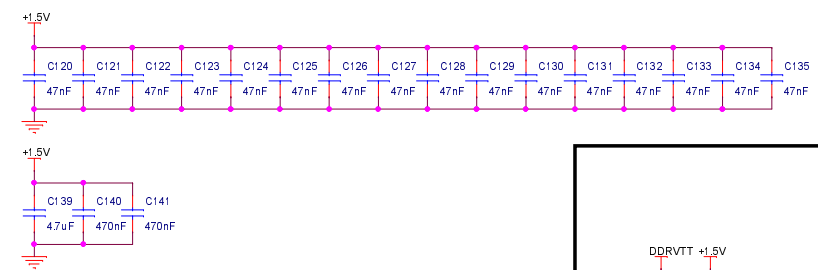
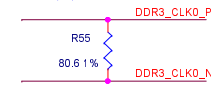
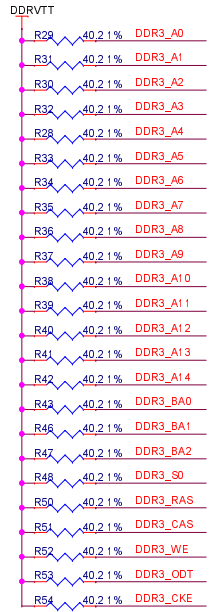
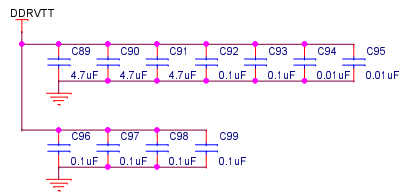
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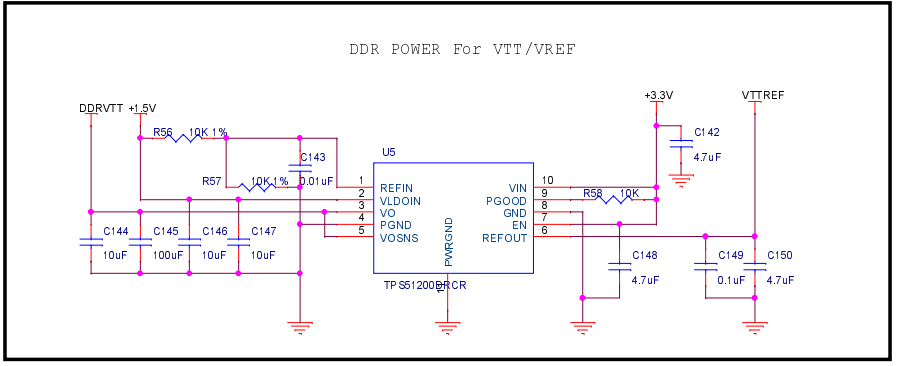
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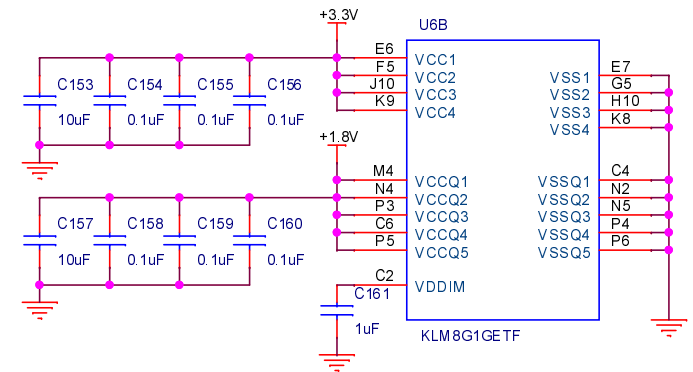
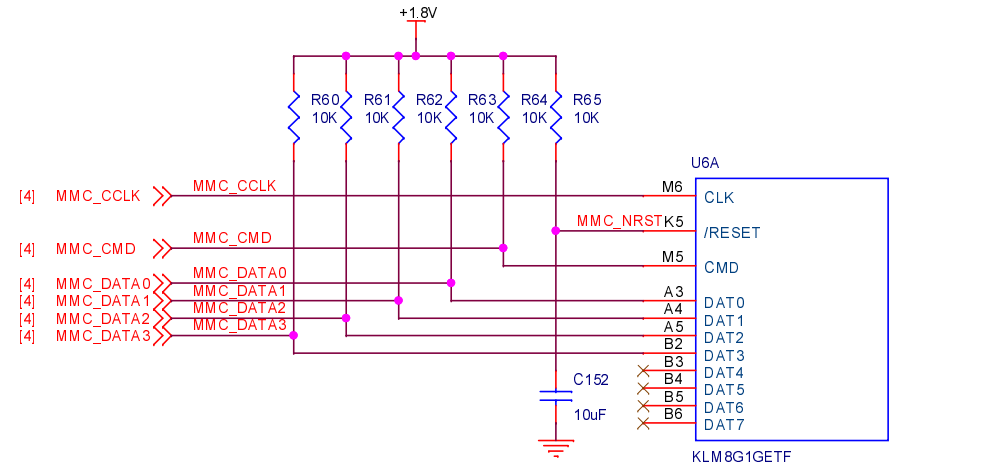
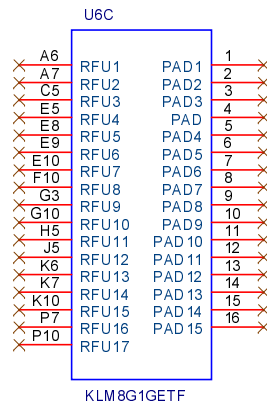
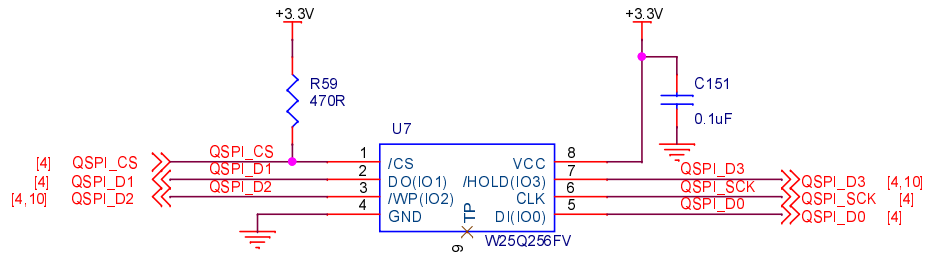
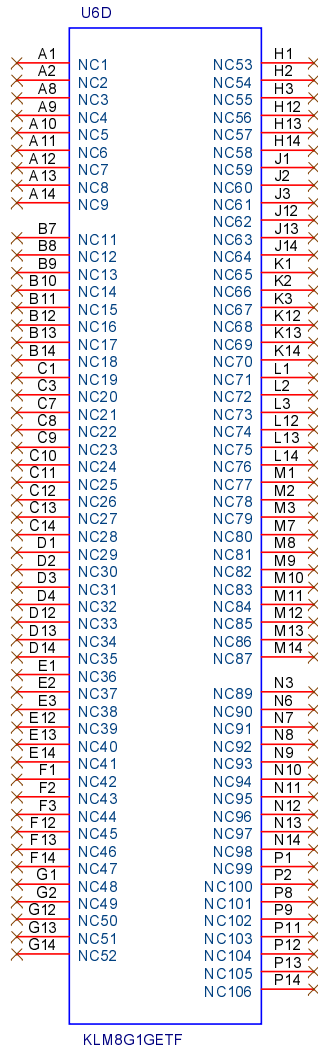
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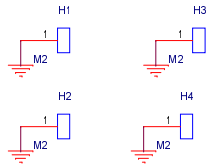
DDR POWER For VTT/VREF



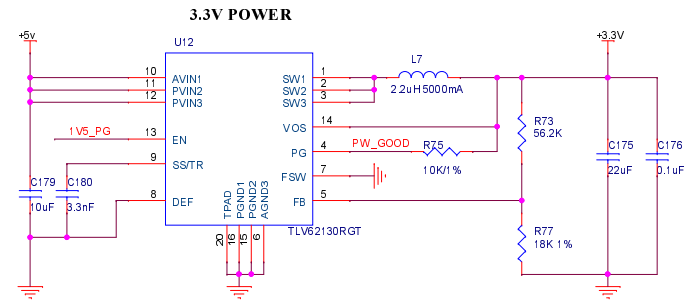
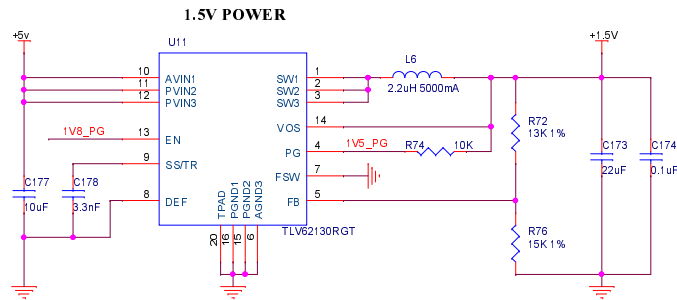
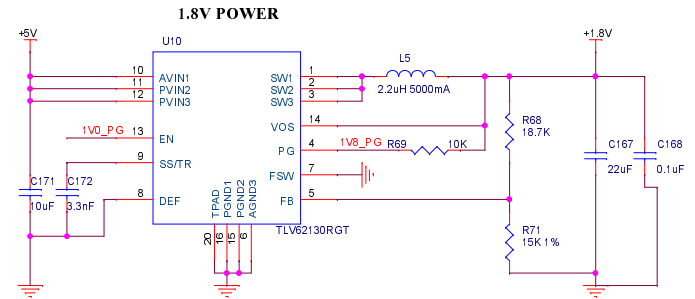
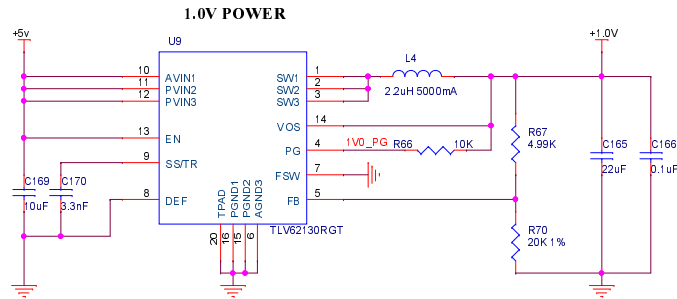
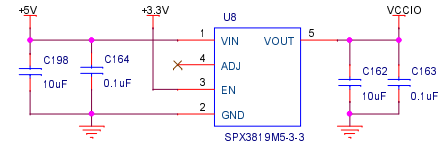
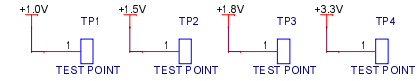
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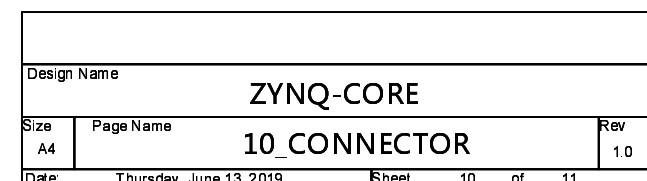
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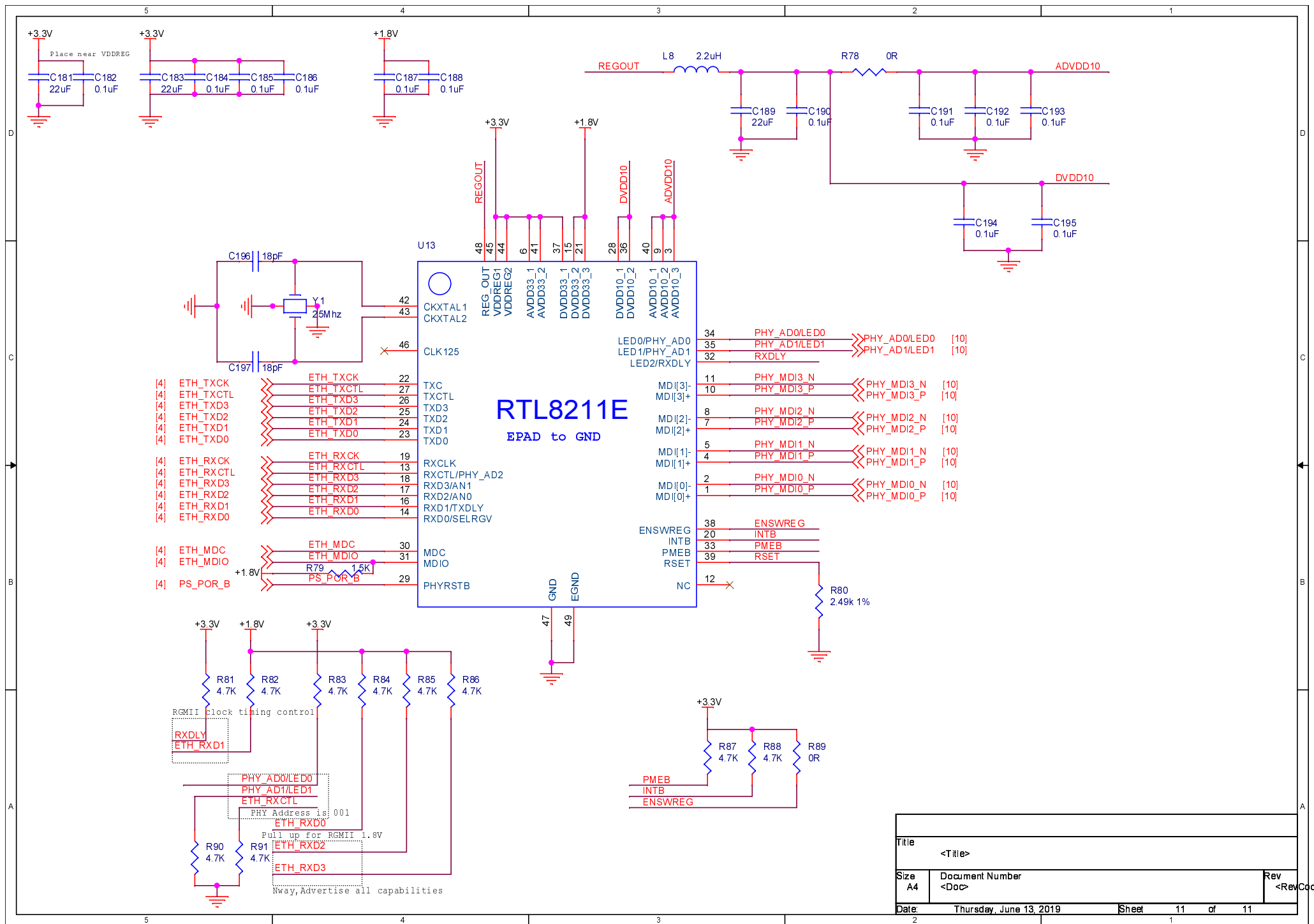


Power On Sequence:
1.0V -> 1.8V -> 1.5V -> 3.3V -> VCCIO



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A3	09_Power	1.0	
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