

EMIF0 SDRAM	<ul style="list-style-type: none"> • 0x481A_E000 • 4KB
GPIO0	<ul style="list-style-type: none"> • 0x4C00_0000 • 16MB EMIF0 Configuration Registers
UART0	<ul style="list-style-type: none"> • 0x44E0_8000 • 4KB
I2C0	0x44E0_9000 <ul style="list-style-type: none"> • 4KB
UART1	<ul style="list-style-type: none"> • 0x4802_2000 • 4KB
UART2	<ul style="list-style-type: none"> • 0x4802_2000 • 4KB
I2C1	<ul style="list-style-type: none"> • 0x4802_A000 • 4KB
GPIO1	0x44E0_B000 <ul style="list-style-type: none"> • 4KB
I2C2	<ul style="list-style-type: none"> • 0x4819_C000 • 4KB
UART3	<ul style="list-style-type: none"> • 0x481A_6000 • 4KB
UART4	<ul style="list-style-type: none"> • 0x481A_8000 • 4KB
UART5	<ul style="list-style-type: none"> • 0x481A_A000 • 4KB
GPIO2	<ul style="list-style-type: none"> • 0x4804_C000 • 4KB
GPIO3	<ul style="list-style-type: none"> • 0x481A_C000 • 4KB