List of Figures

1 Grids

Nam dui ligula, fringilla a, euismod sodales, sollicitudin vel, wisi. Morbi auctor lorem non justo. Nam lacus libero, pretium at, lobortis vitae, ultricies et, tellus. Donec aliquet, tortor sed accumsan bibendum, erat ligula aliquet magna, vitae ornare odio metus a mi. Morbi ac orci et nisl hendrerit mollis. Suspendisse ut massa. Cras nec ante. Pellentesque a nulla. Cum sociis natoque penatibus et magnis dis parturient montes, nascetur ridiculus mus. Aliquam tincidunt urna. Nulla ullamcorper vestibulum turpis. Pellentesque cursus luctus mauris.

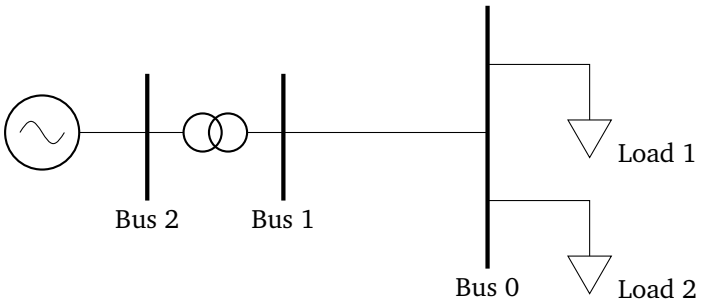

Figure 1.1: Single line network with two loads

Figure 1.2: SMIB model

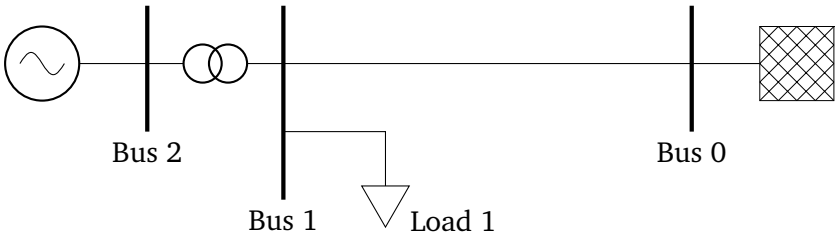

Figure 1.3: SMIB model with additional load

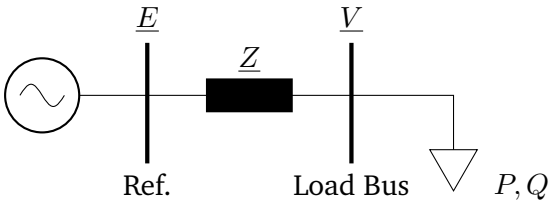

Figure 1.4: SMIB model with additional load

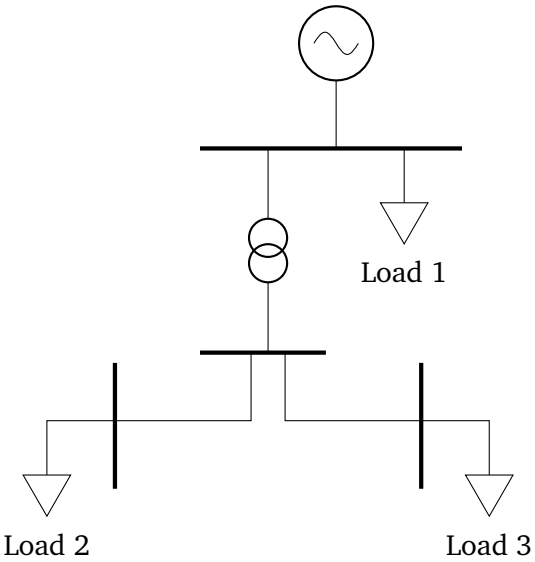

Figure 1.5: Random network with three loads on multiple voltage levels

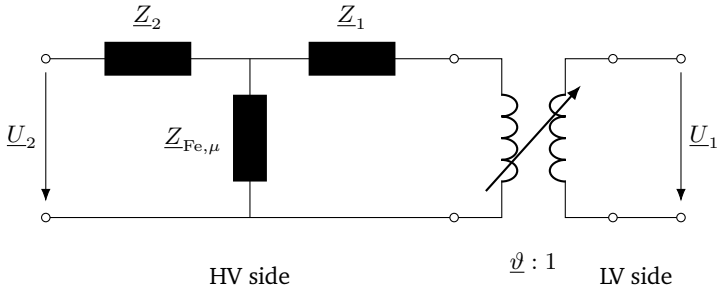

Figure 1.6: Complete transformer circuit

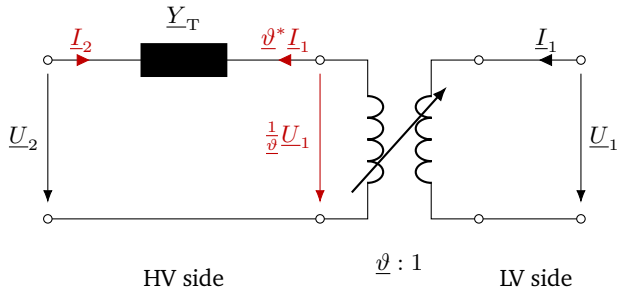

Figure 1.7: Reduced transformer circuit; based on Ilyas calculation

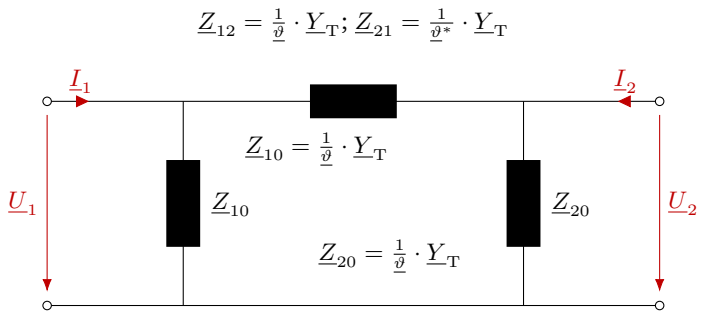

Figure 1.8: Transformer Pi circuit

2 Control Blocks

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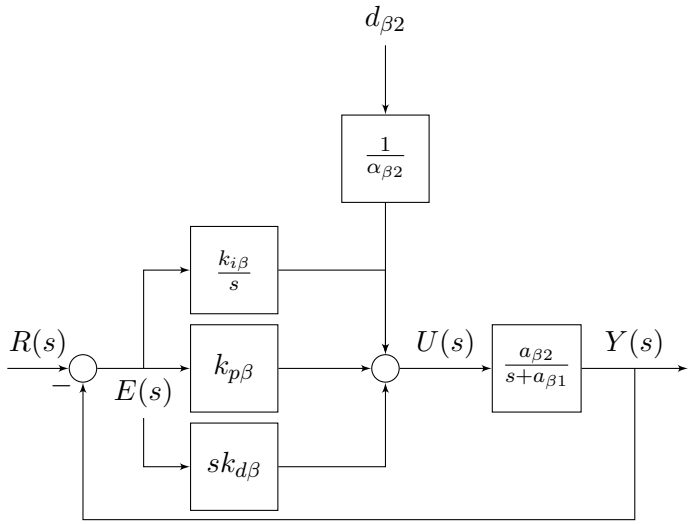

Figure 2.1: Example: Control block diagram

3 Others

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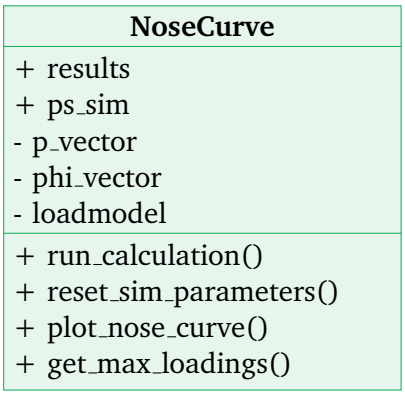

Figure 3.1: Class NoseCurve Diagram

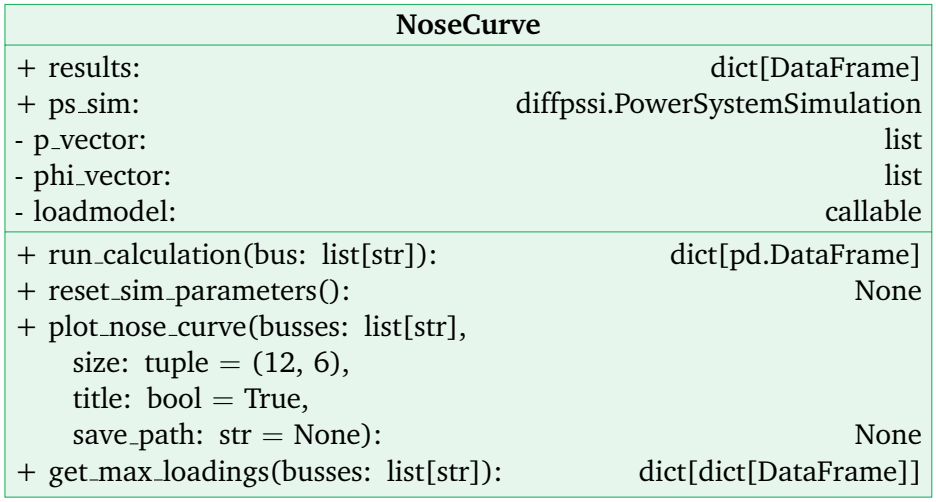

Figure 3.2: Class NoseCurve Diagram Complete

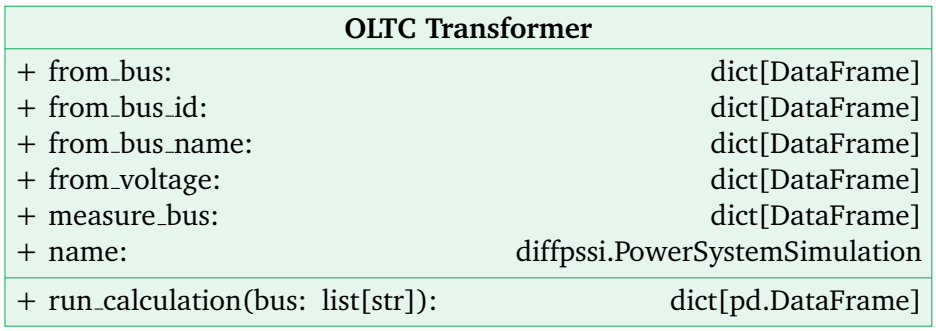

Figure 3.3: Class OLTC Transformer Diagram Complete

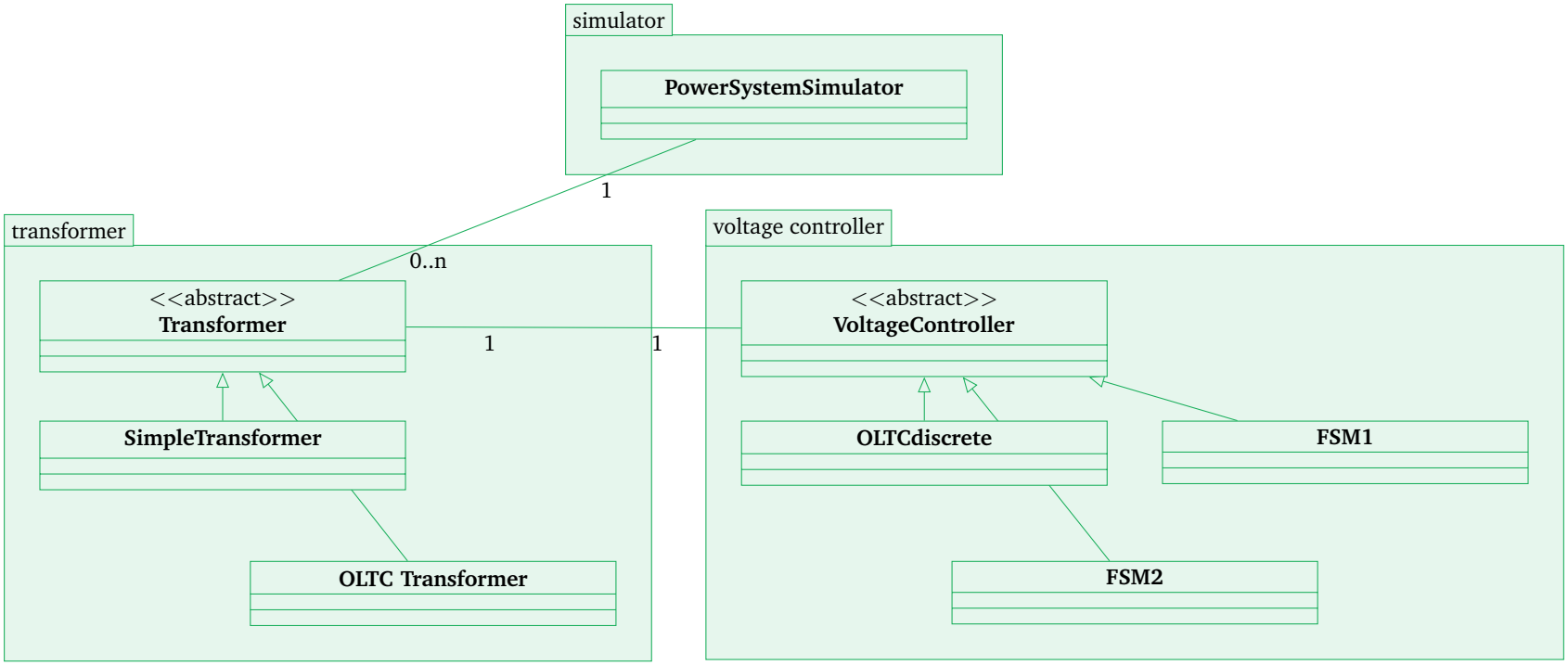

Figure 3.4: Software Structure idea