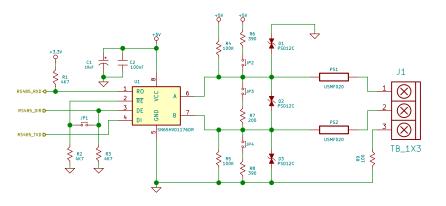


**RS485** 



JP2, JP3, JP4: cortocircuitar en caso que sea el último nodo de la red.

Los resistores de 290 y 200 (220 valor E12/E24) se denominas bias resistors y son los recomendados la especificación de Profibus.

R9 de acuerdo a Fig 27 de la AN: slla070d de TI.





Licencia: https://github.com/ciaa/Hardware/tree/master/Readme (LICENSE) Computadora Industrial Abierta Argentina Versión Educativa EDU-CIAA-NXP

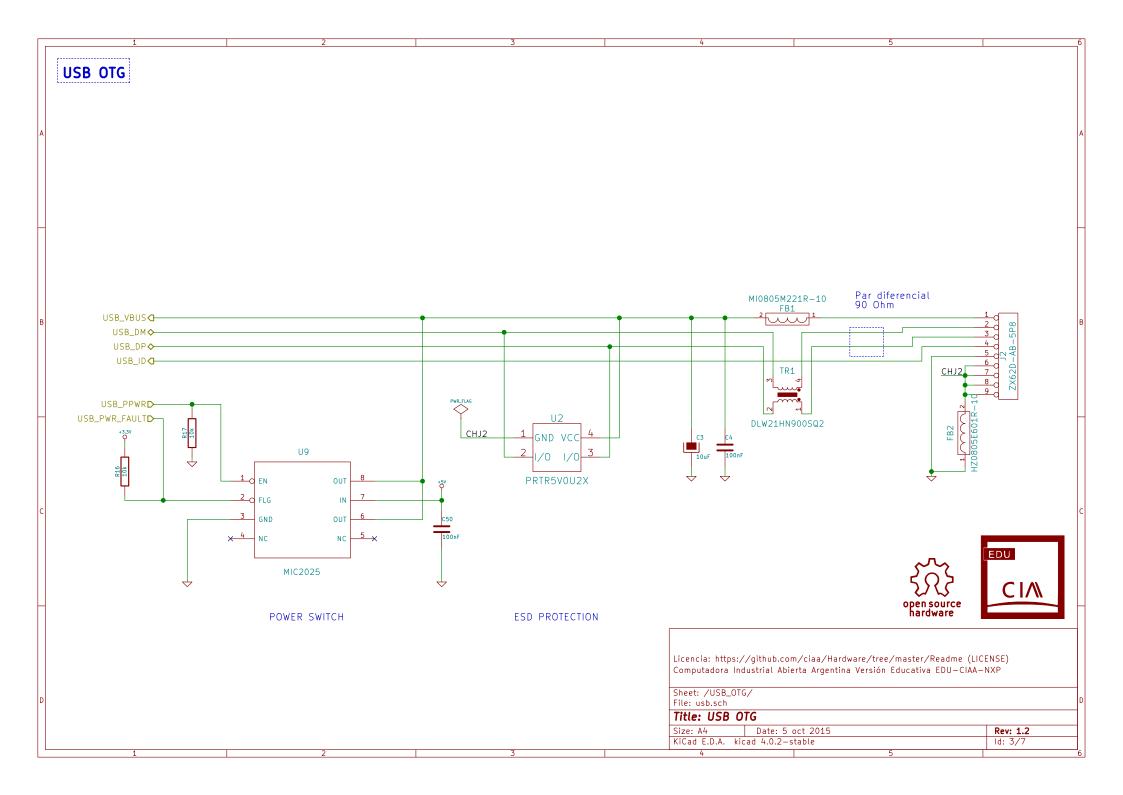
Sheet: /RS485/ File: rsS485\_can.sch

Title: RS485

 Size: A4
 Date: 5 oct 2015
 Rev: 1.2

 KiCad E.D.A. kicad 4.0.2-stable
 Id: 2/7

D



**GPIO** Conectores de expansión LVTTL. En formato de pines, 2.54mm de pitch. GPIO\_GND GPIO\_GND RESET 4 GPIO\_GND DENET\_RXD1 ISP **d** -DWAKEUP GPIO\_GND ANALOG\_GND -dENET\_TX\_EN ANALOG\_GND R23 1k2 ENET\_RXD0 -**□**ENET\_MDC ADCO\_3 ANALOG\_GND GPIO\_GND R22 1k2 DENET\_CRS\_DV ADC0\_24-ANALOG\_GND GPIO\_GND R21 1k2 → ENET\_MDIO ADCO\_1( ANALOG\_GND R20 470 ENET\_REF\_CLKD **⊸**ENET\_TXD0 0 8 0 10 0 12 0 14 0 16 0 18 0 20 DAC D ANALOG\_GND 8 0 10 0 12 0 14 0 16 0 20 0 22 0 24 0 26 0 28 0 30 0 32 0 34 0 36 0 36 0 40 GPIO\_GND **⊸**ENET\_TXD1 VDD\_AD-ANALOG\_GND 11 0 13 0 15 0 17 0 21 0 23 0 25 0 27 0 29 0 GPIO\_GND I2C\_SDA♦ DSPI\_MISO GPIO\_GND I2C\_SCLD-SPI\_MOSID-SPI\_SCK GPIO\_GND RS232\_RXD LCD\_END-**⊸**CD4 GPIO\_GND GPIO\_GND RS232\_TXDD GPIO\_GND **⊸**LCD\_RS 0 28 0 30 0 32 0 34 0 36 0 38 0 40 GPIO\_GND **a**LCD3 GPIO\_GND CAN\_RD -31 33 35 37 39 GPI00♦ -CD2 GPIO\_GND CAN\_TDD GPIO\_GND GPI02♦ -CD1 TEC\_COL1D-TEC COL2 GPI04♦ → GPI01 TEC\_F1 GPI06♦ ◆ GPI03 TEC\_FO -GPIO\_GND GPIO\_GND ◆GPI05 TEC\_F3 -♦GPI07 GPIO\_GND GPIO\_GND TEC\_F2 ◆GPI08 TEC\_COLOD-**FDU** open source hardware ANALOG\_GND GNDA Licencia: https://github.com/ciaa/Hardware/tree/master/Readme (LICENSE) Computadora Industrial Abierta Argentina Versión Educativa EDU-CIAA-NXP GPIO\_GND Sheet: /GPIO/ File: gpio.sch Title: GPIO Size: A4 Date: 5 oct 2015 Rev: 1.2 KiCad E.D.A. kicad 4.0.2-stable Id: 4/7

