

GNDA, GNDA, GND and GND and and and and T_C0L2

aND

GNDA GNDA GNDA,

Tira de 40 pines hembra de 0.1"(2,54 mm) de espaciado

aND aND

T_FIL1

36 38

35

31

T_C0L1 T_FILO T_FIL3 T_FILZ T_C0L0

39

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E 3

EC 1

LED 3

LED 2

E

...

GPI01

GPI03 GPI05 GPI07

34 36 38 40

33

GPIOO GPI02 GPI04 GPIO6

GND GND

SPI_MOSI LCD_EN

GND GND

and

aND

GND

GND GND 35

37

GND

37

Tira de 40 pines hembra de 0.1"(2,54 mm) de espaciado

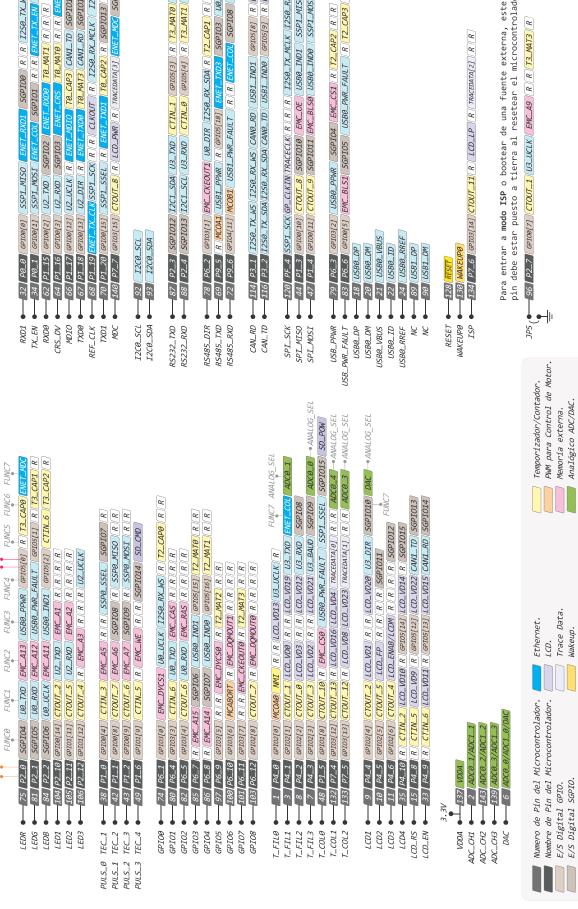


SCU SCU PORT PIN

EDU-CIAA-NXP Pines utilizados del NXP LPC4337 JBD144



GPIO GPIO
FUNÇ3 FUNC4 FUNC5 FUNC6 FUNC7



88 | P2_4 | SGP1013 | 12C1_SCL|| U3_RXO | CTIN_0 | GP105[4] | R | T3_MAT1 | USB0_PWR_FAULT • 47 P1_4 (GP100[11]) CTOUT_9 | SGP1011 (EMC_BLS0| USB0_IND0 | SSP1_MOS1 | R ||SD_VOL11 ENET_RXD1 | SGPIO0 | R | R | I2S0_TX_WS | I2S1_TX_WS EN 1251_TX_SDA WET_TX_CLK[|SSP1_SCK||R]||R]||CLKOUT||R]||I2S0_RX_MCLK||I2S1_TX_SCK|| •— 87 | P2_3 | SGP1012 | I2C1_SDA| U3_TXD | CTIN_1 | GP105[3] | R | [73_MAT0 | USB0_PPWR _114 P3_1 (1250_TX_MS)(1250_RX_MS)(CAN0_RD)(USB1_IND1 (GP105[8])(R)(LCD_VD15)(R) COL SGPIO8 UB_RXD . 83 | P6_6 | GPIOO[5] |EMC_BLS1| SGPIO5 | USB0_PWR_FAULT || R]<mark>(T2_CAP3]</mark> | R] | R TXD0 TO_MAT3 CAN1_RD SGPIO12 R XD3 SGPIO3 UB_TXD ENET_MDIO 76_CAP3 CAN1_TD SGPIO11 R TXD1 (T0_CAP2)(R) SGPI013)(R) RXDO TO MATI R R R CRS TO_MATO R R EN -79 | P6_3 | GP103[2] | USB0_PPWR | SGPIO4 | EMC_CS1 | R | <mark>T2_CAP2 |</mark> R | R •——134 P7_6 GPI03[14]]CTOUT_1] R] LCD_LP] R] TRACEDATA[2]] R] R COL SGPIO1 (R)(R)

pin debe estar puesto a tierra al resetear el microcontrolador. . 96 | P2_7 | GPIO0[7] | CTOUT_1 | U3_UCLK | EMC_A9 | R | R | (T3_MAT3 | R |

Jumpers de RS485

Función Reservada (no

disponible).

Entrada de interrupción externa a Interrupción No Enmascarable

Reset.

Serie (USART, I2C, SPI o USB).

(NMI).

JP1 - Abierto permite recibir un eco local de lo transmitido. Cortocircuitar para no recibir eco. JP2, JP3 y JP4 - Cortocircuitar en caso que sea el último nodo de la red. JP1 JP2 JP3 JP4